

TENTATIVE TOSHIBA TRANSISTOR SILICON NPN EPITAXIAL PLANAR TYPE

HN2C10FT

VHF~UHF BAND LOW NOISE AMPLIFIER APPLICATIONS

Unit in mm

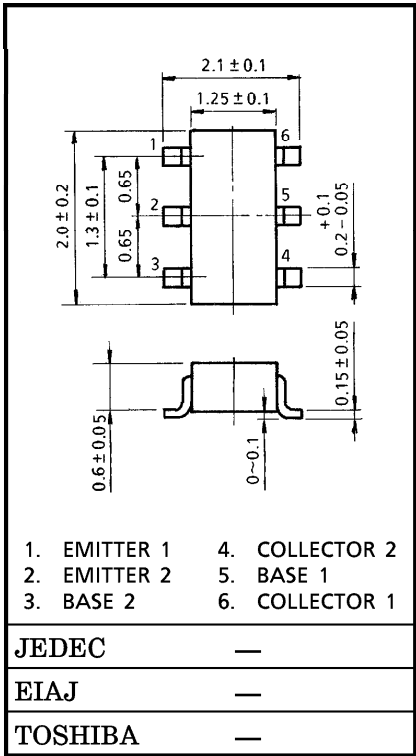
- TWO devices are built in to the super-thin and ultra super mini (6 pins) package : TU6

MOUNTED DEVICES

	Q1 / Q2
Three-pins (SSM) mold products are corresponded	2SC5086

MAXIMUM RATINGS (Ta = 25°C)

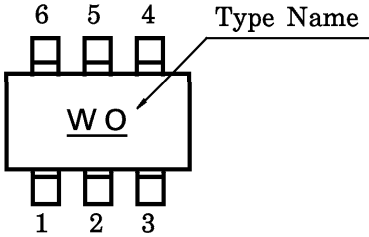
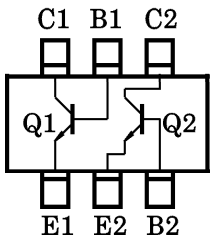
CHARACTERISTIC	SYMBOL	RATING	UNIT
Collector-Base Voltage	VCBO	20	V
Collector-Emitter Voltage	VCEO	12	V
Emitter-Base Voltage	VEBO	3	V
Collector Current	IC	80	mA
Base Current	IB	40	mA
Collector Power Dissipation	PC	200	mW
Junction Temperature	Tj	125	°C
Storage Temperature Range	Tstg	-55~125	°C



Weight : 0.008 g

PIN ASSIGNMENT (TOP VIEW)

MARKING



961001EAA1

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ELECTRICAL CHARACTERISTICS (Q1, Q2) (Ta = 25°C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Collector Cut-off Current	I_{CBO}	$V_{CB} = 10\text{ V}, I_E = 0$	—	—	1	μA
Emitter Cut-off Current	I_{EBO}	$V_{EB} = 1\text{ V}, I_C = 0$	—	—	1	μA
DC Current Gain	h_{FE}	$V_{CE} = 10\text{ V}, I_C = 20\text{ mA}$	80	—	240	—
Transition Frequency	f_T	$V_{CE} = 10\text{ V}, I_C = 20\text{ mA}$	5	7	—	GHz
Insertion Gain	$ S_{21e} ^2$ Q1	$V_{CE} = 10\text{ V}, I_C = 20\text{ mA},$ $f = 1\text{ GHz}$	8	11.5	—	dB
	$ S_{21e} ^2$ Q2		8.5	12	—	dB
Noise Figure	NF	$V_{CE} = 10\text{ V}, I_C = 5\text{ mA},$ $f = 1\text{ GHz}$	—	1.1	2	dB
Reverse Transfer Capacitance	C_{re} Q1	$V_{CB} = 10\text{ V}, I_E = 0,$ $f = 1\text{ MHz}$ (Note)	—	0.55	1.05	pF
	C_{re} Q2		—	0.6	1.1	

(Note) : C_{re} is measured by 3 terminal method capacitance bridge.