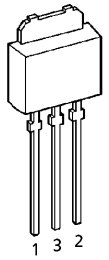
TOSHIBA BIPOLAR LINEAR INTEGRATED CIRCUIT SILICON MONOLITHIC

TA78DL05F, TA78DL06F, TA78DL08F, TA78DL09F TA78DL10F, TA78DL12F, TA78DL15F

5 V, 6 V, 8 V, 9 V, 10 V, 12 V, 15 V LOW DROPOUT VOLTAGE REGULATOR.

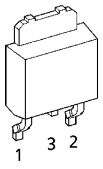
The TA78DLXXF series consists of positive fixed output voltage regulator IC capable of sourcing current up to 250 mA.
Due to the features of low dropout voltage and low standby current, these devices are useful for battery powered equipment.
This series includes current limiting, thermal shutdown, overvoltage protection, input fault protection and excessive transient protection circuits internally.

TA78DLXXF



P-HSIP3-2.30B

TA78DLXXF
(LBSTA1)
(TE16L)



1. INPUT
2. OUTPUT
3. COMMON (HEATSINK)

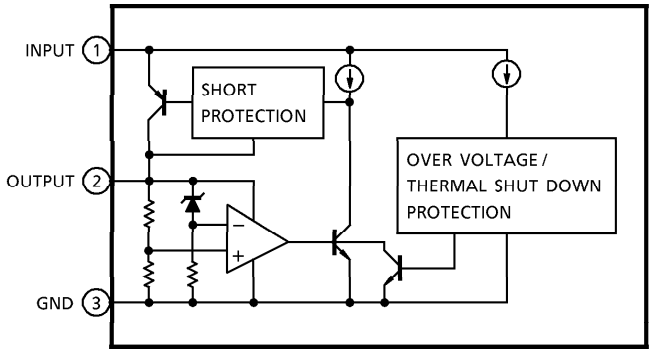
P-HSOP3-2.30A

Weight
P-HSIP3-2.30B : 0.36 g (Typ.)
P-HSOP3-2.30A : 0.36 g (Typ.)

FEATURES

- Low Standby Current of 500 μA Typical.
- Maximum Output Current Up to 250 mA.
- Low Dropout Voltage of Less than 0.6 V (@ I_{OUT} = 0.2 A).
- Multi-protection
  : Reverse Connection of Power Supply, 60 V Load Dump, Thermal Shut Down and Current Limiting.
- Packaged in POWER MOLD.

BLOCK DIAGRAM



980910EBA1

MAXIMUM RATINGS (Ta = 25°C)

CHARACTERISTIC		SYMBOL	RATING	UNIT
Operating Input Voltage		V_{IN}	29	V
Input Voltage of Surge		V_{IN}	60	V
Power Dissipation	(Ta = 25°C)	P_D	1	W
	(Tc = 25°C)		10	
Operating Temperature		T_{opr}	−40~85	°C
Storage Temperature		T_{stg}	−55~150	°C
Junction Temperature		T_j	150	°C
Thermal Resistance		$R_{th\ (j\text{-}c)}$	12.5	°C / W
		$R_{th\ (j\text{-}a)}$	125	
Storage Temperature Time		T_{sol}	260 (10 s)	°C

TA78DL05F

ELECTRICAL CHARACTERISTICS (Unless otherwise specified, V_{IN} = 14 V, I_{OUT} = 10 mA, T_j = 25°C)

CHARACTERISTIC	SYMBOL	TEST CIR-CUIT	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Output Voltage	V_{OUT}	—	5.35 V ≦ V_{IN} ≦ 26 V −40°C ≦ Ta ≦ 85°C	4.5	5	5.5	V
Line Regulation	Reg·line	—	9 V ≦ V_{IN} ≦ 16 V	—	2	10	mV
			6 V ≦ V_{IN} ≦ 26 V	—	4	30	
Load Regulation	Reg·load	—	10 mA ≦ I_{OUT} ≦ 200 mA	—	14	50	mV
Quiescent Current	I_B	—	I_{OUT} ≦ 10 mA, 6 V ≦ V_{IN} ≦ 26 V	—	0.5	1	mA
Dropout Voltage	V_D	—	I_{OUT} = 50 mA	—	0.15	0.3	V
			I_{OUT} = 200 mA	—	0.4	0.6	
Max. Operating Voltage	V_{IN}	—	—	29	33	—	V

TA78DL06F

ELECTRICAL CHARACTERISTICS (Unless otherwise specified, V_{IN} = 14 V, I_{OUT} = 10 mA, T_j = 25°C)

CHARACTERISTIC	SYMBOL	TEST CIR-CUIT	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Output Voltage	V_{OUT}	—	6.35 V ≦ V_{IN} ≦ 26 V −40°C ≦ Ta ≦ 85°C	5.4	6	6.6	V
Line Regulation	Reg·line	—	10 V ≦ V_{IN} ≦ 17 V	—	2	12	mV
			7 V ≦ V_{IN} ≦ 26 V	—	5	36	
Load Regulation	Reg·load	—	10 mA ≦ I_{OUT} ≦ 200 mA	—	17	60	mV
Quiescent Current	I_B	—	I_{OUT} ≦ 10 mA, 7 V ≦ V_{IN} ≦ 26 V	—	0.55	—	mA
Dropout Voltage	V_D	—	I_{OUT} = 50 mA	—	0.15	0.3	V
			I_{OUT} = 200 mA	—	0.4	0.6	
Max. Operating Voltage	V_{IN}	—	—	29	33	—	V

TA78DL08F

ELECTRICAL CHARACTERISTICS (Unless otherwise specified, V_{IN} = 16 V, I_{OUT} = 10 mA, T_j = 25°C)

CHARACTERISTIC	SYMBOL	TEST CIR-CUIT	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Output Voltage	V_{OUT}	—	8.35 V ≦ V_{IN} ≦ 26 V −40°C ≦ Ta ≦ 85°C	7.2	8	8.8	V
Line Regulation	Reg·line	—	12 V ≦ V_{IN} ≦ 19 V	—	3	16	mV
			9 V ≦ V_{IN} ≦ 26 V	—	6	45	
Load Regulation	Reg·load	—	10 mA ≦ I_{OUT} ≦ 200 mA	—	22	80	mV
Quiescent Current	I_B	—	I_{OUT} ≦ 10 mA, 9 V ≦ V_{IN} ≦ 26 V	—	0.6	—	mA
Dropout Voltage	V_D	—	I_{OUT} = 50 mA	—	0.15	0.3	V
			I_{OUT} = 200 mA	—	0.4	0.6	
Max. Operating Voltage	V_{IN}	—	—	29	33	—	V

TA78DL09F

ELECTRICAL CHARACTERISTICS (Unless otherwise specified, V_{IN} = 16 V, I_{OUT} = 10 mA, T_j = 25°C)

CHARACTERISTIC	SYMBOL	TEST CIR-CUIT	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Output Voltage	V_{OUT}	—	9.35 V ≦ V_{IN} ≦ 26 V −40°C ≦ Ta ≦ 85°C	8.1	9	9.9	V
Line Regulation	Reg·line	—	13 V ≦ V_{IN} ≦ 20 V	—	3	18	mV
			10 V ≦ V_{IN} ≦ 26 V	—	7	50	
Load Regulation	Reg·load	—	10 mA ≦ I_{OUT} ≦ 200 mA	—	25	90	mV
Quiescent Current	I_B	—	I_{OUT} ≦ 10 mA, 10 V ≦ V_{IN} ≦ 26 V	—	0.65	—	mA
Dropout Voltage	V_D	—	I_{OUT} = 50 mA	—	0.15	0.3	V
			I_{OUT} = 200 mA	—	0.4	0.6	
Max. Operating Voltage	V_{IN}	—	—	29	33	—	V

TA78DL10F

ELECTRICAL CHARACTERISTICS (Unless otherwise specified, V_{IN} = 16 V, I_{OUT} = 10 mA, T_j = 25°C)

CHARACTERISTIC	SYMBOL	TEST CIR-CUIT	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Output Voltage	V_{OUT}	—	10.35 V ≦ V_{IN} ≦ 26 V −40°C ≦ Ta ≦ 85°C	9	10	11	V
Line Regulation	Reg·line	—	14 V ≦ V_{IN} ≦ 21 V	—	4	20	mV
			11 V ≦ V_{IN} ≦ 26 V	—	8	60	
Load Regulation	Reg·load	—	10 mA ≦ I_{OUT} ≦ 200 mA	—	28	100	mV
Quiescent Current	I_B	—	I_{OUT} ≦ 10 mA, 11 V ≦ V_{IN} ≦ 26 V	—	0.7	—	mA
Dropout Voltage	V_D	—	I_{OUT} = 50 mA	—	0.15	0.3	V
			I_{OUT} = 200 mA	—	0.4	0.6	
Max. Operating Voltage	V_{IN}	—	—	29	33	—	V

TA78DL12F

ELECTRICAL CHARACTERISTICS (Unless otherwise specified, V_{IN} = 18 V, I_{OUT} = 10 mA, T_j = 25°C)

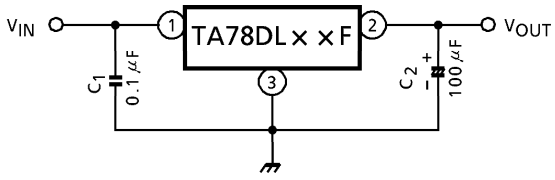
CHARACTERISTIC	SYMBOL	TEST CIR-CUIT	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Output Voltage	V_{OUT}	—	12.35 V ≦ V_{IN} ≦ 26 V −40°C ≦ Ta ≦ 85°C	10.8	12	13.2	V
Line Regulation	Reg·line	—	16 V ≦ V_{IN} ≦ 23 V	—	5	24	mV
			13 V ≦ V_{IN} ≦ 26 V	—	10	70	
Load Regulation	Reg·load	—	10 mA ≦ I_{OUT} ≦ 200 mA	—	33	120	mV
Quiescent Current	I_B	—	I_{OUT} ≦ 10 mA, 13 V ≦ V_{IN} ≦ 26 V	—	0.8	—	mA
Dropout Voltage	V_D	—	I_{OUT} = 50 mA	—	0.15	0.3	V
			I_{OUT} = 200 mA	—	0.4	0.6	
Max. Operating Voltage	V_{IN}	—	—	29	33	—	V

TA78DL15F

ELECTRICAL CHARACTERISTICS (Unless otherwise specified, V_{IN} = 20 V, I_{OUT} = 10 mA, T_j = 25°C)

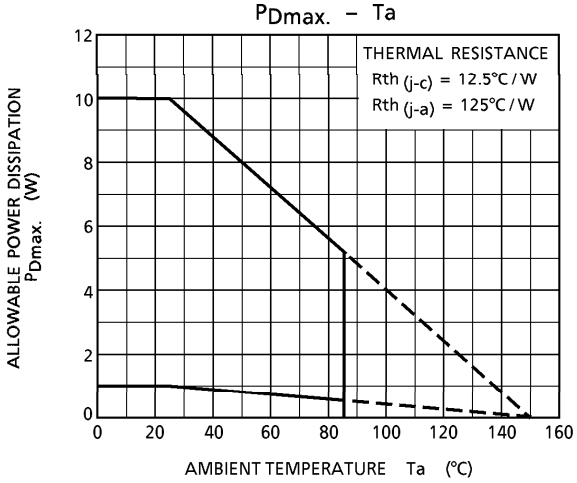
CHARACTERISTIC	SYMBOL	TEST CIR-CUIT	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Output Voltage	V_{OUT}	—	15.35 V ≦ V_{IN} ≦ 26 V −40°C ≦ Ta ≦ 85°C	13.5	15	16.5	V
Line Regulation	Reg·line	—	19 V ≦ V_{IN} ≦ 26 V	—	6	30	mV
			16 V ≦ V_{IN} ≦ 26 V	—	12	80	
Load Regulation	Reg·load	—	10 mA ≦ I_{OUT} ≦ 200 mA	—	40	150	mV
Quiescent Current	I_B	—	I_{OUT} ≦ 10 mA, 16 V ≦ V_{IN} ≦ 26 V	—	0.9	—	mA
Dropout Voltage	V_D	—	I_{OUT} = 50 mA	—	0.15	0.3	V
			I_{OUT} = 200 mA	—	0.4	0.6	
Max. Operating Voltage	V_{IN}	—	—	29	33	—	V

APPLICATION CIRCUIT

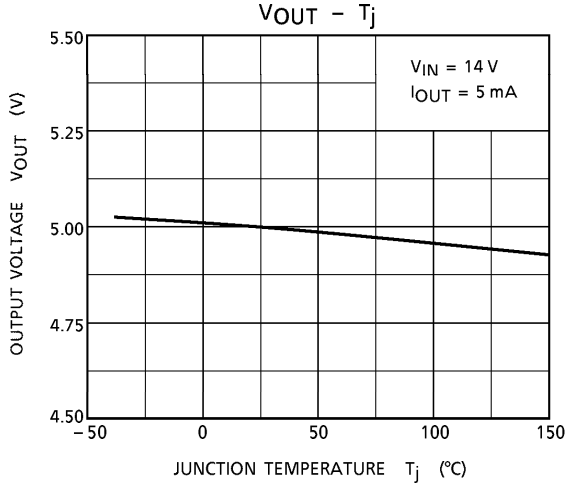


Capacitor C_2 must be guaranteed to operate of the temperature range that the regulator should be operated correctly.
100 μF is a suitable value to suppress the oscillation phenomenon at the output terminal.

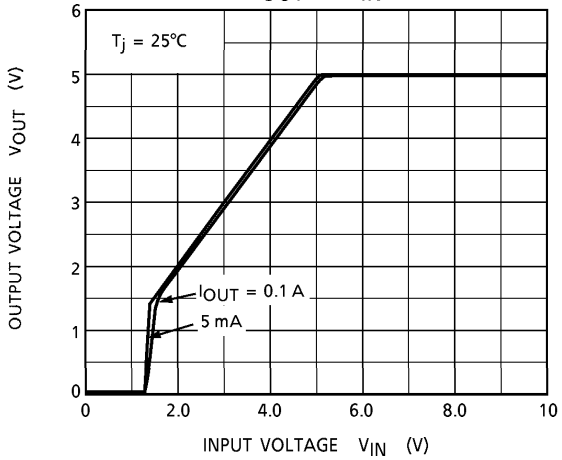
$P_{Dmax.}$ – Ta



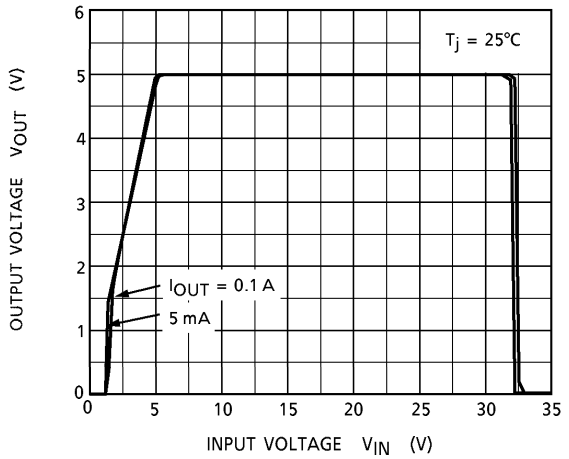
V_{OUT} – T_j



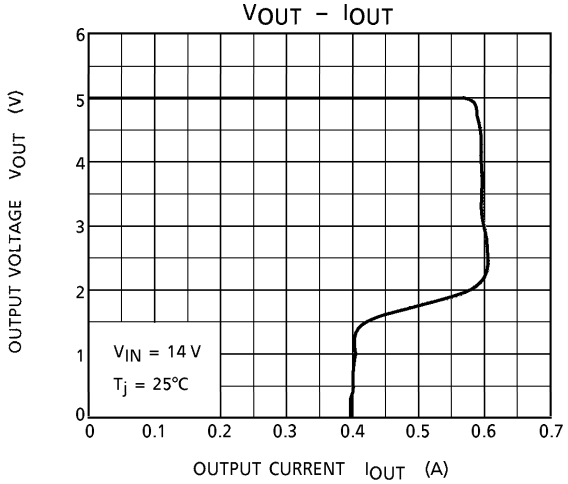
V_{OUT} – V_{IN}



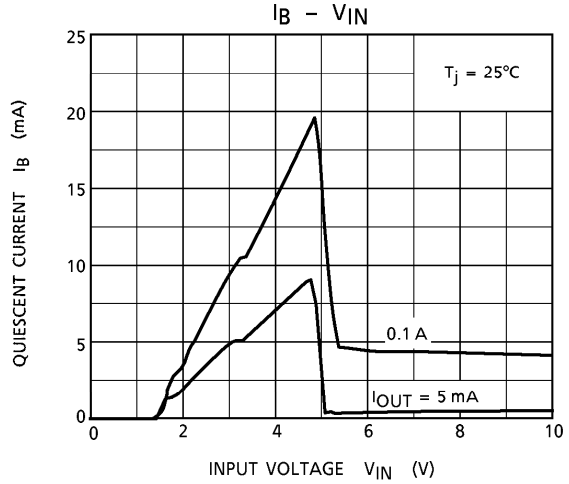
V_{OUT} – V_{IN}



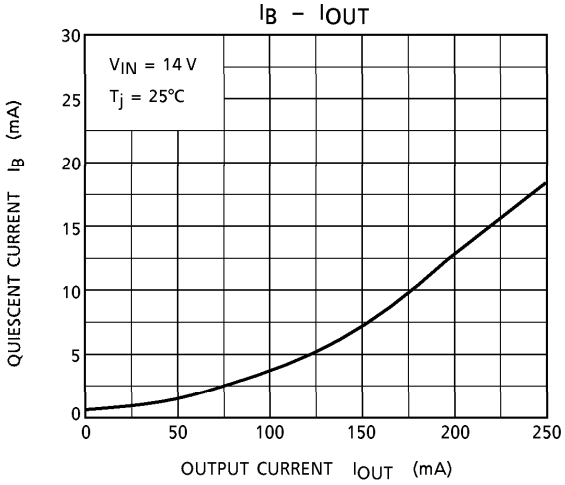
V_{OUT} – I_{OUT}



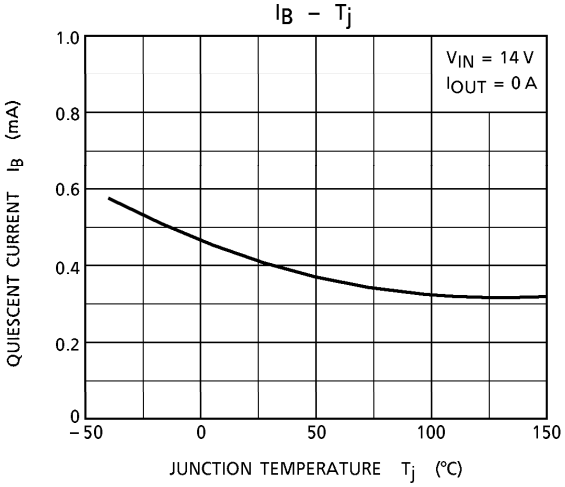
I_B – V_{IN}



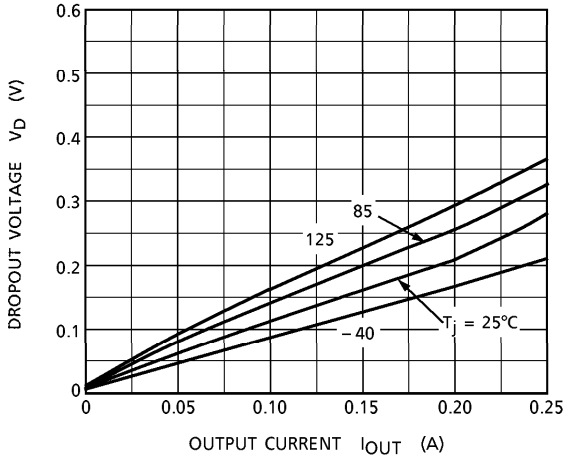
I_B – I_{OUT}



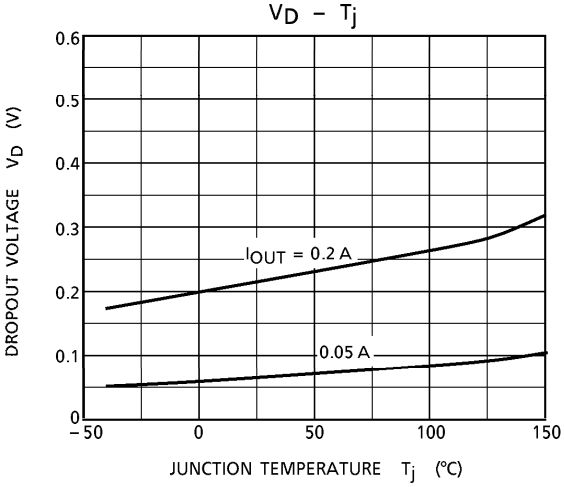
I_B – T_j



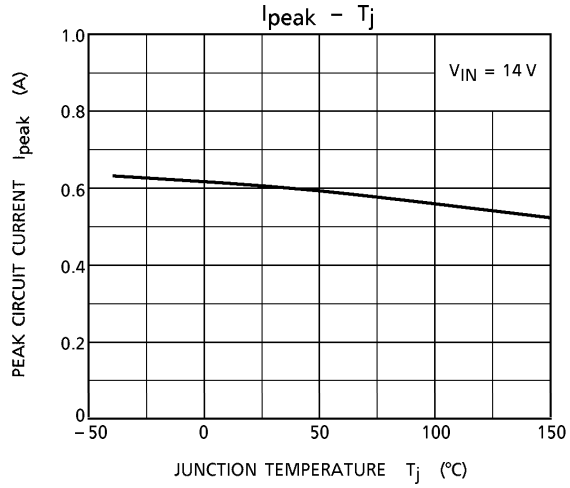
V_D – I_{OUT}



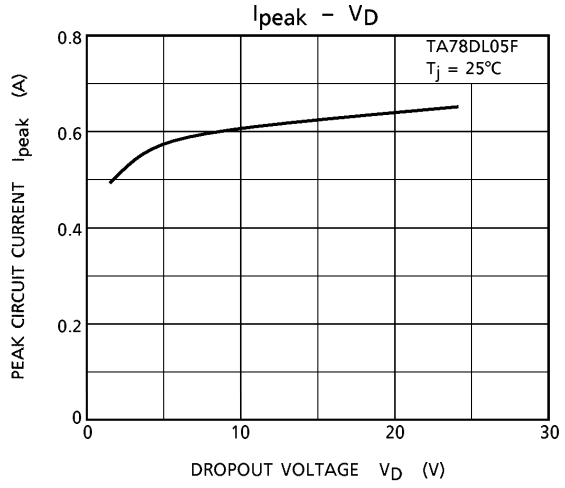
V_D – T_j

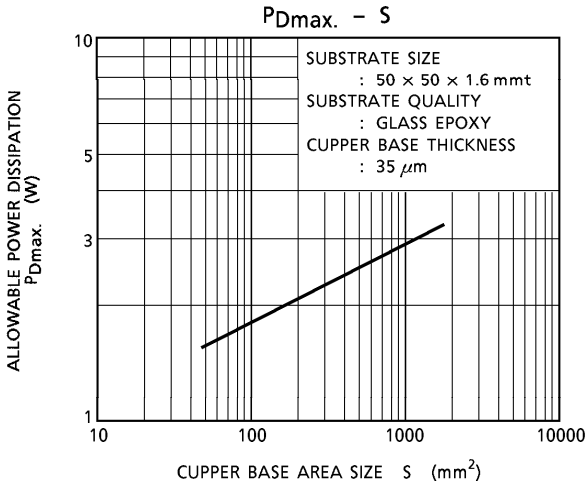


I_{peak} – T_j



I_{peak} – V_D

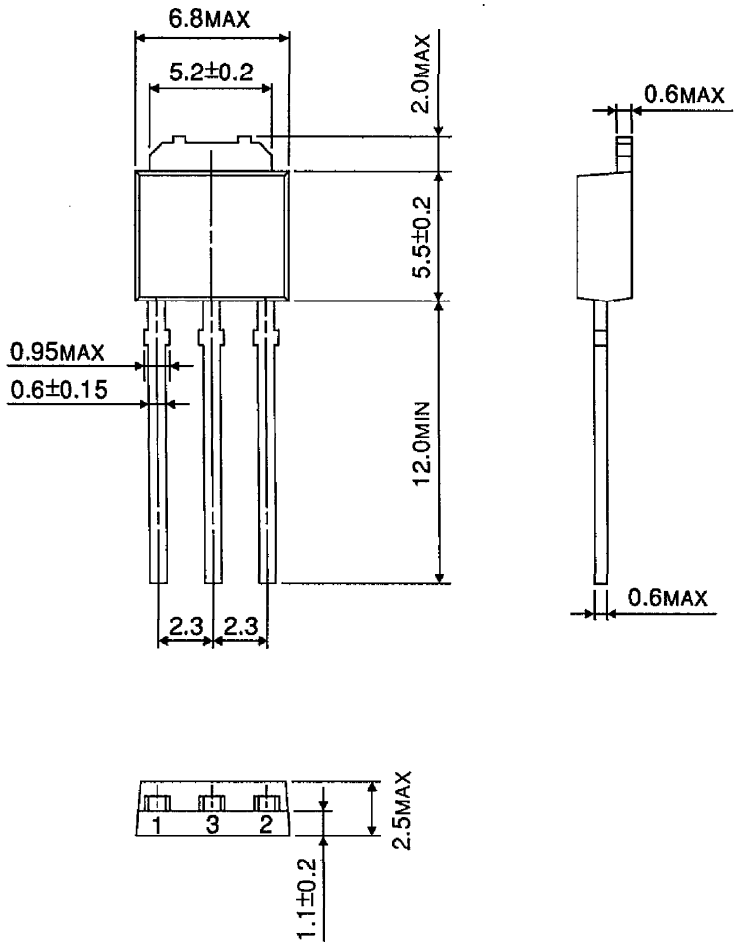



$P_{Dmax.}$ – S
ALLOWABLE POWER DISSIPATION $P_{Dmax.}$ (W)
10
5
3
1
SUBSTRATE SIZE
: 50 × 50 × 1.6 mmt
SUBSTRATE QUALITY
: GLASS EPOXY
CUPPER BASE THICKNESS
: 35 μm
10
100
1000
10000
CUPPER BASE AREA SIZE S (mm²)

PACKAGE DIMENSIONS

P-HSIP3-2.30B

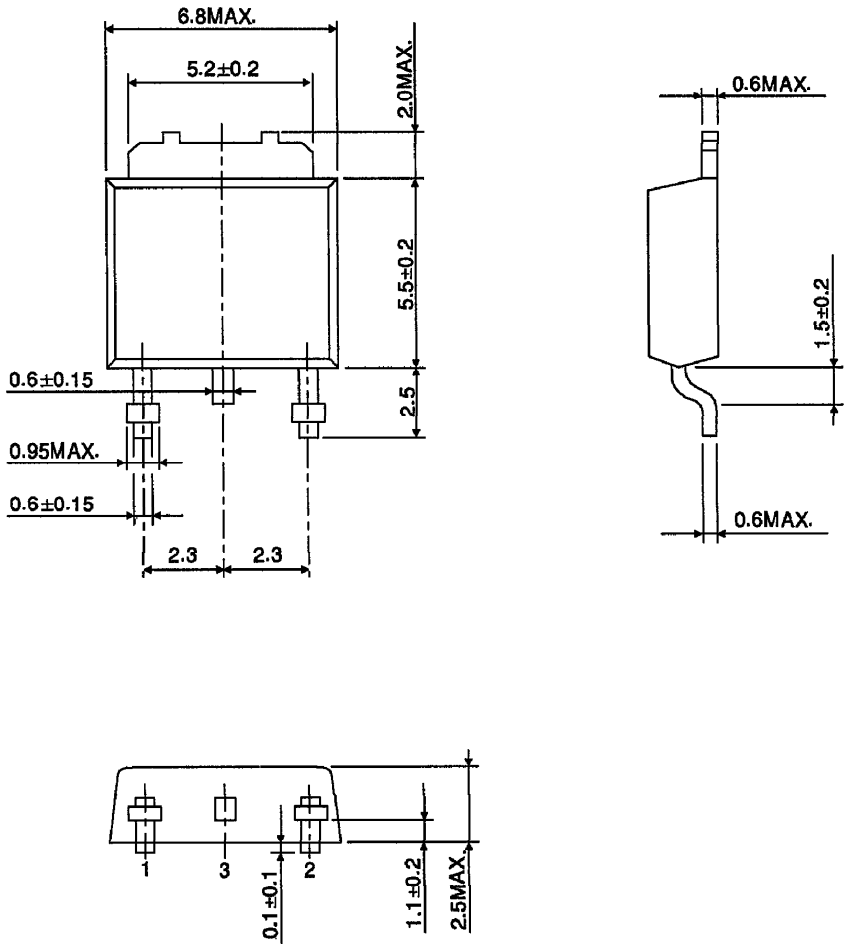
Unit : mm



Weight : 0.36 g (Typ.)

PACKAGE DIMENSIONS

P-HSOP3-2.30A Unit : mm



Weight : 0.36 g (Typ.)