Unit in mm

TOSHIBA BI-DIRECTIONAL TRIODE THYRISTOR SILICON PLANAR TYPE

S6903G, S6903J

AC POWER CONTROL APPLICATIONS

High Rush Current Capability Optimal for controlling actuators where high rush current may

: $I_{TRM} = 120A$ (n=100k cycle, $T_{c} = 45$ °C)

R.M.S On-State Current $: I_{T(RMS)} = 20A$

Repetitive Peak Off-State Voltage: VDRM=400V, 600V

MAXIMUM RATINGS

CHARACTERIS	SYMBOL	RATING	UNIT		
Repetitive Peak	S6903G	VDDM	400	V	
Off-State Voltage	S6903J	$v_{ m DRM}$	600	V	
R.M.S On-State Current (Full Sine Waveform Tc=100°C)		IT (RMS)	20	A	
Peak One Cycle Surge C	I _{TSM}	180 (50Hz)	A		
Current (Non-Repetitive)		200 (60Hz)			
Repetitive Surge On-State	I _{TRM} 120		A		
I ² t Limit Value	${f I}^2{f t}$	167	${\sf A}^2{\sf s}$		
Critical Rate of Rise of Current	di / dt	50	A/μs		
Peak Gate Power Dissipa	P_{GM}	5	W		
Average Gate Power Dis	PG (AV)	0.5	W		
Peak Gate Voltage	v_{GM}	10	V		
Peak Gate Current	I _{GM} 2		A		
Junction Temperature	T _j -40~125		$^{\circ}\mathrm{C}$		
Storage Temperature Ra	T _{stg} -40~125		$^{\circ}\mathrm{C}$		

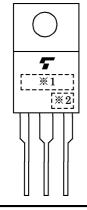
0.5 2.54 ± 0.25 2.54 ± 0.25 2. T2 (HEAT SINK) **JEDEC** TO-220AB **EIAJ**

13-10G1A

Weight: 2.0g

TOSHIBA

MARKING



NUMBER	SYMBOL		MARK
※1	TYPE	S6903G S6903J	S6903G S6903J
*2		nth (Starting from) Alphabet A ar (Last Decimal Digit) of the Current Year)	Example 8A: January 1998 8B: February 1998 8L: December 1998

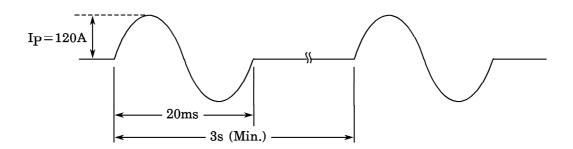
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CHARACTERISTIC		SYMBOL	TEST CONDITION		MIN.	TYP.	MAX.	UNIT
Repetitive Peak Off-State Current		$I_{ m DRM}$	V _{DRM} =Rated		_	_	20	μ A
Gate Trigger Voltage	Ι	$ m V_{GT}$	$V_D = 12V$ $R_L = 20\Omega$	T2 (+), Gate (+)	_	_	1.5	- V
	Ι			T2 (+), Gate (-)	-		1.5	
	\blacksquare			T2 (-), Gate (-)			1.5	
	IV			T2 (-), Gate (+)	_	_	_	
Gate Trigger Current	Ι	$I_{ m GT}$	$V_{\mathbf{D}} = 12V$ $R_{\mathbf{L}} = 20\Omega$	T2 (+), Gate (+)	_	_	30	mA
	Ι			T2 (+), Gate (-)	_	_	30	
	\blacksquare			T2 (-), Gate (-)	_	_	30	
	IV			T2 (-), Gate (+)	_	_	_	
Peak On-State Voltage		$ m V_{TM}$	I _{TM} =30A		_	_	1.6	V
Gate Non-Trigger Voltage		v_{GD}	V _D =Rated, Tc=125°C		0.2	_	_	V
Holding Current		$I_{\mathbf{H}}$	$V_D=12V, I_{TM}=2A$		_	_	50	mA
Thermal Resistance		R _{th (j-c)}	Junction to Case, AC		_	_	1.0	°C/W
Critical Rate of Rise of Off- State Voltage at Commutation		(dv / dt) c	$V_{ m DRM} = 400 m V, \ T_j = 125 m ^{\circ} C \ (di \ / \ dt)_{c} = -8.7 m A \ / \ ms$		10	_	_	V / μs

(Note 1) Repetitive Surge On-State Current



 $I_{\mbox{\footnotesize P}}\!=\!120\mbox{A}$ (f=50Hz) at Tc=45°C

Max. Repetitive Number of cycle n=100k cycle (Repetitive cycle T=3s Min.)