

TOSHIBA FIELD EFFECT TRANSISTOR SILICON N CHANNEL MOS TYPE (L^2 - π -MOSV)

2SK2789

HIGH SPEED, HIGH CURRENT SWITCHING APPLICATIONS

CHOPPER REGULATOR, DC-DC CONVERTER AND MOTOR DRIVE APPLICATIONS

INDUSTRIAL APPLICATIONS

Unit in mm

- 4 V Gate Drive
- Low Drain-Source ON Resistance : $R_{DS(ON)} = 66 \text{ m}\Omega$ (Typ.)
- High Forward Transfer Admittance : $|Y_{fs}| = 16 \text{ S}$ (Typ.)
- Low Leakage Current : $I_{DSS} = 100 \mu\text{A}$ (Max.) ($V_{DS} = 100 \text{ V}$)
- Enhancement-Mode : $V_{th} = 0.8 \sim 2.0 \text{ V}$
($V_{DS} = 10 \text{ V}$, $I_D = 1 \text{ mA}$)

MAXIMUM RATINGS ($T_a = 25^\circ\text{C}$)

CHARACTERISTIC		SYMBOL	RATING	UNIT
Drain-Source Voltage		V_{DSS}	100	V
Drain-Gate Voltage ($R_{GS} = 20 \text{ k}\Omega$)		V_{DGR}	100	V
Gate-Source Voltage		V_{GSS}	± 20	V
Drain Current	DC	I_D	27	A
	Pulse	I_{DP}	108	A
Drain Power Dissipation ($T_c = 25^\circ\text{C}$)		P_D	60	W
Single Pulse Avalanche Energy**		E_{AS}	193	mJ
Avalanche Current		I_{AR}	27	A
Repetitive Avalanche Energy*		E_{AR}	6	mJ
Channel Temperature		T_{ch}	150	$^\circ\text{C}$
Storage Temperature Range		T_{stg}	$-55 \sim 150$	$^\circ\text{C}$

THERMAL CHARACTERISTICS

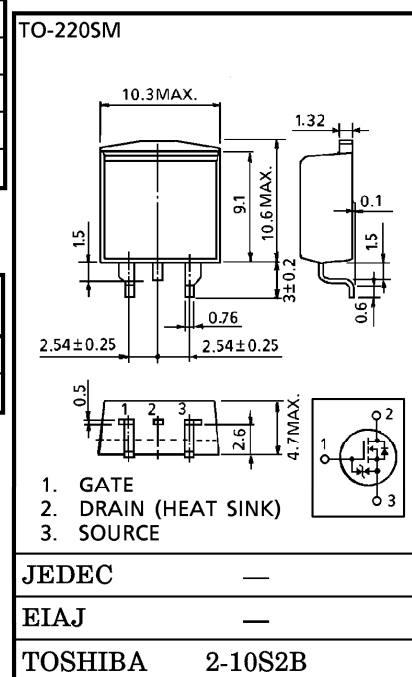
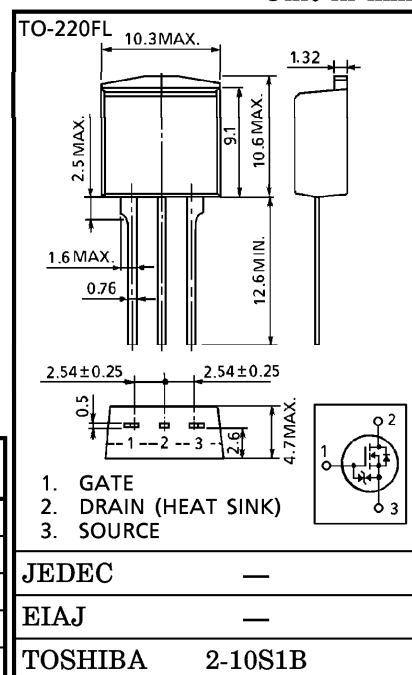
CHARACTERISTIC	SYMBOL	MAX.	UNIT
Thermal Resistance, Channel to Case	$R_{th(ch-c)}$	2.08	$^\circ\text{C/W}$
Thermal Resistance, Channel to Ambient	$R_{th(ch-a)}$	83.3	$^\circ\text{C/W}$

Note ;

* Repetitive rating ; Pulse Width Limited by Max. junction temperature.

** $V_{DD} = 25 \text{ V}$, $T_{ch} = 25^\circ\text{C}$ (initial), $L = 428 \mu\text{H}$
 $I_{AR} = 27 \text{ A}$, $R_G = 25 \Omega$

This transistor is an electrostatic sensitive device.
Please handle with caution.

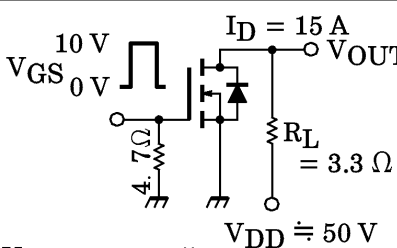


Weight : 1.5 g

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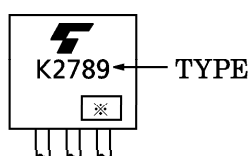
ELECTRICAL CHARACTERISTICS (Ta = 25°C)

CHARACTERISTIC		SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Gate Leakage Current		I_{GSS}	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0 \text{ V}$	—	—	± 10	μA
Drain Cut-off Current		I_{DSS}	$V_{DS} = 100 \text{ V}, V_{GS} = 0 \text{ V}$	—	—	100	μA
Drain-Source Breakdown Voltage		$V_{(BR) DSS}$	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$	100	—	—	V
Gate Threshold Voltage		V_{th}	$V_{DS} = 10 \text{ V}, I_D = 1 \text{ mA}$	0.8	—	2.0	V
Drain-Source ON Resistance		$R_{DS(ON)}$	$V_{GS} = 4 \text{ V}, I_D = 15 \text{ A}$	—	0.09	0.13	Ω
			$V_{GS} = 10 \text{ V}, I_D = 15 \text{ A}$	—	0.066	0.085	
Forward Transfer Admittance		$ Y_{fs} $	$V_{DS} = 10 \text{ V}, I_D = 15 \text{ A}$	8	16	—	S
Input Capacitance		C_{iss}	$V_{DS} = 10 \text{ V}, V_{GS} = 0 \text{ V},$ $f = 1 \text{ MHz}$	—	1100	—	pF
Reverse Transfer Capacitance		C_{rss}		—	180	—	
Output Capacitance		C_{oss}		—	400	—	
Switching Time	Rise Time	t_r		—	20	—	ns
	Turn-on Time	t_{on}		—	30	—	
	Fall Time	t_f		—	50	—	
	Turn-off Time	t_{off}		—	140	—	
Total Gate Charge (Gate-Source Plus Gate-Drain)		Q_g	$V_{DD} \approx 80 \text{ V}, V_{GS} = 10 \text{ V},$ $I_D = 27 \text{ V}$	—	50	—	nC
Gate-Source Charge		Q_{gs}		—	34	—	
Gate-Drain ("Miller") Charge		Q_{gd}		—	16	—	

SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Continuous Drain Reverse Current	I_{DR}	—	—	—	27	A
Pulse Drain Reverse Current	I_{DRP}	—	—	—	108	A
Diode Forward Voltage	V_{DSF}	$I_{DR} = 27 \text{ A}, V_{GS} = 0 \text{ V}$	—	—	—1.7	V
Reverse Recovery Time	t_{rr}	$I_{DR} = 27 \text{ A}, V_{GS} = 0 \text{ V}$	—	155	—	ns
Reverse Recovery Charge	Q_{rr}	$dI_{DR} / dt = 50 \text{ A} / \mu\text{s}$	—	0.31	—	μC

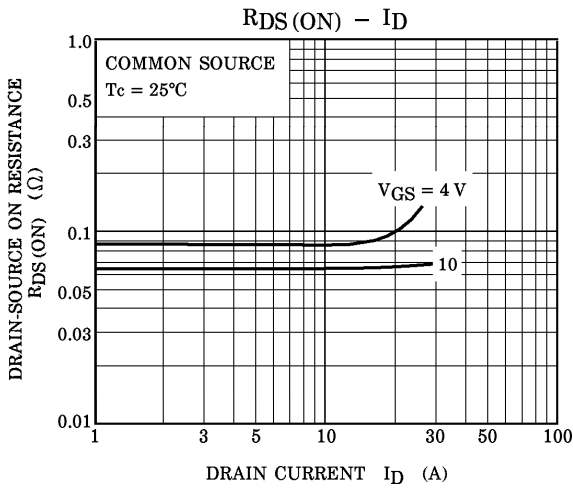
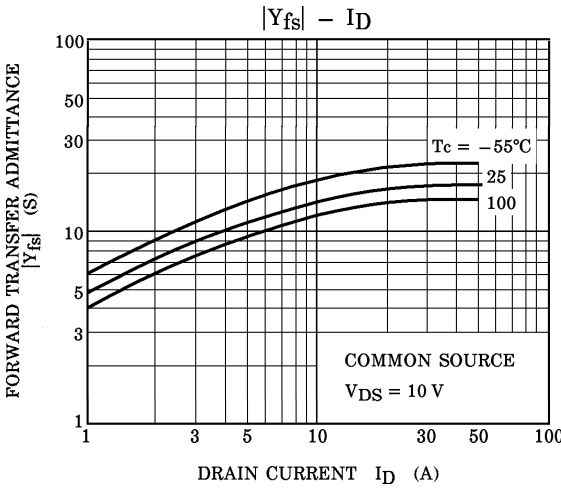
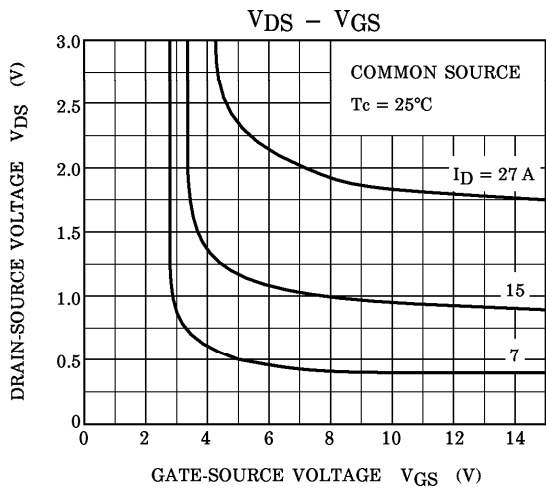
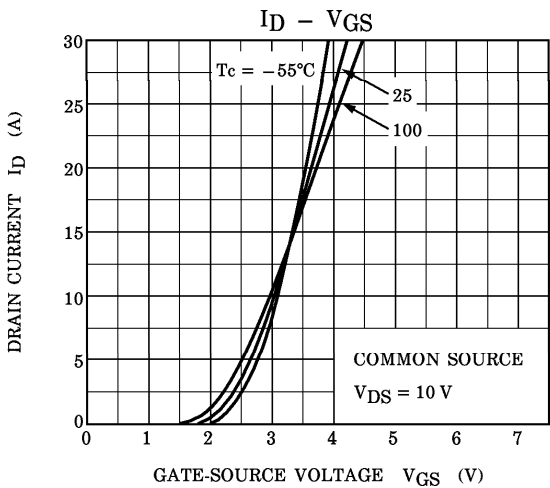
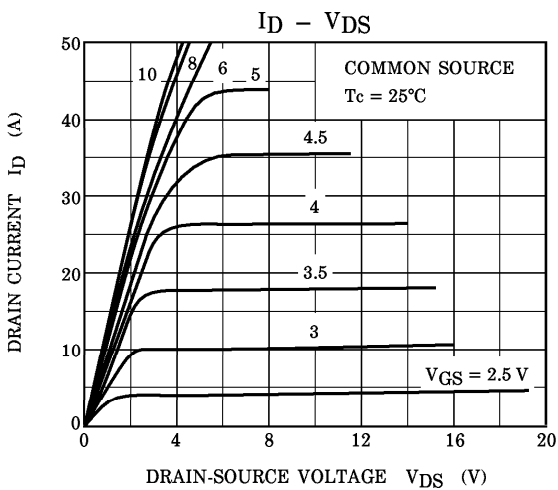
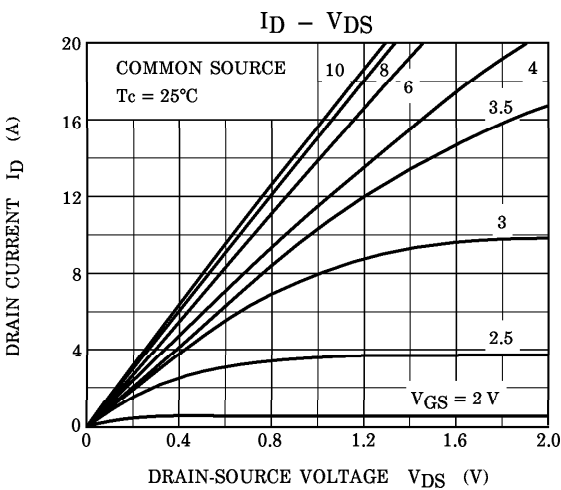
MARKING

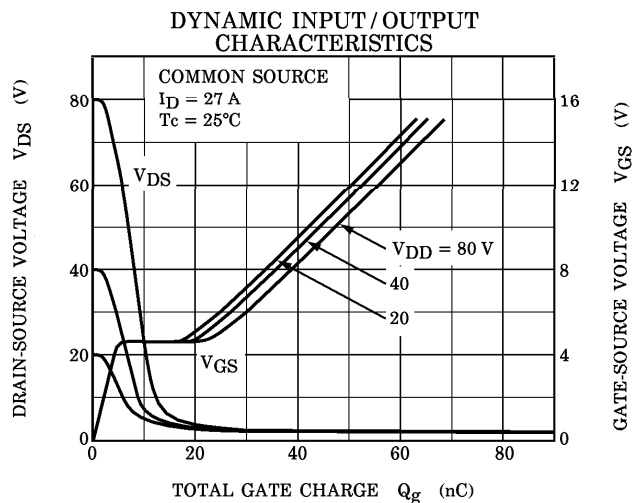
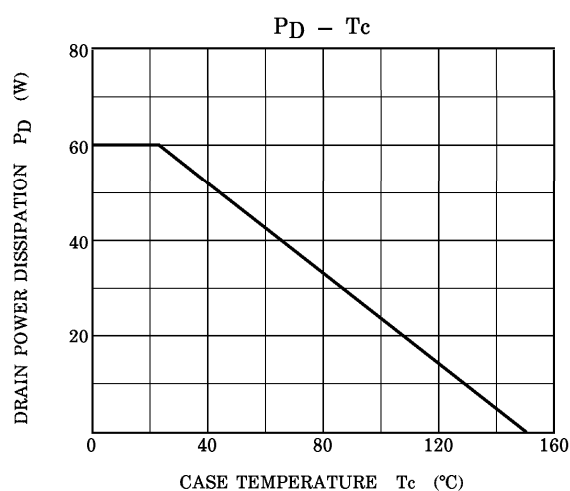
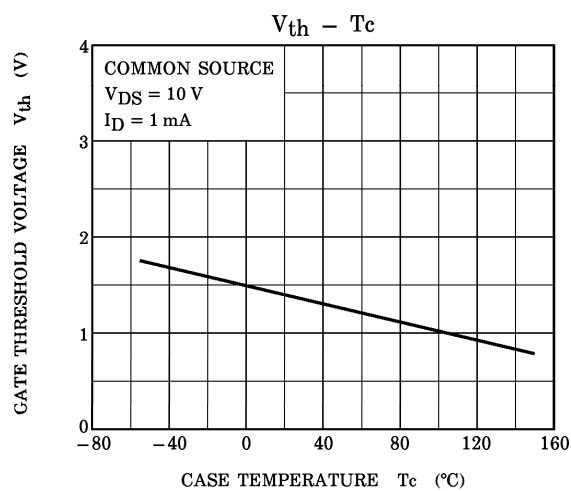
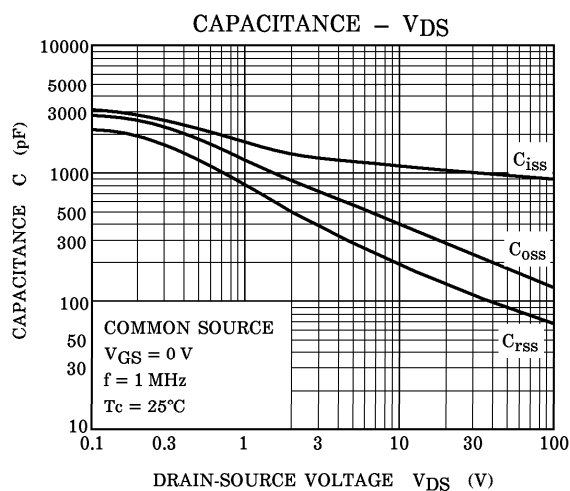
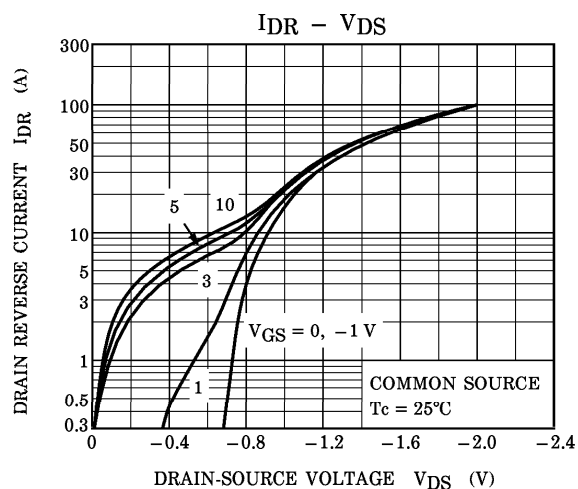
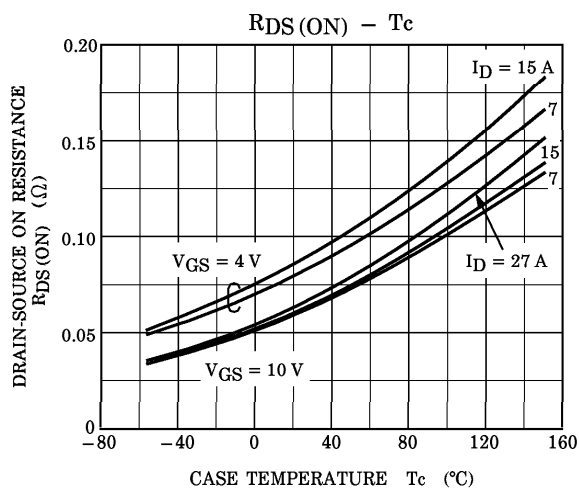


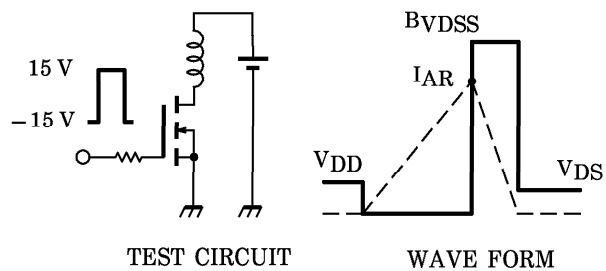
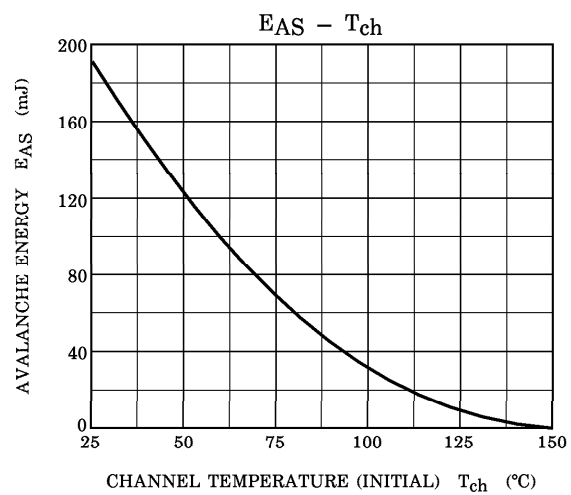
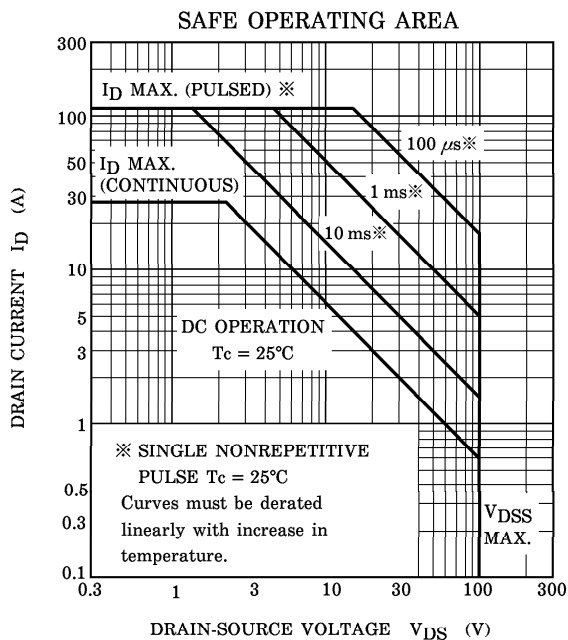
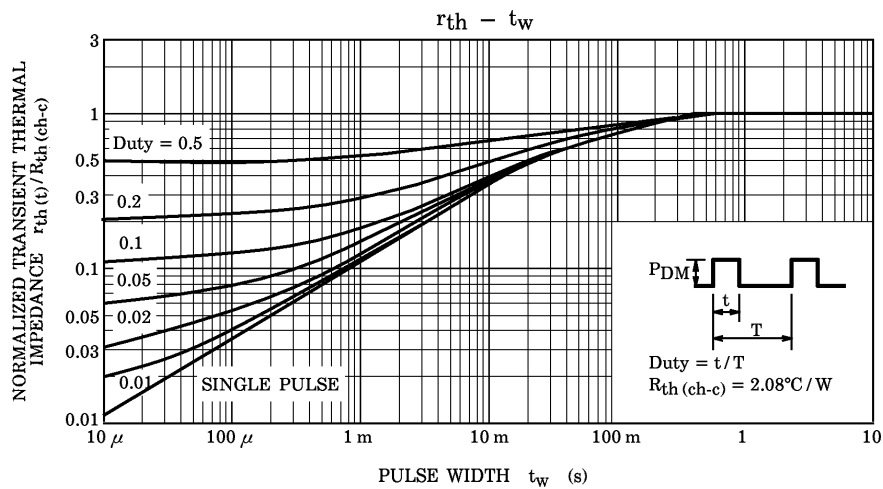
※ Lot Number

□ □ — Month (Starting from Alphabet A)

— Year (Last Number of the Christian Era)







Peak $I_{AR} = 27\text{ A}$, $R_G = 25\ \Omega$
 $V_{DD} = 25\text{ V}$, $L = 428\ \mu\text{H}$

$$E_{AS} = \frac{1}{2} \cdot L \cdot I^2 \cdot \left(\frac{B_{VDSS}}{B_{VDSS} - V_{DD}} \right)$$