

# 2SK1159, 2SK1160

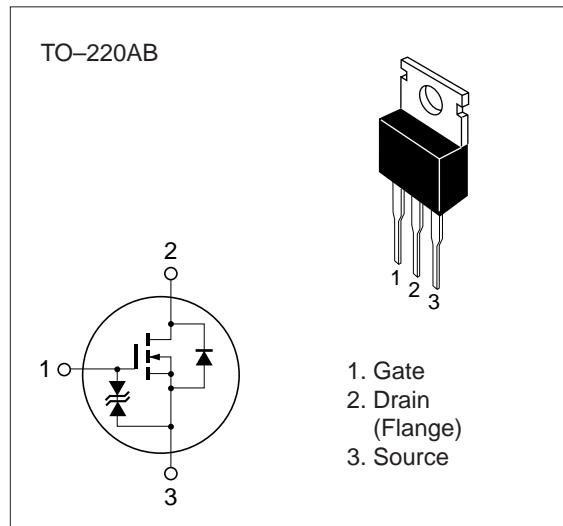
Silicon N-Channel MOS FET

## Application

High speed power switching

## Features

- Low on-resistance
- High speed switching
- Low drive current
- No secondary breakdown
- Suitable for switching regulator, DC-DC converter and motor driver



**Table 1 Absolute Maximum Ratings (Ta = 25°C)**

Item	Symbol	Ratings	Unit
Drain to source voltage 2SK1159	V <sub>DSS</sub>	450	V
		500	
Gate to source voltage	V <sub>GSS</sub>	±30	V
Drain current	I <sub>D</sub>	8	A
Drain peak current	I <sub>D(pulse)</sub> *	32	A
Body to drain diode reverse drain current	I <sub>DR</sub>	8	A
Channel dissipation	P <sub>ch</sub> **	60	W
Channel temperature	T <sub>ch</sub>	150	°C
Storage temperature	T <sub>stg</sub>	-55 to +150	°C

\* PW ≤ 10 µs, duty cycle ≤ 1 %

\*\* Value at T<sub>C</sub> = 25 °C

**Table 2 Electrical Characteristics (Ta = 25°C)**

Item		Symbol	Min	Typ	Max	Unit	Test conditions
Drain to source breakdown voltage	2SK1159	V <sub>(BR)DSS</sub>	450	—	—	V	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0
	2SK1160		500				
Gate to source breakdown voltage		V <sub>(BR)GSS</sub>	±30	—	—	V	I <sub>G</sub> = ±100 µA, V <sub>DS</sub> = 0
Gate to source leak current		I <sub>GSS</sub>	—	—	±10	µA	V <sub>GS</sub> = ±25 V, V <sub>DS</sub> = 0
Zero gate voltage drain current	2SK1159	I <sub>DSS</sub>	—	—	250	µA	V <sub>DS</sub> = 360 V, V <sub>GS</sub> = 0
	2SK1160						V <sub>DS</sub> = 400 V, V <sub>GS</sub> = 0
Gate to source cutoff voltage		V <sub>GS(off)</sub>	2.0	—	3.0	V	I <sub>D</sub> = 1 mA, V <sub>DS</sub> = 10 V
Static Drain to source on state resistance	2SK1159	R <sub>DS(on)</sub>	—	0.55	0.7	Ω	I <sub>D</sub> = 4 A, V <sub>GS</sub> = 10 V *
	2SK1160		—	0.60	0.8		
Forward transfer admittance		y <sub>fs</sub>	4.5	7.5	—	S	I <sub>D</sub> = 4 A, V <sub>DS</sub> = 10 V *
Input capacitance		C <sub>iss</sub>	—	1150	—	pF	V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 0,
Output capacitance		C <sub>oss</sub>	—	340	—	pF	f = 1 MHz
Reverse transfer capacitance		C <sub>rss</sub>	—	55	—	pF	
Turn-on delay time		t <sub>d(on)</sub>	—	17	—	ns	I <sub>D</sub> = 4 A, V <sub>GS</sub> = 10 V,
Rise time		t <sub>r</sub>	—	55	—	ns	R <sub>L</sub> = 7.5 Ω
Turn-off delay time		t <sub>d(off)</sub>	—	100	—	ns	
Fall time		t <sub>f</sub>	—	45	—	ns	
Body to drain diode forward voltage		V <sub>DF</sub>	—	0.9	—	V	I <sub>F</sub> = 8 A, V <sub>GS</sub> = 0
Body to drain diode forward voltage		t <sub>rr</sub>	—	350	—	ns	I <sub>F</sub> = 8 A, V <sub>GS</sub> = 0, di <sub>F</sub> /dt = 100 A/µs

\* Pulse Test

