2SJ504

Silicon P Channel MOS FET High Speed Power Switching

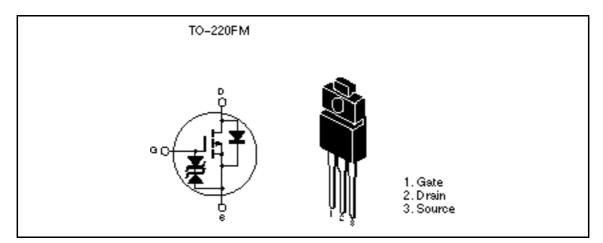
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ADE-208-546 Target specification 1st. Edition

Features

- Low on-resistance
- $R_{\text{DS(on)}}=0.042 \quad typ.$
- Low drive current.
- 4V gate drive devices.
- High speed switching.

Outline





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Absolute Maximum Ratings (Ta = 25° C)

Item	Symbol	Ratings	Unit	
Drain to source voltage	V _{DSS}	-60	V	
Gate to source voltage	V _{GSS}	±20	V	
Drain current	I _D	-20	А	
Drain peak current	L _{D(pulse)} *1	-80	А	
Body to drain diode reverse drain current	I _{DR}	-20	А	
Avalanche current	l* ³	-20	А	
Avalanche energy	E _{AR} * ³	34	mJ	
Channel dissipation	Pch* ²	30	W	
Channel temperature	Tch	150	°C	
Storage temperature	Tstg	-55 to +150	°C	
Notaci 1 DW 10up duty avala 1.0/				

Notes: 1. PW 10 μ s, duty cycle 1 %

2. Value at Tc = 25°C

3. Value at Ta = 25°C, Rg $\,$ 50 $\,$, L=100 μH

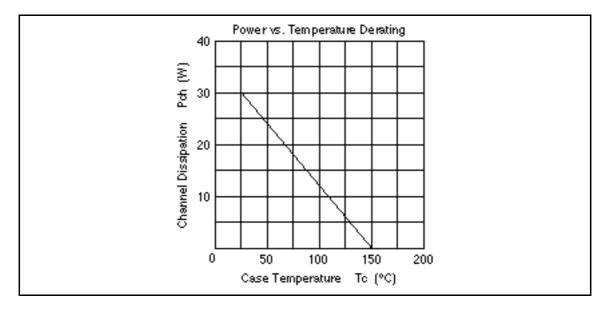
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	-60	_	—	V	$I_{\rm D} = -10 {\rm mA}, V_{\rm GS} = 0$
Gate to source breakdown voltage	$V_{(\text{BR})\text{GSS}}$	±20	—	_	V	$I_{\rm G} = \pm 100 \mu A, V_{\rm DS} = 0$
Zero gate voltege drain current	I _{DSS}	_	—	-10	μA	$V_{DS} = -60 \text{ V}, \text{ V}_{GS} = 0$
Gate to source leak current	I _{GSS}	_	_	±10	μA	$V_{\rm GS}=\pm 16V,~V_{\rm DS}=0$
Gate to source cutoff voltage	$V_{GS(off)}$	-1.0	_	-2.0	V	$I_{\rm D} = -1$ mA, $V_{\rm DS} = -10$ V
Static drain to source on state	$R_{\text{DS(on)}}$	_	0.042	0.055		$I_{\rm D} = -10$ A, $V_{\rm GS} = -10$ V ^{*1}
resistance	R _{DS(on)}	_	0.065	0.095		$I_{\rm D} = -10$ A, $V_{\rm GS} = -4$ V ^{*1}
Forward transfer admittance	y _{fs}	10	16	_	S	$I_{\rm D} = 10$ A, $V_{\rm DS} = 10$ V ^{*1}
Input capacitance	Ciss	_	1750	_	pF	$V_{DS} = -10V$
Output capacitance	Coss	_	800	_	pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss	_	180	_	pF	f = 1MHz
Turn-on delay time	t _{d(on)}	_	16	_	ns	$V_{GS} = -10V, I_{D} = -10A$
Rise time	t _r	_	100	_	ns	R _L = 3
Turn-off delay time	$t_{d(off)}$	_	230	_	ns	
Fall time	t _f	—	140	_	ns	
Body to drain diode forward voltage	V_{DF}	_	-1.0	—	V	$I_{\rm F} = -20$ A, $V_{\rm GS} = 0$
Body to drain diode reverse recovery time	t _{rr}	—	100	—	ns	$I_F = -20A$, $V_{GS} = 0$ diF/ dt = 50A/µs
Noto: 1 Dulas test						

Electrical Characteristics (Ta = 25° C)

Note: 1. Pulse test

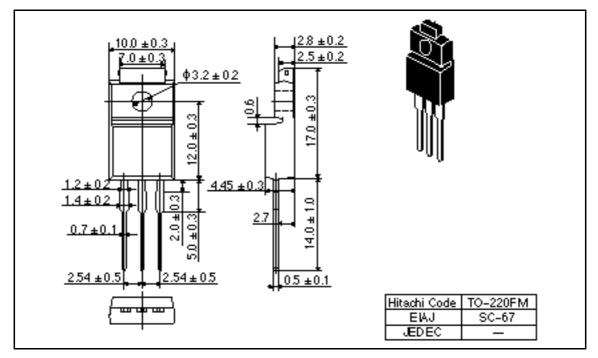
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Main Characteristics



Package Dimensions





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