

**PM3350**

**ELAN 8X10**

**10 MBIT/S ETHERNET SWITCH**

**REVISION C DEVICE ERRATA**

**ISSUE 3: JULY 1998**

**CONTENTS**

1	ISSUE 1 INTRODUCTION.....	1
	1.1 DEVICE IDENTIFICATION .....	1
	1.2 REFERENCE .....	1
2	FUNCTIONAL DISCREPANCIES OVERVIEW.....	2
3	HARDWARE ERRATA.....	3
	3.1 NONE KNOWN .....	3
4	DATASHEET ERRATA.....	4
	4.1 FEATURES – PAGE 6 .....	5
	4.2 FIRMWARE ERRATA.....	5
5	CONTACTING PMC-SIERRA, INC. ....	7

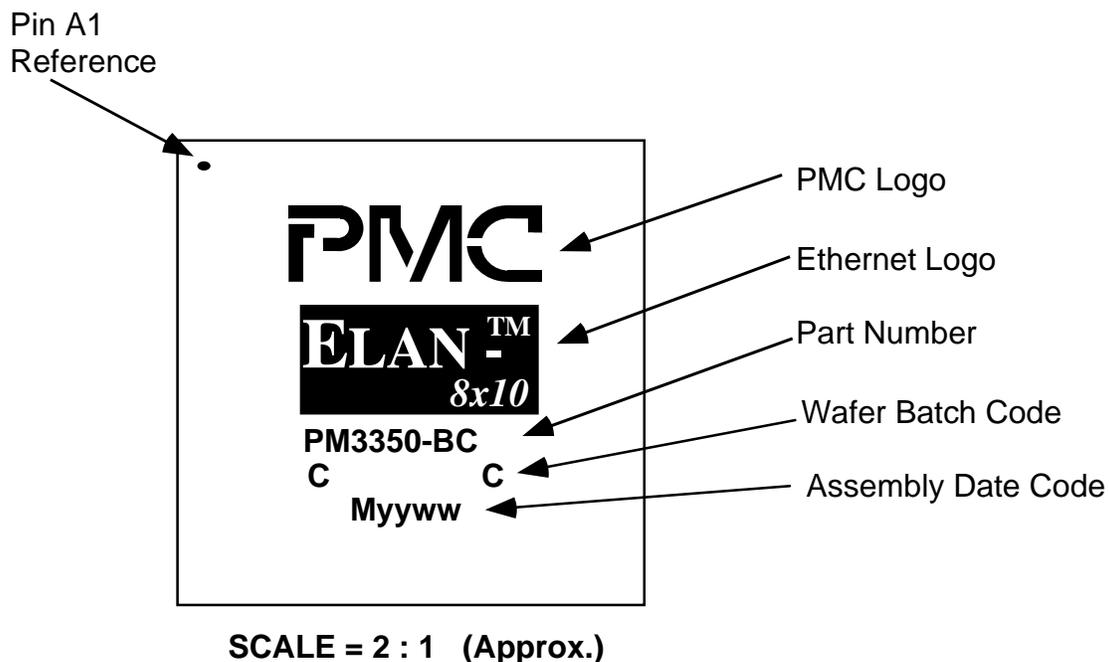
## 1 ISSUE 1 INTRODUCTION

This document is the errata notice for Revision C of the ELAN 8x10 (PM3350-BC) and issue 3 ELAN 8x10 datasheet. The issue 3 ELAN 8x10 datasheet (PMC-970109) and issue 3 errata supersede all prior editions and versions of the datasheet.

### 1.1 DEVICE IDENTIFICATION

The information contained in Section 2 relates to Revision C of PM3350 Elan 8x10 only. The device revision code is marked at the end of the Wafer batch Code on the face of the device (as shown in Fig. 1.1). PM330 Elan 8x10 Revision C is packaged in a 256-pin Ball Grid Array (BGA) package.

Fig 1.1: PM3350 Elan 8x10 Branding Format



### 1.2 REFERENCE

PMC-970109, Elan 8x10 Long Form Data Sheet, Issue 3

## **2 FUNCTIONAL DISCREPANCIES OVERVIEW**

This section outlines the known functional discrepancies between the PM3350 ELAN 8x10 revision C device and the Issue 3 ELAN 8x10 datasheet. The errata are explained in more detail in following sections. Please note that the errata contained in this document are preliminary.

**Table 1 FUNCTIONAL DISCREPANCIES LIST**

<b>Hardware Errata</b>			
<b>#</b>	<b>Discrepancy</b>	<b>Workaround?</b>	<b>Performance without workaround</b>
3.1	No known hardware errors	N/A	

<b>Datasheet Errata</b>	
<b>#</b>	<b>Errata</b>
4.1	Features

### **3 HARDWARE ERRATA**

This section contains errata which reflects chip level discrepancies.

#### **3.1 None Known**

**Description**

None known

**Software Workaround:**

N/A

**Performance With Workaround:**

N/A

## **4 DATASHEET ERRATA**

*The section contains errata in the issue 3 ELAN 8x10 datasheet.*

### **Datasheet Errata Legend**

1. Unaltered text is unchanged to add context to changes
2. ***New material is bold and italicized***
3. ~~Obsolete material is struck out~~
4. *Comments specific to this document are in italics*

## 4.1 Features – page 6

- ~~Implements the Link Partition function to isolate malfunctioning segments or hosts.~~
- ~~IEEE 802.1d compliant spanning tree transparent bridging supported on-chip, with **Factory** configurable aging time and packet lifetime control.~~

*Aging is implemented with released firmware*

- ~~On-chip user **Factory** enabled backpressure flow control with configurable per-port buffer thresholds and limits.~~
- ~~Configuration, management, MIB statistics and diagnostics available in-band (via on-chip SNMP agent with integral TCP/UDP/IP stack) or out-of-band.~~
- ~~Maintains and collects per-port and per-host statistics at wire rates, allowing a network switch comprised of PM3351 and PM3350 chips to implement RMON statistics (EtherStats and HostStats) using supplied on-chip firmware.~~

*Note: All other references in the datasheet to the features discussed above should be interpreted to reflect the above comments and edits.*

## 4.2 Firmware Errata

### **Description**

Firmware does not implement IEEE 802.3 LLC length field consistency checks. Firmware is fully compliant with DIX ethernet.

### **Software Workaround:**

None

### **Performance With Workaround:**

n/a

PRELIMINARY



PM3350 ELAN 8X10

ERRATA

PMC-980355

ISSUE 2

10 MBIT/S ETHERNET SWITCH ERRATA

---

## **5 CONTACTING PMC-SIERRA, INC.**

PMC-Sierra, Inc.  
105-8555 Baxter Place Burnaby, BC  
Canada V5A 4V7

Tel: (604) 415-6000  
Apps Support: (604) 415-4533  
Fax: (604) 415-6200

Document Information: [document@pmc-sierra.com](mailto:document@pmc-sierra.com)  
Corporate Information: [info@pmc-sierra.com](mailto:info@pmc-sierra.com)  
Application Information: [apps@pmc-sierra.com](mailto:apps@pmc-sierra.com)  
Web Site: <http://www.pmc-sierra.com>

None of the information contained in this document constitutes an express or implied warranty by PMC-Sierra, Inc. as to the sufficiency, fitness or suitability for a particular purpose of any such information or the fitness, or suitability for a particular purpose, merchantability, performance, compatibility with other parts or systems, of any of the products of PMC-Sierra, Inc., or any portion thereof, referred to in this document. PMC-Sierra, Inc. expressly disclaims all representations and warranties of any kind regarding the contents or use of the information, including, but not limited to, express and implied warranties of accuracy, completeness, merchantability, fitness for a particular use, or non-infringement.

In no event will PMC-Sierra, Inc. be liable for any direct, indirect, special, incidental or consequential damages, including, but not limited to, lost profits, lost business or lost data resulting from any use of or reliance upon the information, whether or not PMC-Sierra, Inc. has been advised of the possibility of such damage.

© 1998 PMC-Sierra, Inc.

PMC-980355 (R2) ref PMC-970109 (R3) Issue date: June 1998