AN7511

1-W BTL audio power amplifier

Overview

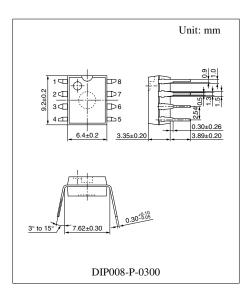
The AN7511 is an audio power amplifier IC with 1-ch output. The BTL (Balanced Transformer-Less) method can provide fewer external parts and more easy design for applications.

Features

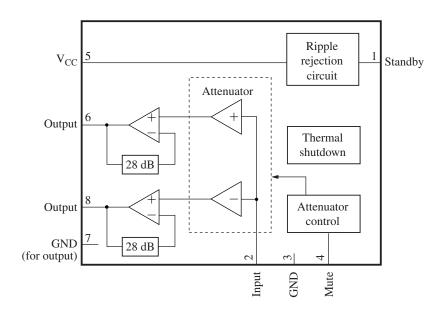
- \bullet 1-W output (8 $\Omega)$ with supply voltage of 5 V
- On-chip standby function
- On-chip muting function

Applications

• Televisions, radios, and personal computers



Block Diagram



Pin Descriptions

Pin No.	Description			
1	Standby (standby state if this pin is open.)			
2	Input			
3	Ground (for input)			
4	Muting (muting on if this pin is open.)			
5	Supply voltage			
6	+ Output			
7	Ground (for output ch.1)			
8	– Output			

Absolute Maximum Ratings

Parameter	Symbol	Rating	Unit
Supply voltage *2	V _{CC}	14	V
Supply current	I _{CC}	1.0	А
Power dissipation *3	P _D	541	mW
Operating ambient temperature *1	T _{opr}	-25 to +70	°C
Storage temperature *1	T _{stg}	-55 to +150	°C

Note) *1: Except for the operating ambient temperature and storage temperature, all ratings are for $T_a = 25^{\circ}C$.

*2: At no signal

*3: The power dissipation shown is the value for $T_a = 70^{\circ}C$.

Recommended Operating Range

Parameter	Symbol	Range	Unit
Supply voltage	V _{CC}	3.5 to 13.5	V

Electrical Characteristics at V_{CC} = 5.0 V, R_L = 8 Ω , f = 1 kHz, T_a = 25°C ± 2°C

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Quiescent circuit current	I _{CQ}	$V_{IN} = 0 mV$	—	30	60	mA
Standby current	I _{STB}	$V_{IN} = 0 mV$	_	1	10	μA
Output noise voltage *	V _{NO}	$R_g = 10 \text{ k}\Omega$	_	0.14	0.4	mV[rms]
Voltage gain	G _V	$P_{O} = 0.25 \text{ W}$	32	34	36	dB
Total harmonic distortion	THD	$P_{O} = 0.25 \text{ W}$	_	0.05	0.5	%
Maximum output power	P _{O1}	THD = 10%	0.8	1.1	_	W
Ripple rejection ratio *	RR	$R_g = 10 \text{ k}\Omega, V_R = 0.5 \text{ V[rms]}, f_R = 120 \text{ Hz}$	30	50		dB
Output offset voltage	V _{OFF}	$R_g = 10 \text{ k}\Omega$	-300	0	300	mV
Muting effect *	MT	$P_{O} = 0.25 \text{ W}$	70	86	_	dB

Note) *: In measuring, the filter for the range of 15 Hz to 30 kHz (12 dB/OCT) is used.

Terminal Equivalent Circuits

Pin No.	Pin name	Equivalent circuit	Voltage
1	Standby pin	V_{CC} Q	5 V
2	Input pin	V_{CC} · · · · · · · · · · · · · · · · · · ·	_
3	GND	(3)	0 V
4	Muting pin	$V_{CC} \circ$	_

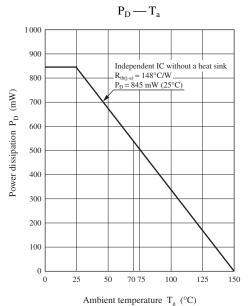
Terminal Equivalent Circuits (continued)

Pin No.	Pin name	Equivalent circuit	Voltage
5	V _{CC}	_	5.0 V
6	+ Output pin	$1/2 V_{CC} \qquad \qquad$	2.15 V
7	GND	7	0 V
8	– Output pin	$1/2 V_{CC} \xrightarrow{W}_{20 k\Omega} \xrightarrow{20 k\Omega} \xrightarrow{50 \Omega} \xrightarrow{8}_{777}$	2.15 V

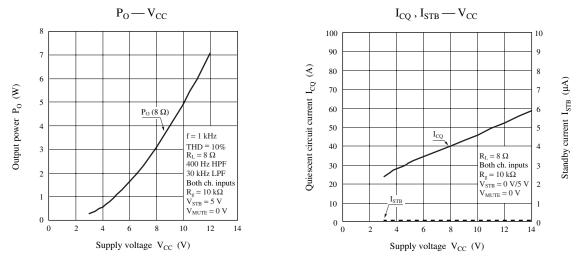
Usage Notes

- \bullet Please avoid the short circuit to V_{CC} , ground, or load short circuit.
- Please connect the cooling fin with the GND potential.
- The thermal shutdown circuit operates at about $T_j = 150^{\circ}$ C. However, the thermal shutdown circuit is reset automatically if the temperature drops.
- \bullet Please carefully design the heat radiation especially when you take out high power at high V_{CC} .
- Please connect only the ground of signal with the signal GND of the amplifier in the previous stage.

1. Package power dissipation

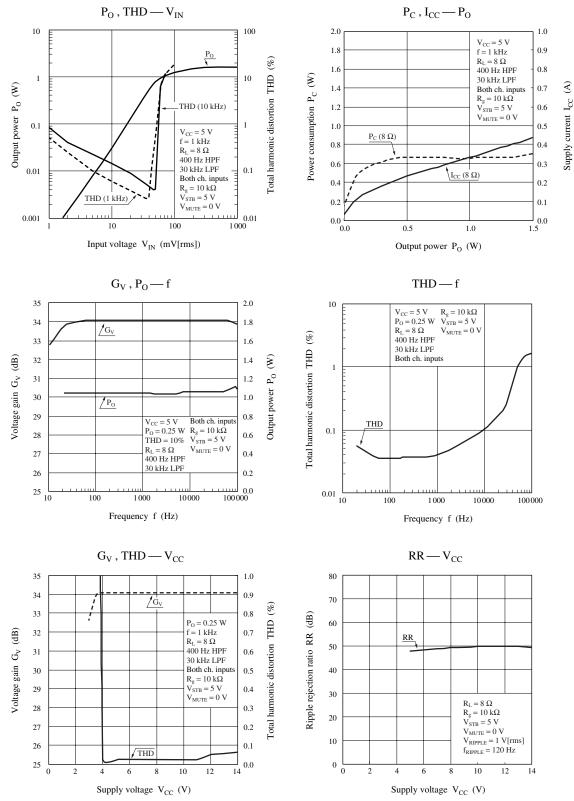


2. Main characteristics

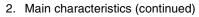


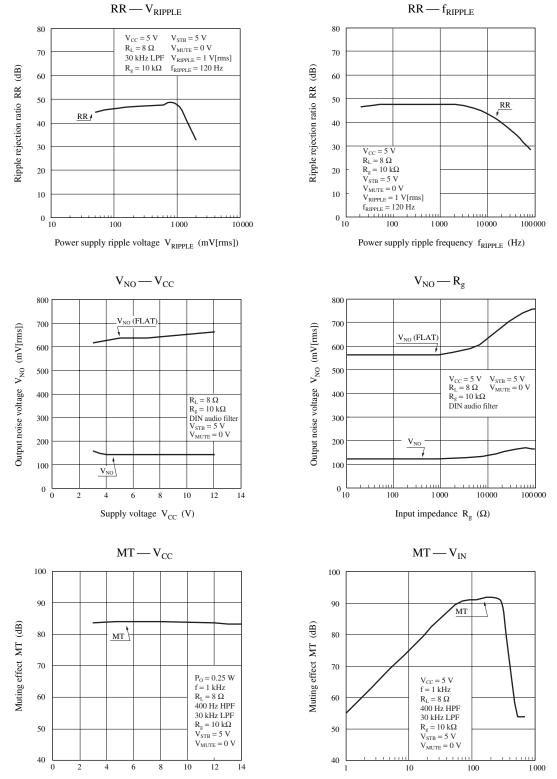
Technical Data (continued)





Technical Data (continued)



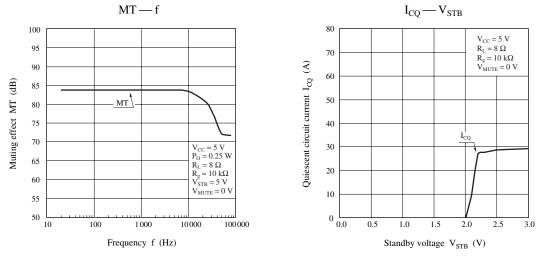


Input voltage V_{IN} (mV[rms])

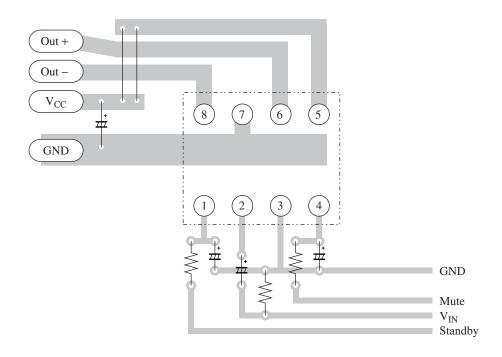
Supply voltage V_{CC} (V)

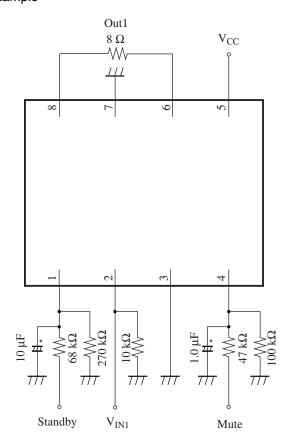
■ Technical Data (continued)





3. Example of PCB pattern





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