

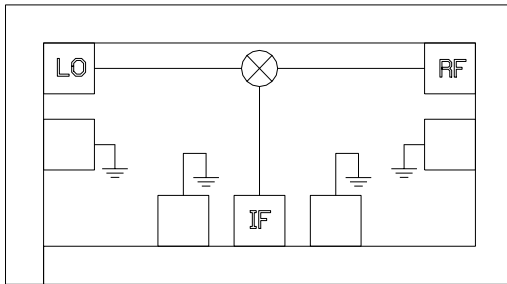
GaAs MMIC FUNDAMENTAL MIXER, 14 - 26 GHz

Typical Applications

The HMC260 is ideal for:

- Point to Point Radios
- Point to Multi Point Radios

Functional Diagram



Features

Passive: No DC Bias Required

Input IP3: +20 dBm

LO/RF Isolation: 39 dB

Small Size: 0.55mm²

General Description

The HMC260 is a passive double balanced mixer that can be used as an upconverter or downconverter between 14 and 26 GHz. The miniature monolithic mixer (MMIC) requires no external components or matching circuitry. The HMC260 provides excellent LO to RF and LO to IF suppression due to optimized balun structures. The mixer operates with LO drive levels above +9 dBm. Measurements were made with the chip mounted and bonded into in a 50 ohm test fixture. Data includes the parasitic effects of wire bond assembly. Connections were made with a 3 mil ribbon bond with minimal length (<12 mil).

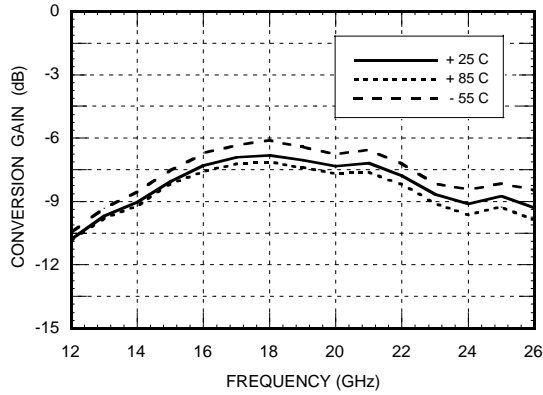
Electrical Specifications, $T_A = +25^\circ C$

Parameter	LO = +13 dBm, IF = 1 GHz			Units
	Min.	Typ.	Max.	
Frequency Range, RF & LO	14 - 26			GHz
Frequency Range, IF	DC - 8			GHz
Conversion Loss		7.5	10.5	dB
Noise Figure (SSB)		7.5	10.5	dB
LO to RF Isolation	30	39		dB
LO to IF Isolation	25	35		dB
RF to IF Isolation	18	25		dB
IP3 (Input)	13	20		dBm
IP2 (Input)	45	55		dBm
1 dB Gain Compression (Input)	6	11		dBm

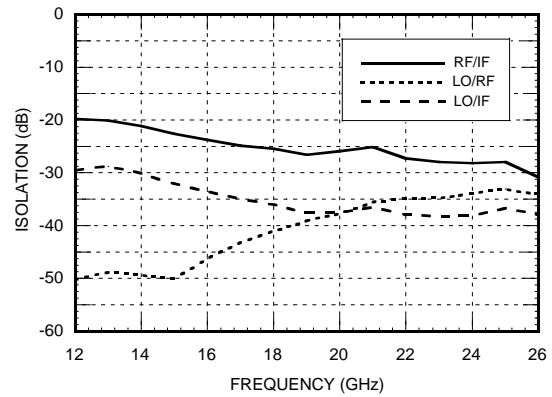
*Unless otherwise noted, all measurements performed as downconverter, IF= 1 GHz.

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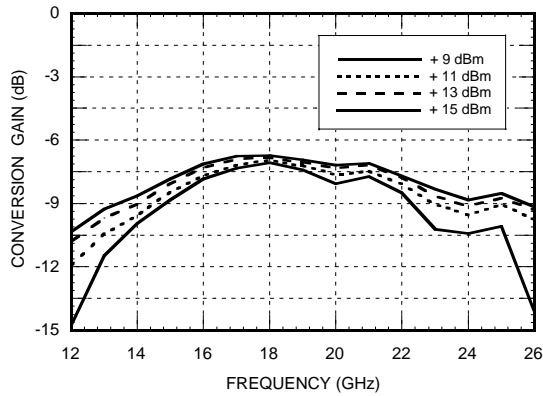
Conversion Gain vs. Temperature @ LO = +13 dBm



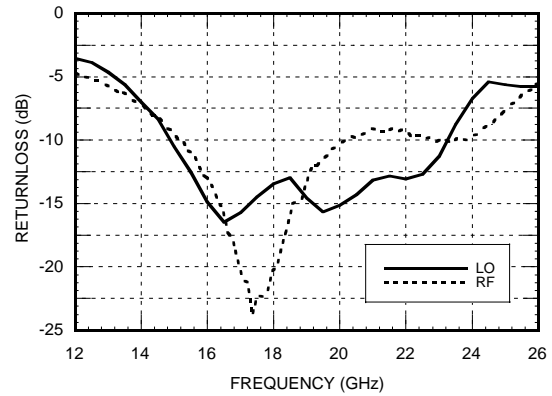
Isolation @ LO = +13 dBm



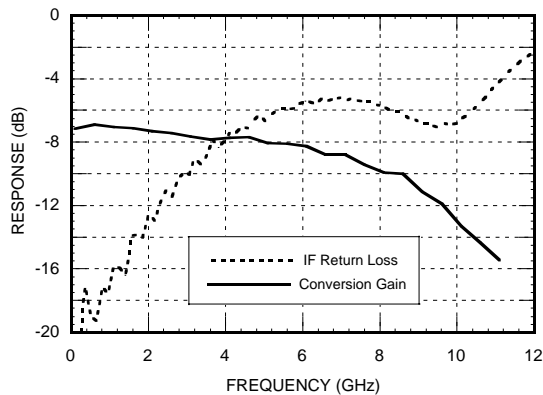
Conversion Gain vs. LO Drive



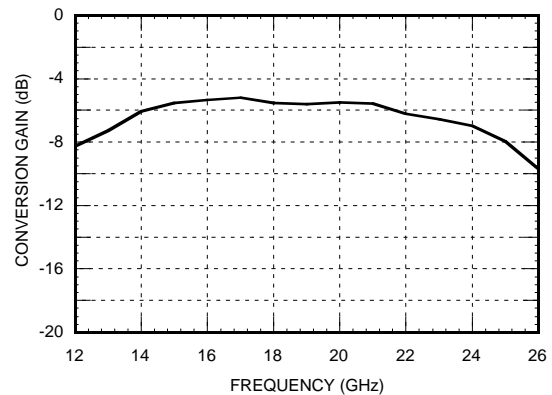
Return Loss @ LO = +13 dBm



IF Bandwidth @ LO = +13 dBm

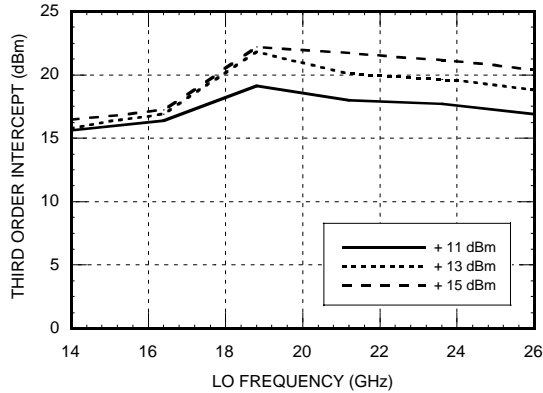


Upconverter Performance Conversion Gain @ LO = +13 dBm

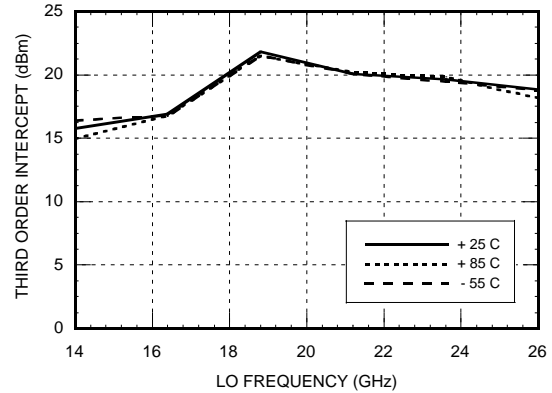


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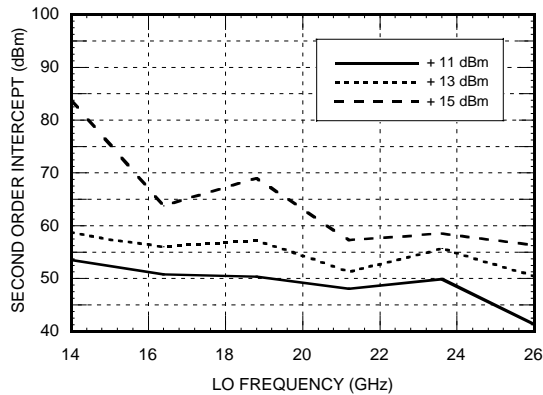
Input IP3 vs. LO Drive *



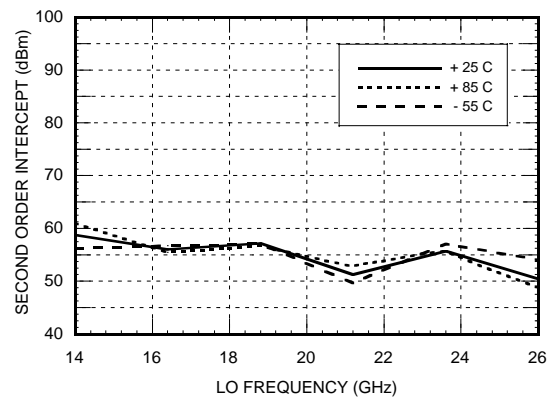
Input IP3 vs. Temperature @ LO = +13 dBm *



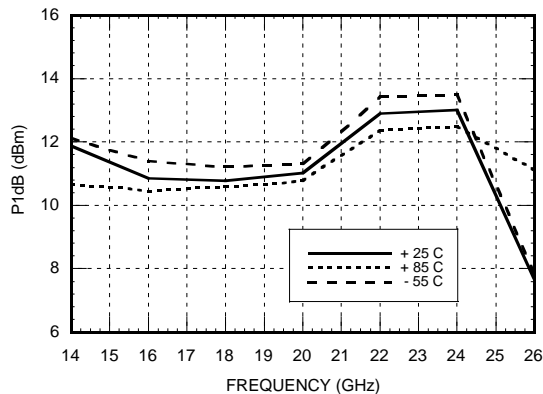
Input IP2 vs. LO Drive *



Input IP2 vs. Temperature @ LO = +13 dBm *



Input P1dB vs. Temperature @ LO = +13 dBm



MxN Spurious Outputs

mRF	nLO				
	0	1	2	3	4
0	xx	9	19	xx	xx
1	20	0	46	37	xx
2	64	72	68	82	95
3	xx	92	99	83	94
4	xx	xx	102	>110	>110

RF = 21 GHz @ -10 dBm
LO = 22 GHz @ +13 dBm
All values in dBc below the IF output power level.

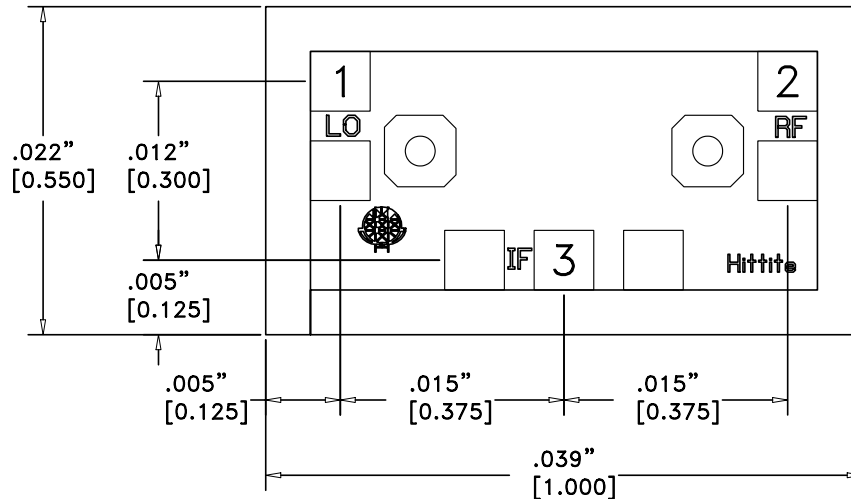
* Two-tone input power = -5 dBm each tone, 1 MHz spacing.

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Absolute Maximum Ratings

RF / IF Input	+15 dBm
LO Drive	+27 dBm
Storage Temperature	-65 to +150 °C
Operating Temperature	-55 to +85 °C
IF DC Current	±4 mA

Outline Drawing (See Handling Mounting Bonding Note)

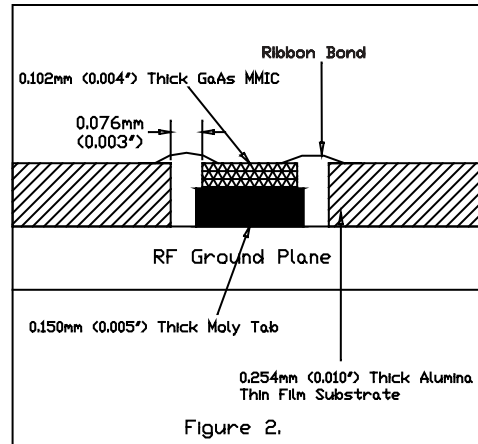
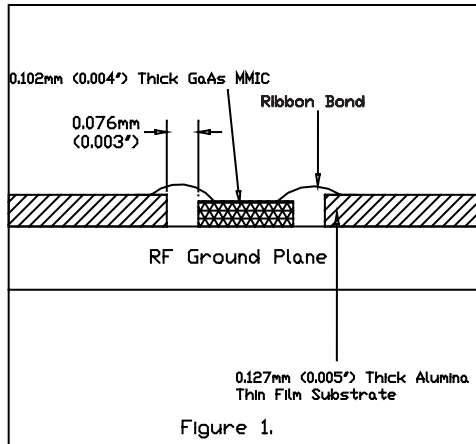


NOTES:

1. ALL DIMENSIONS ARE IN INCHES [MM].
2. DIE THICKNESS IS .004".
3. TYPICAL BOND PAD IS .004" SQUARE.
4. BOND PAD SPACING CENTER TO CENTER IS .006".
5. BACKSIDE METALLIZATION: GOLD.
6. BOND PAD METALLIZATION: GOLD.
7. BACKSIDE METAL IS GROUND.
8. CONNECTION NOT REQUIRED FOR UNLABELED BOND PADS.

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MIC Assembly Techniques



Mounting & Bonding Techniques for Millimeterwave GaAs MMICs

The die should be attached directly to the ground plane eutectically or with conductive epoxy (see HMC general Handling, Mounting, Bonding Note).

50 Ohm Microstrip transmission lines on 0.127mm (5 mil) thick alumina thin film substrates are recommended for bringing RF to and from the chip (Figure 1). If 0.254mm (10 mil) thick alumina thin film substrates must be used, the die should be raised 0.150mm (6 mils) so that the surface of the die is coplanar with the surface of the substrate. One way to accomplish this is to attach the 0.102mm (4 mil) thick die to a 0.150mm (6 mil) thick molybdenum heat spreader (moly-tab) which is then attached to the ground plane (Figure 2).

Microstrip substrates should be brought as close to the die as possible in order to minimize ribbon bond length. Typical die-to-substrate spacing is 0.076mm (3 mils). Gold ribbon of 0.075 mm (3 mil) width and minimal length <0.31 mm (<12 mils) is recommended to minimize inductance on RF, LO & IF ports.

**GaAs MMIC FUNDAMENTAL
MIXER, 14 - 26 GHz****Handling Precautions**

Follow these precautions to avoid permanent damage.

Cleanliness: Handle the chips in a clean environment. DO NOT attempt to clean the chip using liquid cleaning systems.

Static Sensitivity: Follow ESD precautions to protect against > ± 250V ESD strikes.

Transients: Suppress instrument and bias supply transients while bias is applied. Use shielded signal and bias cables to minimize inductive pick-up.

General Handling: Handle the chip along the edges with a vacuum collet or with a sharp pair of bent tweezers. The surface of the chip has fragile air bridges and should not be touched with vacuum collet, tweezers, or fingers.

Mounting

The chip is back-metallized and can be die mounted with AuSn eutectic preforms or with electrically conductive epoxy. The mounting surface should be clean and flat.

Eutectic Die Attach: A 80/20 gold tin preform is recommended with a work surface temperature of 255 °C and a tool temperature of 265 °C. When hot 90/10 nitrogen/hydrogen gas is applied, tool tip temperature should be 290 °C. DO NOT expose the chip to a temperature greater than 320 °C for more than 20 seconds. No more than 3 seconds of scrubbing should be required for attachment.

Epoxy Die Attach: Apply a minimum amount of epoxy to the mounting surface so that a thin epoxy fillet is observed around the perimeter of the chip once it is placed into position. Cure epoxy per the manufacturer's schedule.

Wire Bonding

RF bonds made with 0.003" x 0.0005" ribbon are recommended. These bonds should be thermosonically bonded with a force of 40-60 grams. DC bonds of 0.001" (0.025 mm) diameter, thermosonically bonded, are recommended. Ball bonds should be made with a force of 40-50 grams and wedge bonds at 18-22 grams. All bonds should be made with a nominal stage temperature of 150 °C. A minimum amount of ultrasonic energy should be applied to achieve reliable bonds. All bonds should be as short as possible, less than 12 mils (0.31 mm).