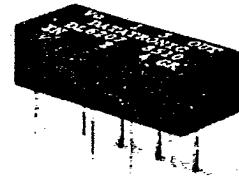


SERIES DL6200 DIGITAL DELAY MODULES

- 16 Pin Package • TTL and DTL Compatible • 5 Equally Spaced Taps

Specifications

- Supply Voltage : 4.75 to 5.25VDC
- Logic 1 Input Current : 50 μ A Max
- Logic 0 Input Current : -2mA Max
- Logic 1 Volt Out : 2.5v Min
- Logic 0 Volt Out : 0.5v Max
- Logic 1 Fan-Out : 20/Tap Max
- Logic 0 Fan-Out : 10/Tap Max
- Power Dissipation : 240mW Total
- Operating Temp Range : 0°C to 70°C
- Temp Coefficient : 100PPM/°C

**Input Test Conditions**

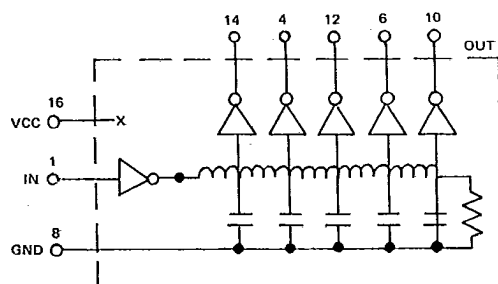
- Input Pulse Voltage : 3.2v
- Input Rise Time : $\leq$ 3NS
- Input Current : 60mA Typ.
- Pulse Width : Min 40% of Total Delay

Electrical Specifications at 25°C (measured with no loads on Taps)

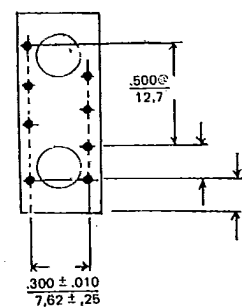
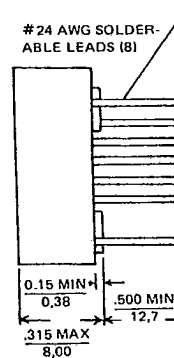
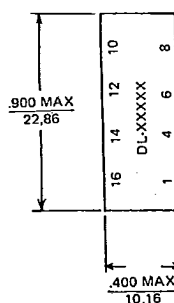
Part Number	Total Delay NS $\pm$ 5%	Tap to Tap Delay NS ⁽¹⁾	Rise Time NS Max ⁽²⁾
DL6205	25	5	3
DL6206	50	10	3
DL6209	75	15	3
DL6207	100	20	3
DL6208	150	30	3
DL6209	200	40	3
DL6210	250	50	4
DL6211	300	60	4
DL6212	400	80	4
DL6213	500	100	4

Note: (1) Measured at 1.5v level leading edge, Tol. $\pm$ 5% or $\pm$ 2NS, whichever is greater.

(2) Measured from 0.75v to 2.4v.



SCHEMATIC



INCHES . XXX $\pm$.010
MILLIMETERS . X $\pm$.25