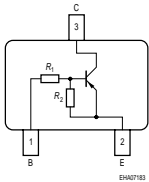


PNP Silicon Digital Transistor

- Switching circuit, inverter, interface circuit, driver circuit
- Built in bias resistor ($R_1 = 47k\Omega$, $R_2 = 22k\Omega$)



BCR196/F/L3
BCR196T/W



Type	Marking	Pin Configuration						Package
		1=B	2=E	3=C	-	-	-	
BCR196	WXs	1=B	2=E	3=C	-	-	-	SOT23
BCR196F	WXs	1=B	2=E	3=C	-	-	-	TSFP-3
BCR196L3	WX	1=B	2=E	3=C	-	-	-	TSLP-3-4
BCR196T	WXs	1=B	2=E	3=C	-	-	-	SC75
BCR196W	WXs	1=B	2=E	3=C	-	-	-	SOT323

Maximum Ratings

Parameter	Symbol	Value	Unit
Collector-emitter voltage	V_{CEO}	50	V
Collector-base voltage	V_{CBO}	50	
Emitter-base voltage	V_{EBO}	10	
Input on voltage	$V_{i(on)}$	50	
Collector current	I_C	70	mA
Total power dissipation- BCR196, $T_S \leq 102^\circ\text{C}$ BCR196F, $T_S \leq 128^\circ\text{C}$ BCR196L3, $T_S \leq 135^\circ\text{C}$ BCR196T, $T_S \leq 109^\circ\text{C}$ BCR196W, $T_S \leq 124^\circ\text{C}$	P_{tot}	200 250 250 250 250	mW
Junction temperature	T_j	150	$^\circ\text{C}$
Storage temperature	T_{stg}	150 ... -65	

Thermal Resistance

Parameter	Symbol	Value	Unit
Junction - soldering point ¹⁾	R_{thJS}		K/W
BCR196		≤ 240	
BCR196F		≤ 90	
BCR196L3		≤ 60	
BCR196T		≤ 165	
BCR196W		≤ 105	

¹⁾For calculation of R_{thJA} please refer to Application Note Thermal Resistance

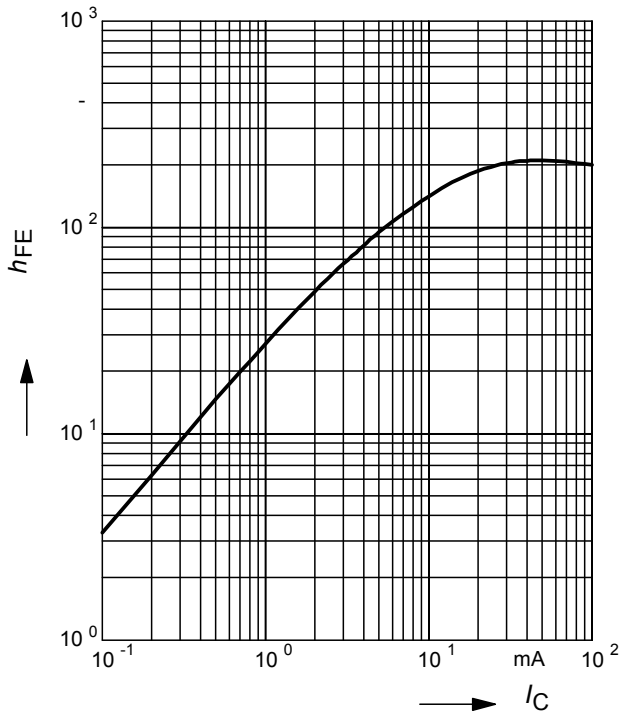
Electrical Characteristics at $T_A = 25^\circ\text{C}$, unless otherwise specified

Parameter	Symbol	Values			Unit
		min.	typ.	max.	
DC Characteristics					
Collector-emitter breakdown voltage $I_C = 100 \mu\text{A}, I_B = 0$	$V_{(BR)CEO}$	50	-	-	V
Collector-base breakdown voltage $I_C = 10 \mu\text{A}, I_E = 0$	$V_{(BR)CBO}$	50	-	-	
Collector-base cutoff current $V_{CB} = 40 \text{ V}, I_E = 0$	I_{CBO}	-	-	100	nA
Emitter-base cutoff current $V_{EB} = 10 \text{ V}, I_C = 0$	I_{EBO}	-	-	220	μA
DC current gain ¹⁾ $I_C = 5 \text{ mA}, V_{CE} = 5 \text{ V}$	h_{FE}	50	-	-	-
Collector-emitter saturation voltage ¹⁾ $I_C = 10 \text{ mA}, I_B = 0,5 \text{ mA}$	V_{CEsat}	-	-	0,3	V
Input off voltage $I_C = 100 \mu\text{A}, V_{CE} = 5 \text{ V}$	$V_{i(off)}$	1,2	-	2,6	
Input on voltage $I_C = 2 \text{ mA}, V_{CE} = 0,3 \text{ V}$	$V_{i(on)}$	1,5	-	4	
Input resistor	R_1	32	47	62	$\text{k}\Omega$
Resistor ratio	R_1/R_2	1,92	2,14	2,36	-
AC Characteristics					
Transition frequency $I_C = 10 \text{ mA}, V_{CE} = 5 \text{ V}, f = 100 \text{ MHz}$	f_T	-	150	-	MHz
Collector-base capacitance $V_{CB} = 10 \text{ V}, f = 1 \text{ MHz}$	C_{cb}	-	3	-	pF

¹Pulse test: $t < 300 \mu\text{s}$; $D < 2\%$

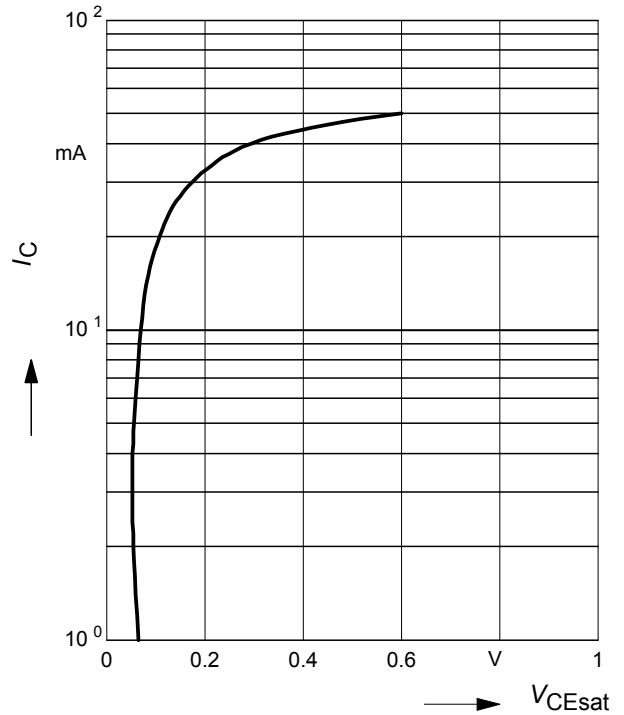
DC current gain $h_{FE} = f(I_C)$

$V_{CE} = 5\text{ V}$ (common emitter configuration)



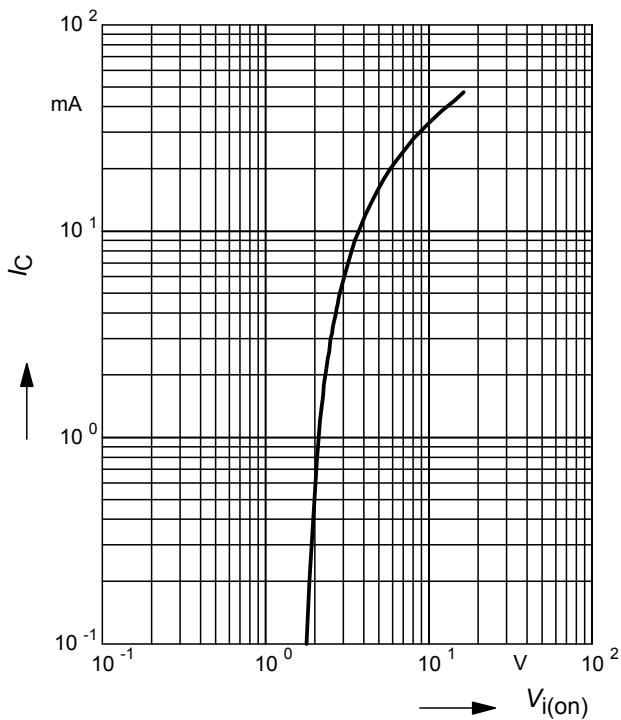
Collector-emitter saturation voltage

$V_{CEsat} = f(I_C), h_{FE} = 20$



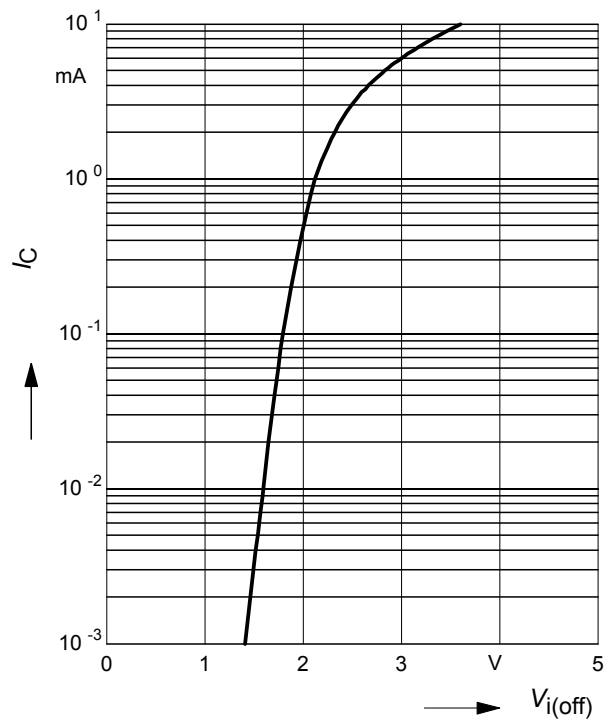
Input on Voltage $V_{i(on)} = f(I_C)$

$V_{CE} = 0.3\text{ V}$ (common emitter configuration)



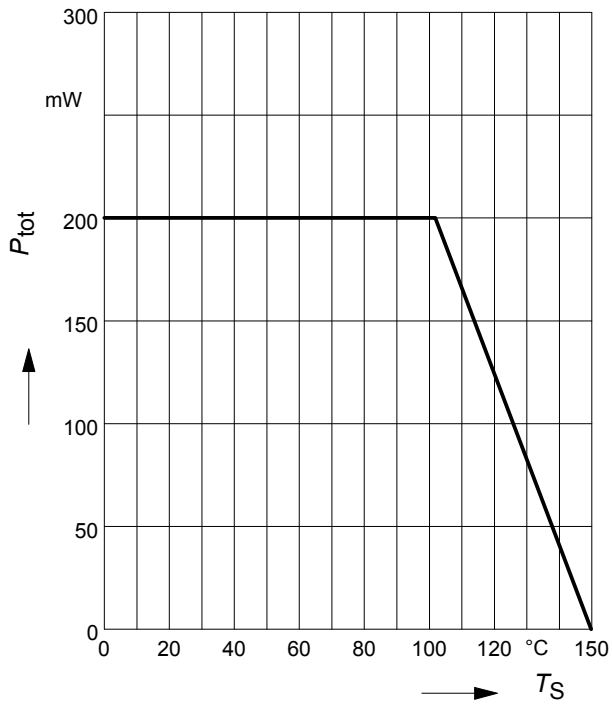
Input off voltage $V_{i(off)} = f(I_C)$

$V_{CE} = 5\text{ V}$ (common emitter configuration)



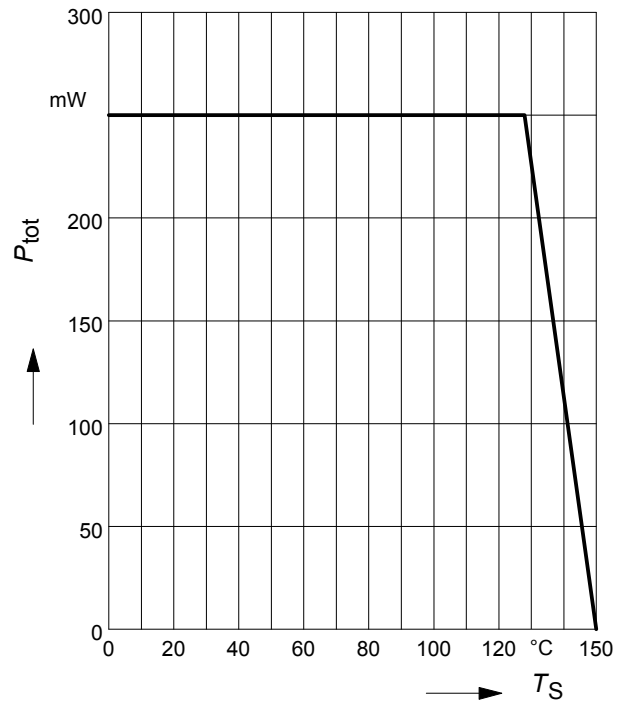
Total power dissipation $P_{tot} = f(T_S)$

BCR196



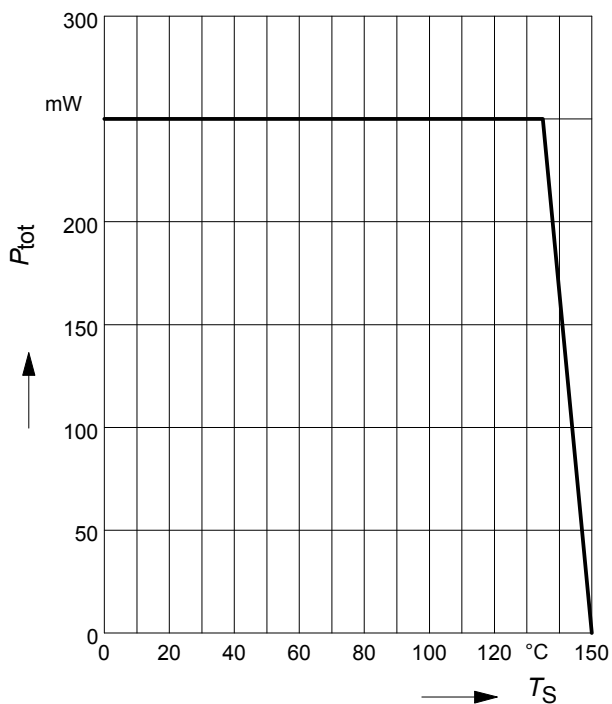
Total power dissipation $P_{tot} = f(T_S)$

BCR196F



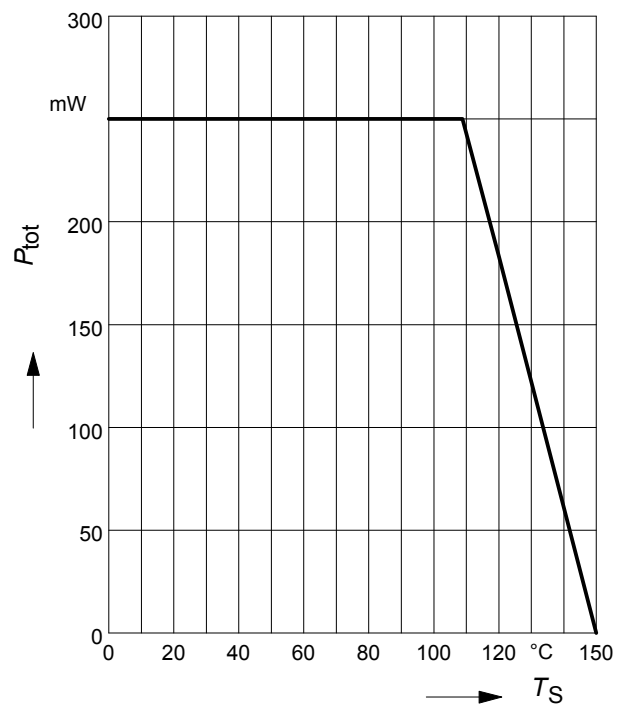
Total power dissipation $P_{tot} = f(T_S)$

BCR196L3



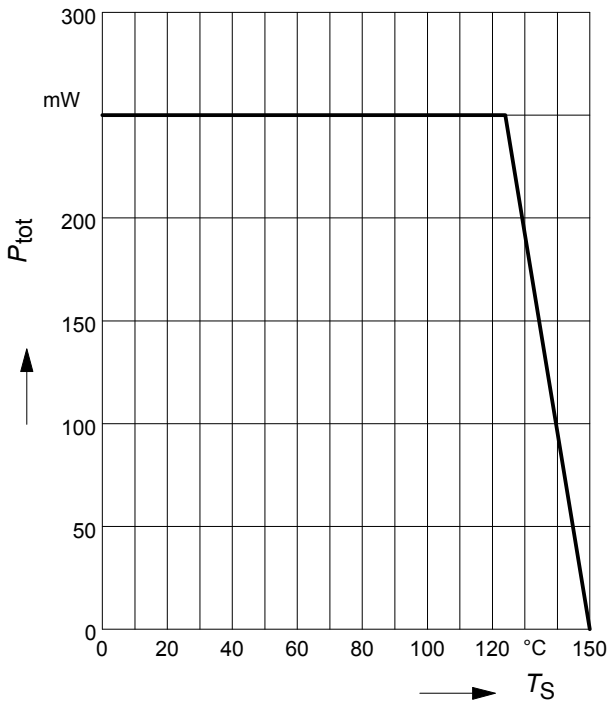
Total power dissipation $P_{tot} = f(T_S)$

BCR196T



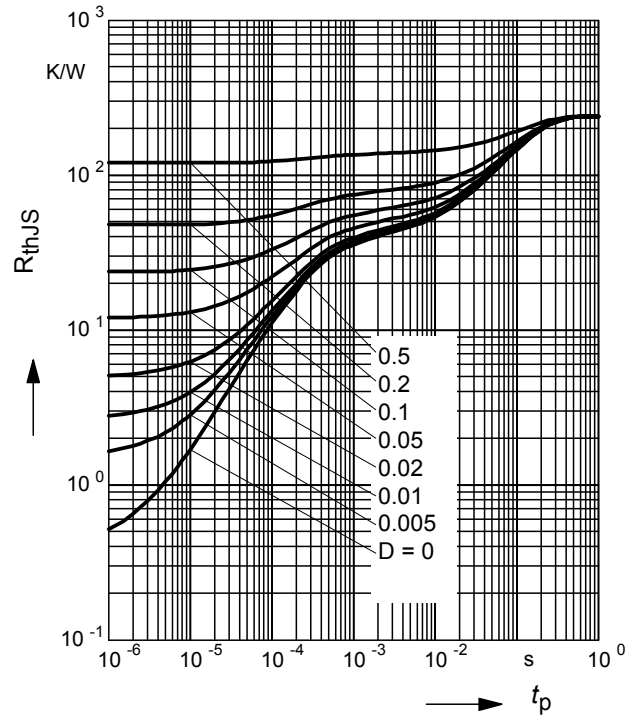
Total power dissipation $P_{tot} = f(T_S)$

BCR196W



Permissible Pulse Load $R_{thJS} = f(t_p)$

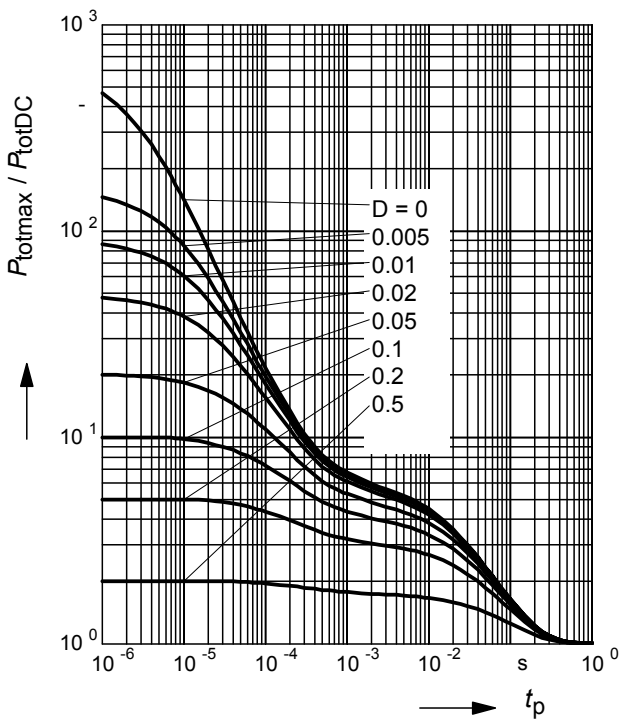
BCR196



Permissible Pulse Load

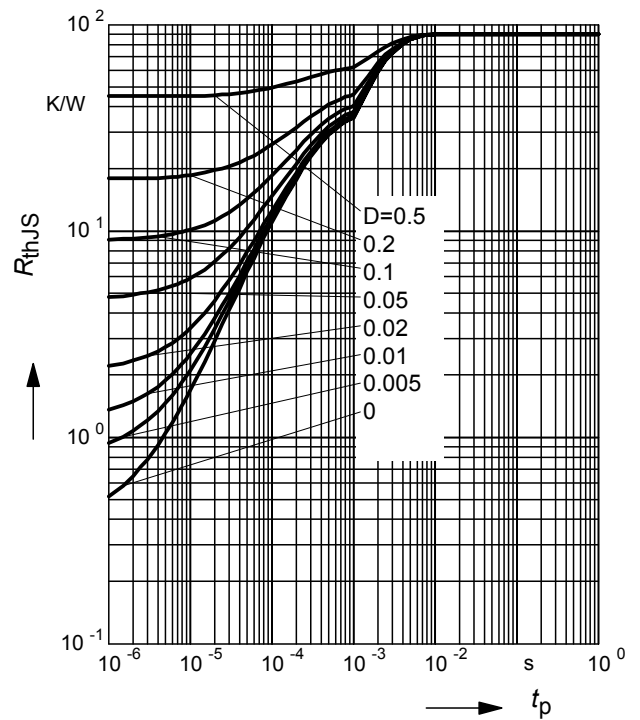
$P_{totmax}/P_{totDC} = f(t_p)$

BCR196



Permissible Puls Load $R_{thJS} = f(t_p)$

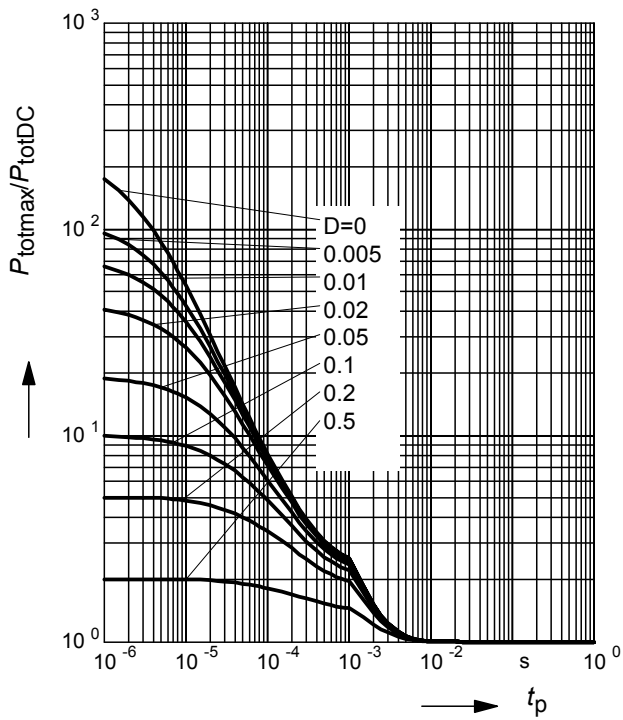
BCR196F



Permissible Pulse Load

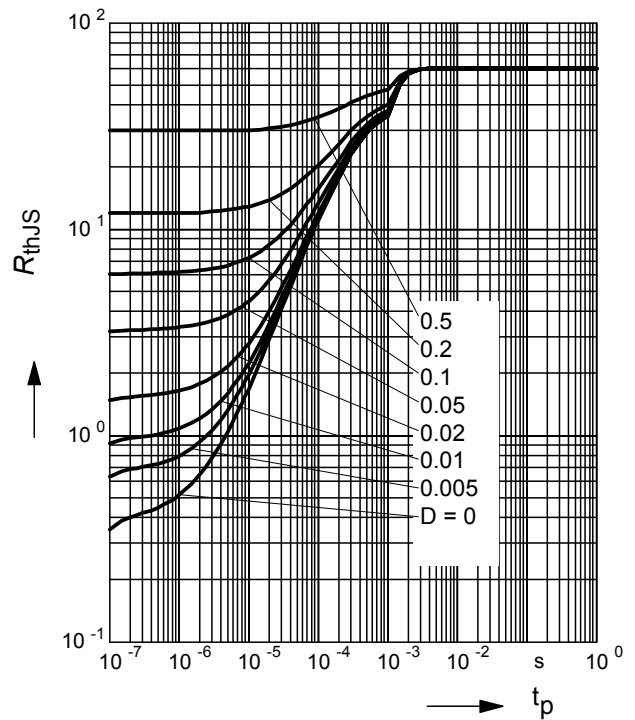
$$P_{\text{totmax}}/P_{\text{totDC}} = f(t_p)$$

BCR196F



Permissible Puls Load $R_{\text{thJS}} = f(t_p)$

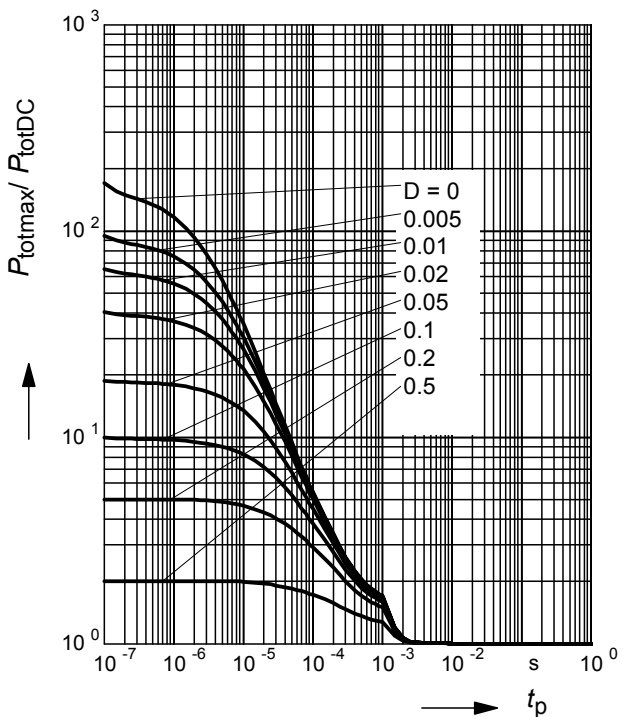
BCR196L3



Permissible Pulse Load

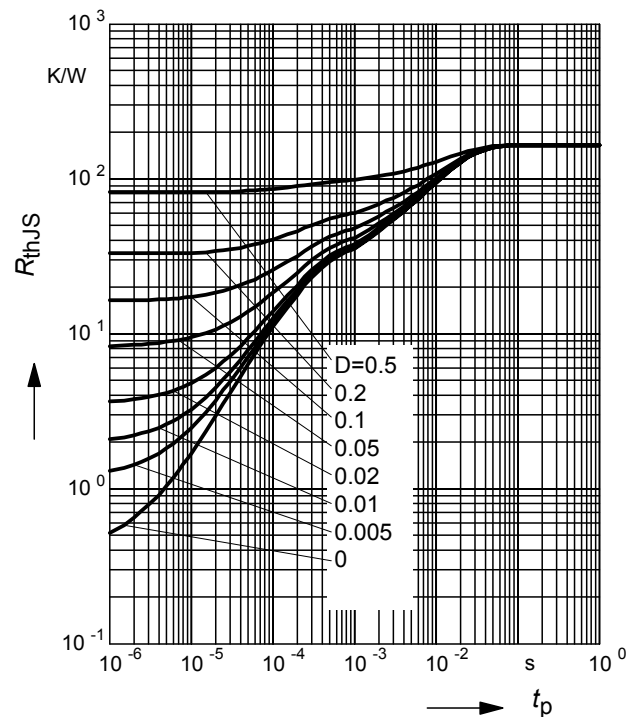
$$P_{\text{totmax}}/P_{\text{totDC}} = f(t_p)$$

BCR196L3



Permissible Puls Load $R_{\text{thJS}} = f(t_p)$

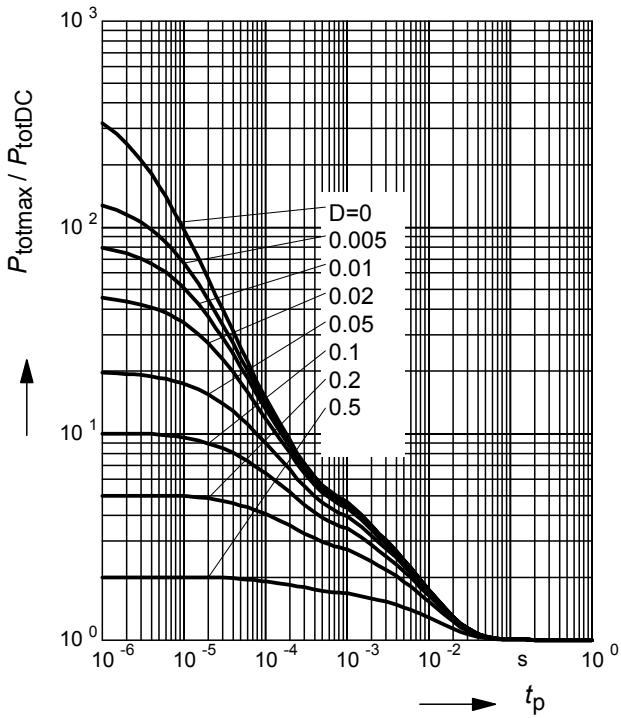
BCR196T



Permissible Pulse Load

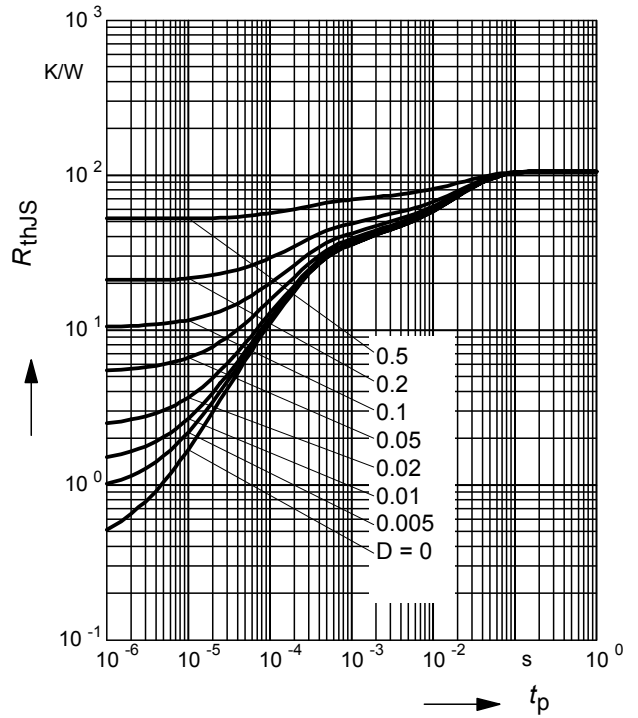
$$P_{\text{totmax}}/P_{\text{totDC}} = f(t_p)$$

BCR196T



Permissible Puls Load $R_{\text{thJS}} = f(t_p)$

BCR196W



Permissible Pulse Load

$$P_{\text{totmax}}/P_{\text{totDC}} = f(t_p)$$

BCR196W

