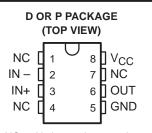
# TL714C HIGH-SPEED DIFFERENTIAL COMPARATOR

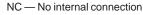
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- Operates From a 5-V Supply
- Self-Biasing Inputs
- Hysteresis ... 10 mV Typ
- Response Time . . . 6 ns Typ
- Maximum Operating Frequency 50 MHz Typ

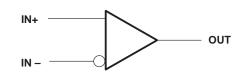
#### description

The TL714C is a high-speed differential comparator fabricated with bipolar Schottky process technology. The circuit has differential inputs and a TTL-compatible logic output with symmetrical switching characteristics.





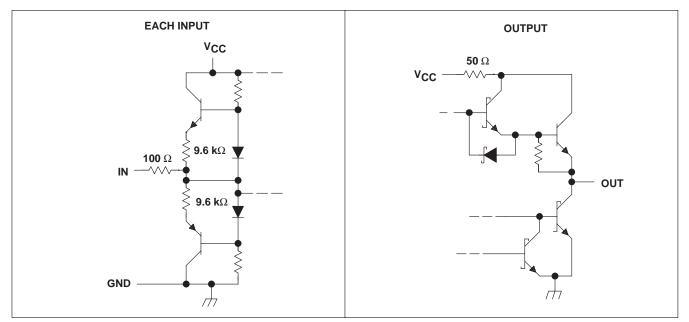




The device operates from a single 5-V supply and is useful as a disk-memory read-chain data comparator.

The TL714C is characterized for operation from 0°C to 70°C.

#### schematic of inputs and outputs



All resistor values shown are nominal.



# TL714C HIGH-SPEED DIFFERENTIAL COMPARATOR

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## absolute maximum ratings over operating free-air temperature range (unless otherwise noted)<sup>†</sup>

Supply voltage, V <sub>CC</sub> (see Note 1)	
Differential input voltage, VID (see Note 2)	± 5 V
Input voltage range, V <sub>I</sub>	
Low-level output current, I <sub>OL</sub>	40 mA
Continuous total power dissipation	See Dissipation Rating Table
Operating free-air temperature range, T <sub>A</sub>	0°C to 70°C
Storage temperature range	– 65°C to 150°C
Lead temperature1,6 mm (1/16 inch) from case for 10 seconds	2222

<sup>†</sup> Stresses beyond those listed under absolute maximum ratings may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the recommended operating conditions section of this specification is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

NOTES: 1. All voltage values, except differential voltage, are with respect to the network ground.

2. Differential voltage values are at IN+ with respect to IN -.

#### DISSIPATION RATING TABLE

PACKAGE	T <sub>A</sub> ≤ 25°C POWER RATING	DERATING FACTOR	DERATE ABOVE T <sub>A</sub>	T <sub>A</sub> = 75°C POWER RATING
D	500 mW	5.8 mW/°C	64°C	464 mW
P	500 mW	N/A	N/A	500 mW

### recommended operating conditions

		MIN	MAX	UNIT
Supply voltage, V <sub>CC</sub>		4.75	5.25	V
Common-mode input voltage, V <sub>IC</sub>	V	1.4 to CC - 1.4		V
High-level output current, I <sub>OH</sub>			- 1	mA
Low-level output current, IOL			16	mA
Operating free-air temperature, T <sub>A</sub>		0	70	°C

# electrical characteristics over free-air operating temperature range, $V_{CC}$ = 5 V (unless otherwise noted)

	PARAMETER	TEST CONDITIONS		MIN	TYP‡	MAX	UNIT
VT	Threshold voltage (V <sub>T+</sub> – V <sub>T</sub> –)	V <sub>IC</sub> = 1.4 V to 3.6 V		–75§		75	mV
V <sub>hys</sub>	Hysteresis ( $V_{T+} - V_{T-}$ )			2	10	30	mV
VOH	High-level output voltage	V <sub>ID</sub> = 100 mV,	I <sub>OH</sub> = - 1 mA	2.7	3.4		V
VOL	Low-level output voltage	$V_{ID} = -100 \text{ mV},$	I <sub>OL</sub> = 16 mA		0.4	0.5	V
IOS	Short-circuit output current			- 30		- 110	mA
rj	Differential input resistance			2.9			kΩ
ICC	Supply current	$V_{ID} = -100 \text{ mV},$	IO = 0		7	12	mA

<sup>‡</sup> All typical values are at  $T_A = 25^{\circ}C$ .

§ The algebraic convention, where the more negative limit is designated as minimum, is used in this data sheet for input threshold voltage levels only.



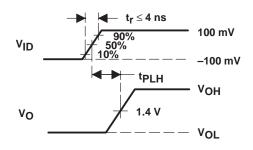
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## switching characteristics, $V_{CC} = 5 V$ , $T_A = 25^{\circ}C$

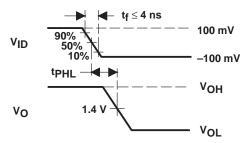
PARAMETER		TEST CONDITIONS		MIN	түр†	MAX	UNIT
f <sub>max</sub>	Maximum operating frequency	$V_{ID} = \pm 250 \text{ mV},$ $C_L = 25 \text{ pF},$	t <sub>r</sub> = t <sub>f</sub> = 4 ns, Input duty cycle = 50%		50		MHz
<sup>t</sup> PLH	Propagation delay time, low-to-high-level output	$V_{ID} = \pm 100 \text{ mV},$	CL = 25 pF,		6	12	ns
<sup>t</sup> PHL	Propagation delay time, high-to-low-level output	See Figures 1 and 2			6	12	ns
t <sub>r</sub>	Rise time	$V_{ID} = \pm 100 \text{ mV},$	C <sub>L</sub> = 25 pF,		4	8	ns
tf	Fall time	See Figure 3			4	8	ns

<sup>†</sup> All typical values are at  $T_A = 25^{\circ}C$ .

## PARAMETER MEASUREMENT INFORMATION



#### Figure 1. Propagation Delay Time, Low to High (t<sub>PLH</sub>)



### Figure 2. Propagation Delay Time, High to Low (t<sub>PHL</sub>)

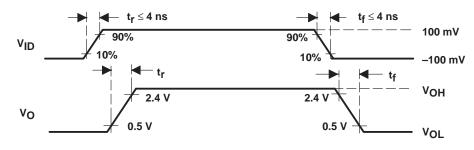


Figure 3. Rise and Fall Times (tr, tf)



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