

FY8ACH-02A

HIGH-SPEED SWITCHING USE

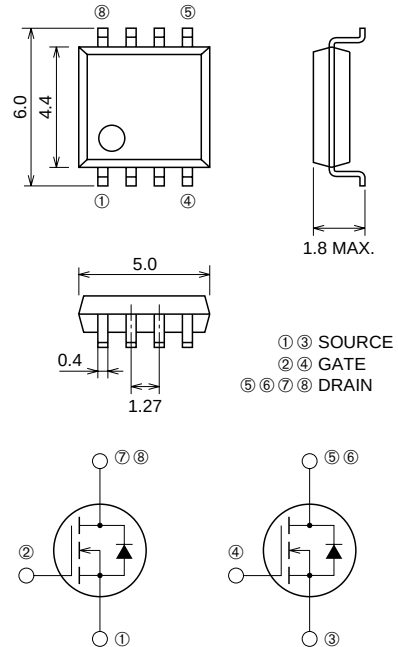
FY8ACH-02A



- 2.5V DRIVE
- V_{DSS} 20V
- $r_{DS(ON)}$ (MAX) 22m Ω
- I_D 8A

OUTLINE DRAWING

Dimensions in mm



SOP-8

APPLICATION

Motor control, Lamp control, Solenoid control
DC-DC converter, etc.

MAXIMUM RATINGS (Tc = 25°C)

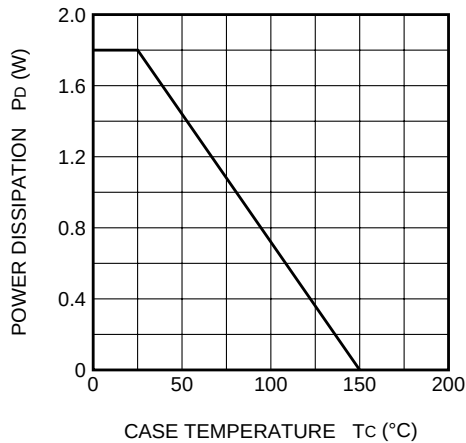
Symbol	Parameter	Conditions	Ratings	Unit
V_{DSS}	Drain-source voltage	$V_{GS} = 0V$	20	V
V_{GSS}	Gate-source voltage	$V_{DS} = 0V$	± 10	V
I_D	Drain current		8	A
I_{DM}	Drain current (Pulsed)		56	A
I_{DA}	Avalanche drain current (Pulsed)	$L = 10\mu H$	8	A
I_S	Source current		1.7	A
I_{SM}	Source current (Pulsed)		6.8	A
P_D	Maximum power dissipation		1.8	W
T_{ch}	Channel temperature		-55 ~ +150	°C
T_{stg}	Storage temperature		-55 ~ +150	°C
—	Weight	Typical value	0.07	g

ELECTRICAL CHARACTERISTICS (T_{ch} = 25°C)

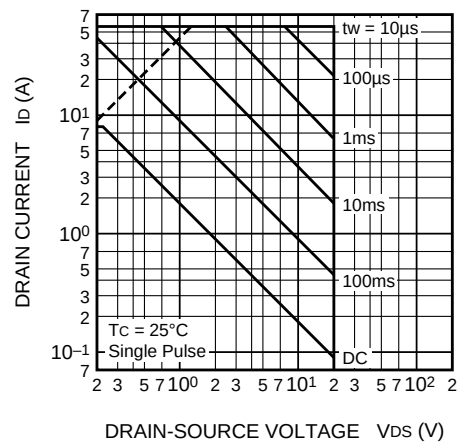
Symbol	Parameter	Test conditions	Limits			Unit
			Min.	Typ.	Max.	
V _{(BR) DSS}	Drain-source breakdown voltage	I _D = 1mA, V _{GS} = 0V	20	—	—	V
I _{GSS}	Gate-source leakage current	V _{GS} = ±10V, V _{DS} = 0V	—	—	±0.1	μA
I _{DSS}	Drain-source leakage current	V _{DS} = 20V, V _{GS} = 0V	—	—	0.1	mA
V _{GS(th)}	Gate-source threshold voltage	I _D = 1mA, V _{DS} = 10V	0.5	0.9	1.3	V
r _{DS(on)}	Drain-source on-state resistance	I _D = 8A, V _{GS} = 4V	—	17	22	mΩ
r _{DS(on)}	Drain-source on-state resistance	I _D = 4A, V _{GS} = 2.5V	—	23	36	mΩ
V _{DS(on)}	Drain-source on-state voltage	I _D = 8A, V _{GS} = 4V	—	0.140	0.176	V
y _{fs}	Forward transfer admittance	I _D = 8A, V _{DS} = 10V	—	22	—	S
C _{iss}	Input capacitance	V _{DS} = 10V, V _{GS} = 0V, f = 1MHz	—	1700	—	pF
C _{oss}	Output capacitance		—	510	—	pF
C _{rss}	Reverse transfer capacitance		—	360	—	pF
t _{d(on)}	Turn-on delay time	V _{DD} = 10V, I _D = 4A, V _{GS} = 4V, R _{GEN} = R _{GS} = 50Ω	—	26	—	ns
t _r	Rise time		—	85	—	ns
t _{d(off)}	Turn-off delay time		—	190	—	ns
t _f	Fall time		—	180	—	ns
V _{SD}	Source-drain voltage	I _S = 1.7A, V _{GS} = 0V	—	0.75	1.1	V
R _{th(ch-a)}	Thermal resistance	Channel to ambient	—	—	69.4	°C/W
t _{rr}	Reverse recovery time	I _S = 1.7A, di _s /dt = -50A/μs	—	100	—	ns

PERFORMANCE CURVES

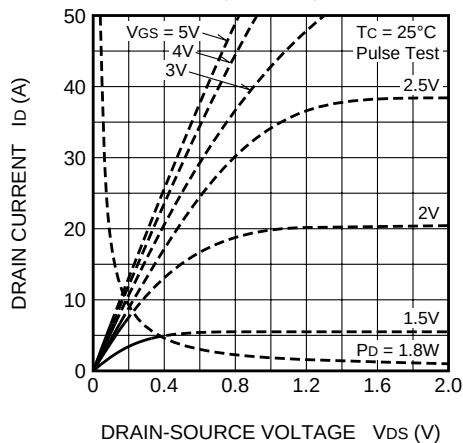
POWER DISSIPATION DERATING CURVE



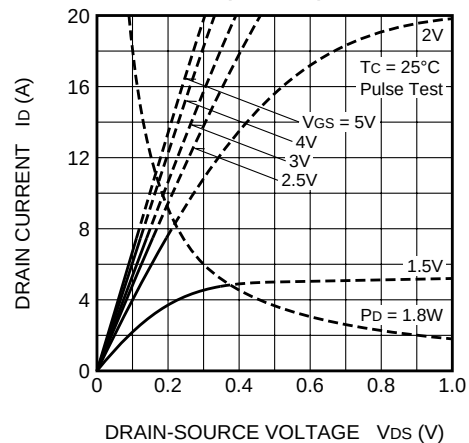
MAXIMUM SAFE OPERATING AREA

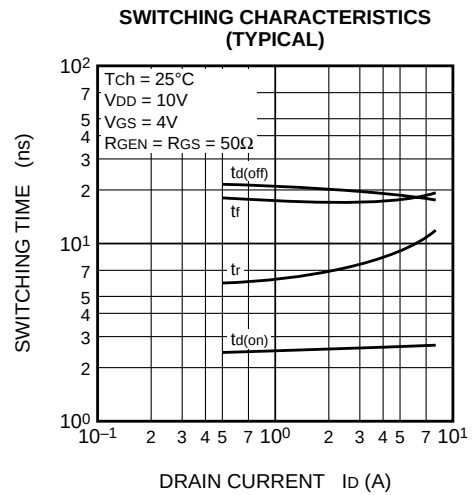
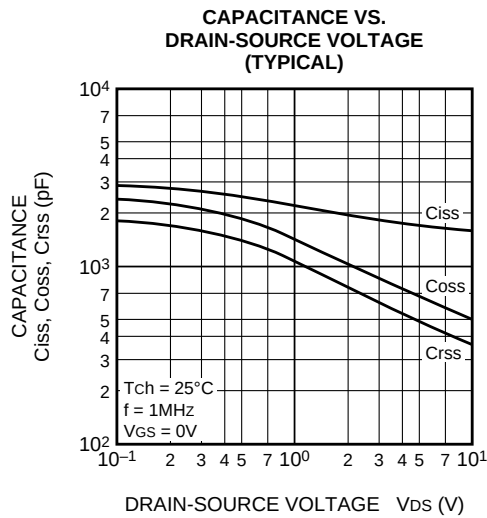
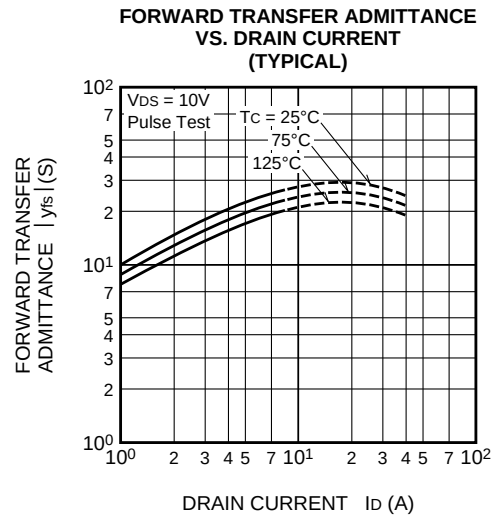
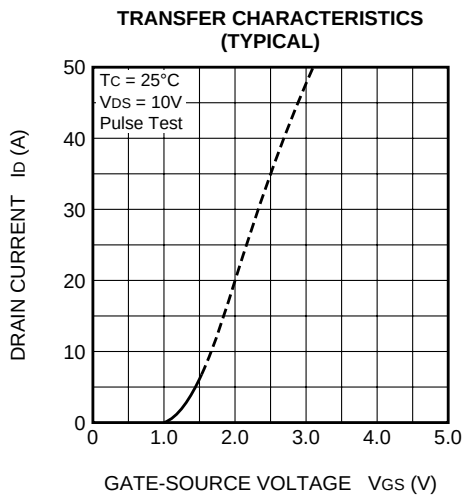
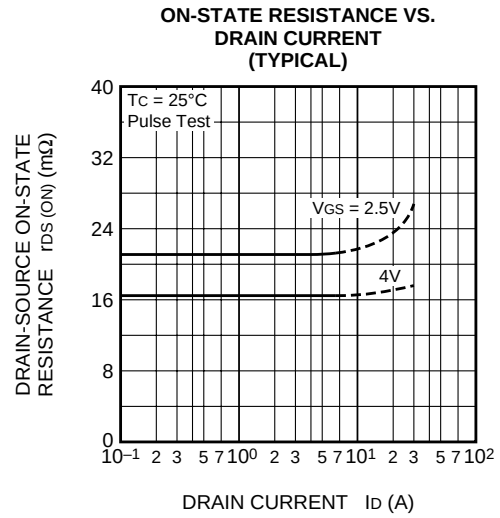
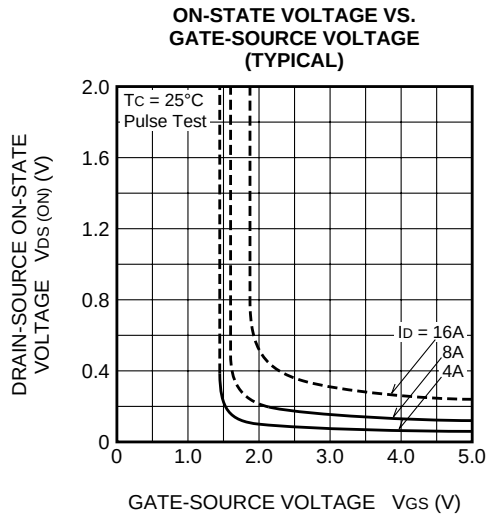


OUTPUT CHARACTERISTICS (TYPICAL)

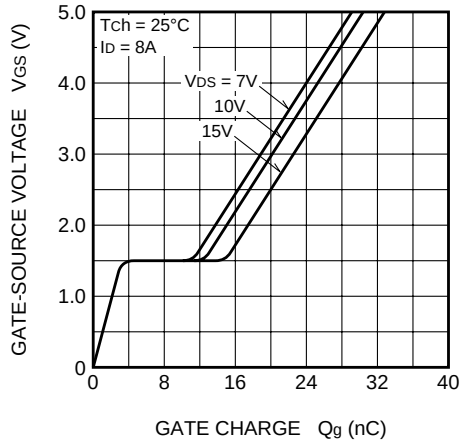


OUTPUT CHARACTERISTICS (TYPICAL)

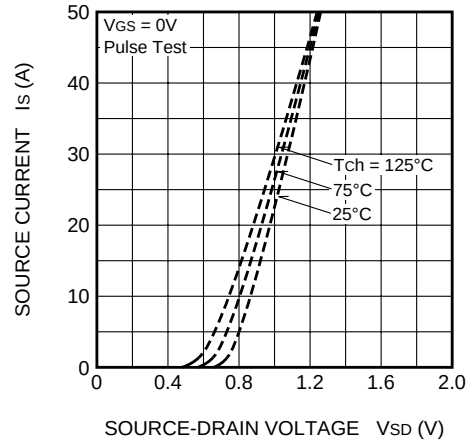




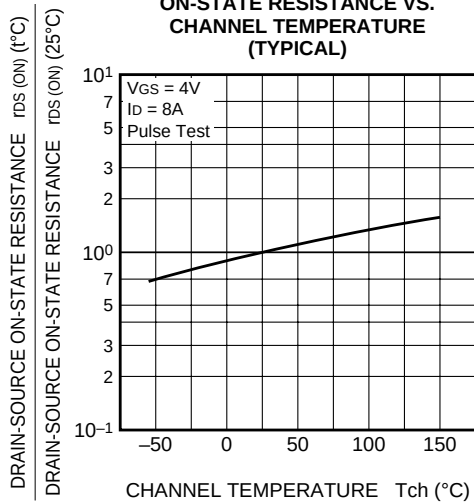
GATE-SOURCE VOLTAGE
VS. GATE CHARGE
(TYPICAL)



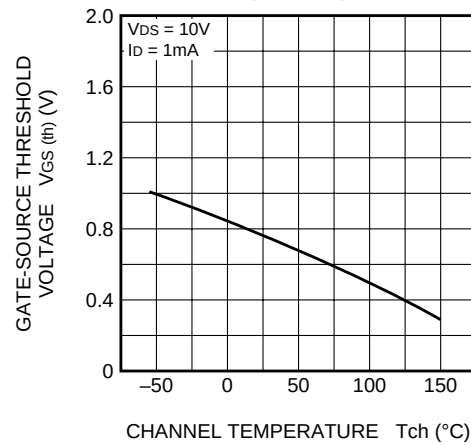
SOURCE-DRAIN DIODE
FORWARD CHARACTERISTICS
(TYPICAL)



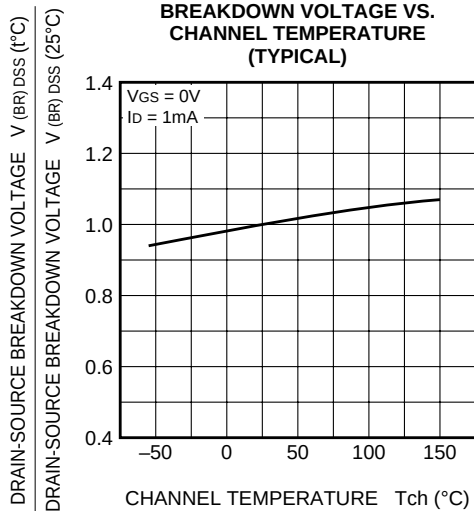
ON-STATE RESISTANCE VS.
CHANNEL TEMPERATURE
(TYPICAL)



THRESHOLD VOLTAGE VS.
CHANNEL TEMPERATURE
(TYPICAL)



BREAKDOWN VOLTAGE VS.
CHANNEL TEMPERATURE
(TYPICAL)



TRANSIENT THERMAL IMPEDANCE
CHARACTERISTICS

