

### DTC143T

### NPN SILICON TRANSISTOR

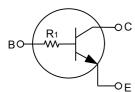
## NPN DIGITAL TRANSISTOR (BUILT- IN BIAS RESISTORS)

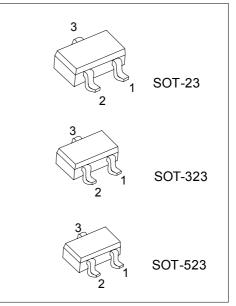
#### FEATURES

\* Built-in bias resistors that implies easy ON/OFF applications.

\* The bias resistors are thin-film resistors with complete isolation to allow negative input.

#### EQUIVALENT CIRCUIT





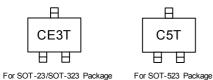
\* Pb-free plating product number: DTC143TL

#### ORDERING INFORMATION

Order Number		Daakaga	Pin Assignment			Booking	
Normal	Lead Free Plating	Package	1	2	3	Packing	
DTC143T-AE3-R	DTC143TL-AE3-R	SOT-23	Е	В	С	Tape Reel	
DTC143T-AL3-R	DTC143TL-AL3-R	SOT-323	Е	В	С	Tape Reel	
DTC143T-AN3-R	DTC143TL-AN3-R	SOT-523	Е	В	С	Tape Reel	

DTC143TL- <u>AE3-R</u> (1)Pack	ng Type (1) R: Ta	ipe Reel			
(2)Pack	age Type (2) AE3:	SOT-23, AL3: SOT-323, AN3: SOT-523			
(3)Lead	Plating (3) L: Le	(3) L: Lead Free Plating, Blank: Pb/Sn			

#### MARKING



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#### ■ ABSOLUTE MAXIMUM RATINGS (Ta=25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT	
Collector-Base Voltage		V <sub>CBO</sub>	50	V	
Collector-Emitter Voltage		$V_{CEO}$	50	V	
Emitter-Base Voltage		V <sub>EBO</sub>	5	V	
Collector Current		Ιc	100	mA	
	SOT-523		150	mW	
Collector Power Dissipation	SOT-23/SOT-323	P <sub>C</sub>	200	mW	
Junction Temperature		TJ	+150		
Storage Temperature		T <sub>STG</sub>	-55~+150		

Note Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

#### ELECTRICAL CHARACTERISTICS (Ta=25°C, unless otherwise specified.)

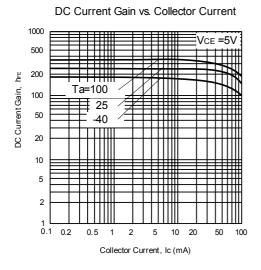
SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
BV <sub>CBO</sub>	I <sub>C</sub> =50μA	50			V
BV <sub>CEO</sub>	I <sub>C</sub> =1mA	50			V
BV <sub>EBO</sub>	I <sub>E</sub> =50μA	5			V
I <sub>CBO</sub>	V <sub>CB</sub> =50V			0.5	μA
I <sub>EBO</sub>	V <sub>EB</sub> =4V			0.5	μA
V <sub>CE(SAT)</sub>	I <sub>C</sub> =5mA, I <sub>B</sub> =0.25mA			0.3	V
h <sub>FE</sub>	V <sub>CE</sub> =5V, I <sub>C</sub> =1mA	100	250	600	
R <sub>1</sub>		3.29	4.7	6.11	kΩ
f⊤	V <sub>CE</sub> =10V, I <sub>E</sub> =5mA, f=100MHz *		250		MHz
	BV <sub>CBO</sub> BV <sub>EBO</sub> I <sub>CBO</sub> I <sub>CBO</sub> V <sub>CE(SAT)</sub> h <sub>FE</sub> R <sub>1</sub>	$\begin{array}{c c} BV_{CBO} & I_{C} = 50 \mu A \\ BV_{CEO} & I_{C} = 1 m A \\ BV_{EBO} & I_{E} = 50 \mu A \\ I_{CBO} & V_{CB} = 50 V \\ I_{EBO} & V_{EB} = 4 V \\ V_{CE(SAT)} & I_{C} = 5 m A, I_{B} = 0.25 m A \\ h_{FE} & V_{CE} = 5 V, I_{C} = 1 m A \\ R_{1} \end{array}$	$\begin{array}{c c c c c c c c c c c c c c c c c c c $	$\begin{array}{c c c c c c c c c c c c c c c c c c c $	$\begin{array}{c c c c c c c c c c c c c c c c c c c $

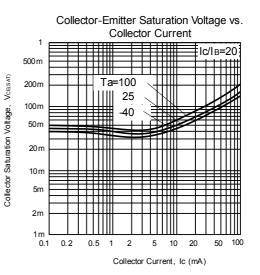
\* Transition frequency of the device.



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#### TYPICAL CHARACTERISTICS





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