

## **STP80NF12**

N-channel 120V -  $0.013\Omega$  - 80A - TO-220 STripFET<sup>TM</sup> II Power MOSFET

### **General features**

Туре	V <sub>DSS</sub> (@Tjmax)	ax) DS(on)	
STP80NF12	120V	<0.018Ω	80A <sup>(1)</sup>

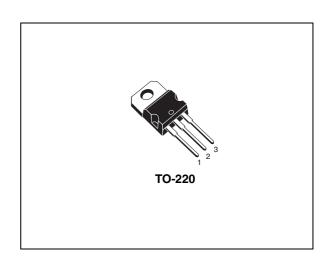
- Exceptional dv/dt capability
- 100% avalanche tested
- Application oriented characterization

### **Description**

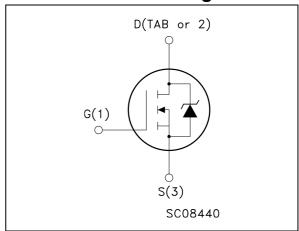
This MOSFET series realized with STMicroelectronics unique STripFET process has specifically been designed to minimize input capacitance and gate charge. It is therefore suitable as primary switch in advanced highefficiency, high-frequency isolated DC-DC converters for Telecom and Computer applications. It is also intended for any applications with low gate drive requirements.

## **Applications**

■ Switching application



### Internal schematic diagram



#### Order codes

Part number	Marking	Package	Packaging
STP80NF12	P80NF12	TO-220	Tube

Contents STP80NF12

# **Contents**

1	Electrical ratings	3
2	Electrical characteristics	4
	2.1 Electrical characteristics (curves)	6
3	Test circuit	8
4	Package mechanical data	9
5	Revision history	11

STP80NF12 Electrical ratings

# 1 Electrical ratings

Table 1. Absolute maximum ratings

Symbol	Parameter	Valu	е	Unit
Symbol	Faranteter	STB_P_W80NF12	STP80NF12FP	Ollit
V <sub>DS</sub>	Drain-source voltage (V <sub>GS</sub> = 0)	120	٧	
V <sub>DGR</sub>	Drain-gate voltage ( $R_{GS} = 20K\Omega$ ) 120		V	
V <sub>GS</sub>	Gate-source voltage ± 22		V	
I <sub>D</sub> <sup>(1)</sup>	Drain current (continuous) at T <sub>C</sub> = 25°C	80	11 <sup>(2)</sup>	Α
I <sub>D</sub>	Drain current (continuous) at T <sub>C</sub> =100°C	60	60 <sup>(2)</sup>	Α
I <sub>DM</sub> <sup>(3)</sup>	Drain current (pulsed)	320	320 <sup>(2)</sup>	Α
P <sub>TOT</sub>	Total dissipation at T <sub>C</sub> = 25°C	300	45	W
	Derating factor	2.0	0.3	W/°C
dv/dt <sup>(4)</sup>	Peak diode recovery voltage slope	10		V/ns
V <sub>ISO</sub>	Insulation withstand voltage (DC)	2500		٧
T <sub>J</sub> T <sub>stg</sub>	Operating junction temperature Storage temperature	-55 to 175		°C

- 1. Limited by Package
- 2. Limited only by maximum temperature allowed
- 3. Pulse width limited by safe operating area
- 4. Starting  $T_J = 25$  °C,  $I_D = 40A$ ,  $V_{DD} = 45V$

Table 2. Thermal data

		Value			
Symbol	Parameter TO-247 D <sup>2</sup> PAK TO-220 TO-220				
R <sub>thJC</sub>	Thermal resistance junction-case Max	0.5	0.5	3.33	°C/W
R <sub>thJA</sub>	Thermal resistance junction-ambient Max	50	62.5	62.5	°C/W
T <sub>I</sub>	Maximum lead temperature for soldering purpose	300	300	300	°C

Electrical characteristics STP80NF12

# 2 Electrical characteristics

(T<sub>CASE</sub>=25°C unless otherwise specified)

Table 3. On/off states

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
V <sub>(BR)DSS</sub>	Drain-source breakdown voltage	$I_D = 250 \mu A, V_{GS} = 0$	120			٧
I <sub>DSS</sub>	Zero gate voltage drain current (V <sub>GS</sub> = 0)	$V_{DS} = Max rating,$ $V_{DS} = Max rating @ 125°C$			1 10	μ <b>Α</b> μ <b>Α</b>
I <sub>GSS</sub>	Gate body leakage current (V <sub>DS</sub> = 0)	V <sub>GS</sub> = ±20V			±100	nA
V <sub>GS(th)</sub>	Gate threshold voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	2			V
R <sub>DS(on)</sub>	Static drain-source on resistance	V <sub>GS</sub> = 10V, I <sub>D</sub> = 40A		0.013	0.018	Ω

Table 4. Dynamic

Symbol	Parameter	Parameter Test conditions		Тур.	Max.	Unit
g <sub>fs</sub> (1)	Forward transconductance	V <sub>DS</sub> =15V, I <sub>D</sub> = 40A		80		S
C <sub>iss</sub> C <sub>oss</sub> C <sub>rss</sub>	Input capacitance Output capacitance Reverse transfer capacitance	V <sub>DS</sub> =25V, f=1 MHz, V <sub>GS</sub> =0		4300 600 230		pF pF pF
Q <sub>g</sub> Q <sub>gs</sub> Q <sub>gd</sub>	Total gate charge Gate-source charge Gate-drain charge	$V_{DD} = 80V, I_{D} = 80A$ $V_{GS} = 10V$		140 23 51	189	nC nC nC

<sup>1.</sup> Pulsed: pulse duration=300µs, duty cycle 1.5%

Table 5. Switching times

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
$\begin{array}{c} t_{d(on)} \\ t_{r} \\ t_{d(off)} \\ t_{f} \end{array}$	Turn-on delay time Rise time Turn-off delay time Fall time	$V_{DD}$ = 50 V, $I_{D}$ = 40A, $R_{G}$ =4.7 $\Omega$ , $V_{GS}$ =10V Figure 13 on page 8		40 145 134 115		ns ns ns

Table 6. Source drain diode

Symbol	Parameter	Test conditions	Min.	Тур.	Max	Unit
I <sub>SD</sub>	Source-drain current				80	Α
I <sub>SDM</sub> <sup>(1)</sup>	Source-drain current (pulsed)				320	Α
V <sub>SD</sub> <sup>(2)</sup>	Forward on voltage	I <sub>SD</sub> =80A, V <sub>GS</sub> =0			1.3	V
t <sub>rr</sub> Q <sub>rr</sub> I <sub>RRM</sub>	Reverse recovery time Reverse recovery charge Reverse recovery current	$I_{SD}$ =80A, di/dt = 100A/µs, $V_{DD}$ =35V, $T_{J}$ = 150°C		155 0.85 11		ns μC A

<sup>1.</sup> Pulse width limited by safe operating area

<sup>2.</sup> Pulsed: pulse duration=300 $\mu$ s, duty cycle 1.5%

Electrical characteristics STP80NF12

## 2.1 Electrical characteristics (curves)

Figure 1. Safe operating area

Figure 2. Thermal impedance

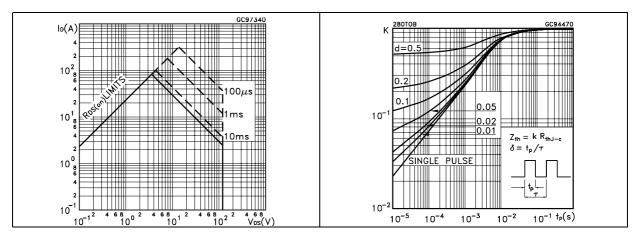


Figure 3. Output characteristics

Figure 4. Transfer characteristics

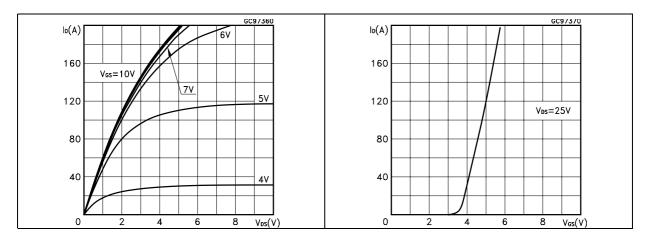
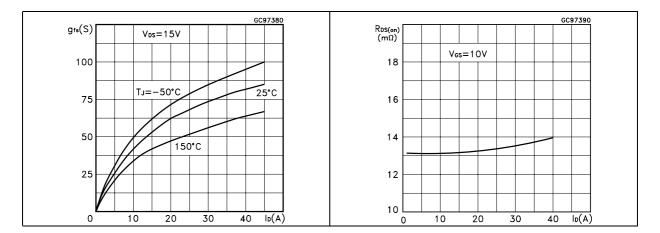


Figure 5. Transconductance

Figure 6. Static drain-source on resistance



6/12

Figure 7. Gate charge vs. gate-source voltage Figure 8. Capacitance variations

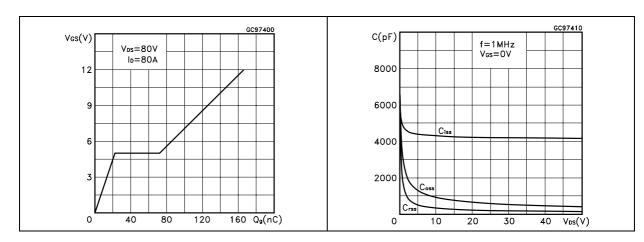


Figure 9. Normalized gate threshold voltage vs. temperature

Figure 10. Normalized on resistance vs. temperature

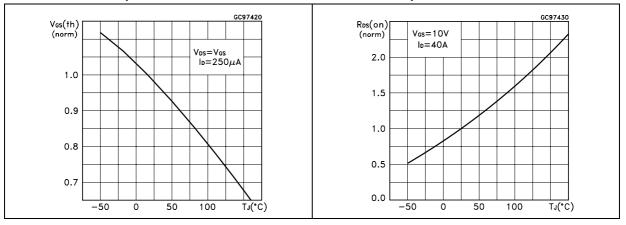
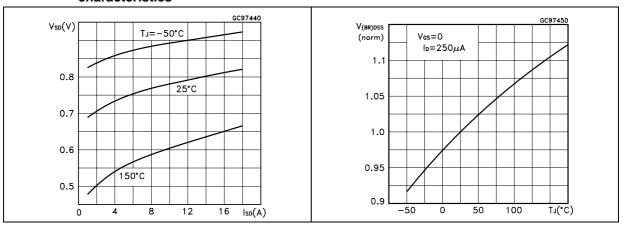


Figure 11. Source-drain diode forward characteristics

Figure 12. Normalized B<sub>VDSS</sub> vs. temperature



**577** 

Test circuit STP80NF12

# 3 Test circuit

Figure 13. Switching times test circuit for resistive load

Figure 14. Gate charge test circuit

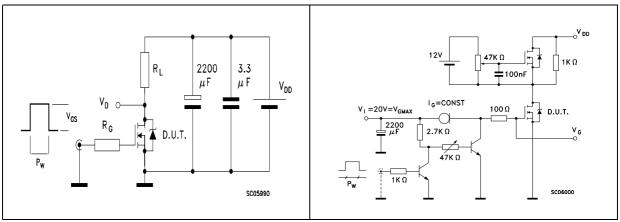


Figure 15. Test circuit for inductive load switching and diode recovery times

Figure 16. Unclamped Inductive load test circuit

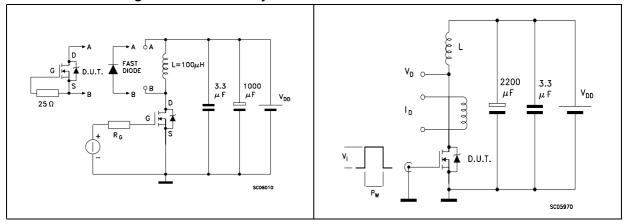
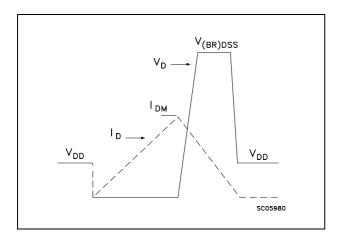


Figure 17. Unclamped inductive waveform



**577** 

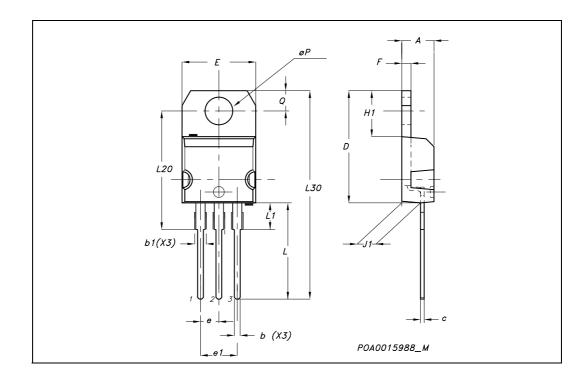
# 4 Package mechanical data

In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a Lead-free second level interconnect. The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: www.st.com

**\_**y/

### **TO-220 MECHANICAL DATA**

DIM		mm.			inch	
DIM.	MIN.	TYP	MAX.	MIN.	TYP.	MAX.
Α	4.40		4.60	0.173		0.181
b	0.61		0.88	0.024		0.034
b1	1.15		1.70	0.045		0.066
С	0.49		0.70	0.019		0.027
D	15.25		15.75	0.60		0.620
Е	10		10.40	0.393		0.409
е	2.40		2.70	0.094		0.106
e1	4.95		5.15	0.194		0.202
F	1.23		1.32	0.048		0.052
H1	6.20		6.60	0.244		0.256
J1	2.40		2.72	0.094		0.107
L	13		14	0.511		0.551
L1	3.50		3.93	0.137		0.154
L20		16.40			0.645	
L30		28.90			1.137	
øΡ	3.75		3.85	0.147		0.151
Q	2.65		2.95	0.104		0.116



STP80NF12 Revision history

# 5 Revision history

Table 7. Revision history

Date	Revision	Changes
21-Jun-2004	2	Preliminary version
24-Jul-2006	3	The document has been reformatted, SOA updated
31-Jan-2007	4	Typo mistake on Table 1.

11/12

#### Please Read Carefully:

Information in this document is provided solely in connection with ST products. STMicroelectronics NV and its subsidiaries ("ST") reserve the right to make changes, corrections, modifications or improvements, to this document, and the products and services described herein at any time, without notice.

All ST products are sold pursuant to ST's terms and conditions of sale.

Purchasers are solely responsible for the choice, selection and use of the ST products and services described herein, and ST assumes no liability whatsoever relating to the choice, selection or use of the ST products and services described herein.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted under this document. If any part of this document refers to any third party products or services it shall not be deemed a license grant by ST for the use of such third party products or services, or any intellectual property contained therein or considered as a warranty covering the use in any manner whatsoever of such third party products or services or any intellectual property contained therein.

UNLESS OTHERWISE SET FORTH IN ST'S TERMS AND CONDITIONS OF SALE ST DISCLAIMS ANY EXPRESS OR IMPLIED WARRANTY WITH RESPECT TO THE USE AND/OR SALE OF ST PRODUCTS INCLUDING WITHOUT LIMITATION IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION), OR INFRINGEMENT OF ANY PATENT, COPYRIGHT OR OTHER INTELLECTUAL PROPERTY RIGHT.

UNLESS EXPRESSLY APPROVED IN WRITING BY AN AUTHORIZED ST REPRESENTATIVE, ST PRODUCTS ARE NOT RECOMMENDED, AUTHORIZED OR WARRANTED FOR USE IN MILITARY, AIR CRAFT, SPACE, LIFE SAVING, OR LIFE SUSTAINING APPLICATIONS, NOR IN PRODUCTS OR SYSTEMS WHERE FAILURE OR MALFUNCTION MAY RESULT IN PERSONAL INJURY, DEATH, OR SEVERE PROPERTY OR ENVIRONMENTAL DAMAGE. ST PRODUCTS WHICH ARE NOT SPECIFIED AS "AUTOMOTIVE GRADE" MAY ONLY BE USED IN AUTOMOTIVE APPLICATIONS AT USER'S OWN RISK.

Resale of ST products with provisions different from the statements and/or technical features set forth in this document shall immediately void any warranty granted by ST for the ST product or service described herein and shall not create or extend in any manner whatsoever, any liability of ST.

ST and the ST logo are trademarks or registered trademarks of ST in various countries.

Information in this document supersedes and replaces all information previously supplied.

The ST logo is a registered trademark of STMicroelectronics. All other names are the property of their respective owners.

© 2007 STMicroelectronics - All rights reserved

STMicroelectronics group of companies

Australia - Belgium - Brazil - Canada - China - Czech Republic - Finland - France - Germany - Hong Kong - India - Israel - Italy - Japan - Malaysia - Malta - Morocco - Singapore - Spain - Sweden - Switzerland - United Kingdom - United States of America

www.st.com

47/