

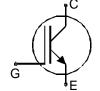
#### IGBT Chip in NPT-technology

#### **FEATURES:**

- 1200V NPT technology 200µm chip
- low turn-off losses
- short tail current
- positive temperature coefficient
- easy paralleling
- integrated gate resistor

#### This chip is used for:

power module BSM 75GD120DN2



#### **Applications:**

• drives

Chip Type	V <sub>CE</sub>	I <sub>Cn</sub>	Die Size	Package	Ordering Code
SIGC121T120R2C	1200V	75A	11.08 X 11.08 mm <sup>2</sup>	sawn on foil	Q67041-
0.00.1220.120					A4682-A003

#### **MECHANICAL PARAMETER:**

Raster size	11.08 X 11.08		
Emitter pad size	8 x ( 2.99 x 1.97 )		
Gate pad size	1.46 x 0.8		
Area total / active	122.8 / 99.6		
Thickness	200	μm	
Wafer size	150	mm	
Flat position	90	grd	
Max.possible chips per wafer	106 pcs		
Passivation frontside	Photoimide		
Emitter metallization	3200 nm Al Si 1%		
Collector metallization	1400 nm Ni Ag –system suitable for epoxy and soft solder die bonding		
Die bond	electrically conductive glue or solder		
Wire bond	Al, <500μm		
Reject Ink Dot Size	Ø 0.65mm; max 1.2mm		
Recommended Storage Environment	store in original container, in dry nitrogen, < 6 month at an ambient temperature of 23°C		



#### **MAXIMUM RATINGS:**

Parameter	Symbol	Value	Unit
Collector-emitter voltage, T <sub>j</sub> =25 °C	V <sub>CE</sub>	1200	V
DC collector current, limited by T <sub>jmax</sub>	I <sub>C</sub>	1)	А
Pulsed collector current, t <sub>p</sub> limited by T <sub>jmax</sub>	I <sub>cpuls</sub>	225	Α
Gate emitter voltage	V <sub>GE</sub>	±20	V
Operating junction and storage temperature	$T_j$ , $T_{stg}$	-55 <b>+</b> 150	°C

<sup>1)</sup> depending on thermal properties of assembly

#### STATIC CHARACTERISTICS (tested on chip), $T_i$ =25 °C, unless otherwise specified:

Parameter	Symbol	Conditions	Value			Unit
- unumeter			min.	typ.	max.	
Collector-emitter breakdown voltage	V <sub>(BR)CES</sub>	V <sub>GE</sub> =0V , I <sub>C</sub> =4mA	1200			
Collector-emitter saturation voltage	V <sub>CE(sat)</sub>	V <sub>GE</sub> =15V, I <sub>C</sub> =75A	2.0	2.5	3.0	V
Gate-emitter threshold voltage	$V_{\rm GE(th)}$	$I_C=3mA$ , $V_{GE}=V_{CE}$	4.5	5.5	6.5	
Zero gate voltage collector current	I <sub>CES</sub>	V <sub>CE</sub> =1200V , V <sub>GE</sub> =0V			9.2	μA
Gate-emitter leakage current	I <sub>GES</sub>	V <sub>CE</sub> =0V , V <sub>GE</sub> =20V			480	nA
Integrated gate resistor	R <sub>Gint</sub>			5		Ω

#### **ELECTRICAL CHARACTERISTICS** (tested at component):

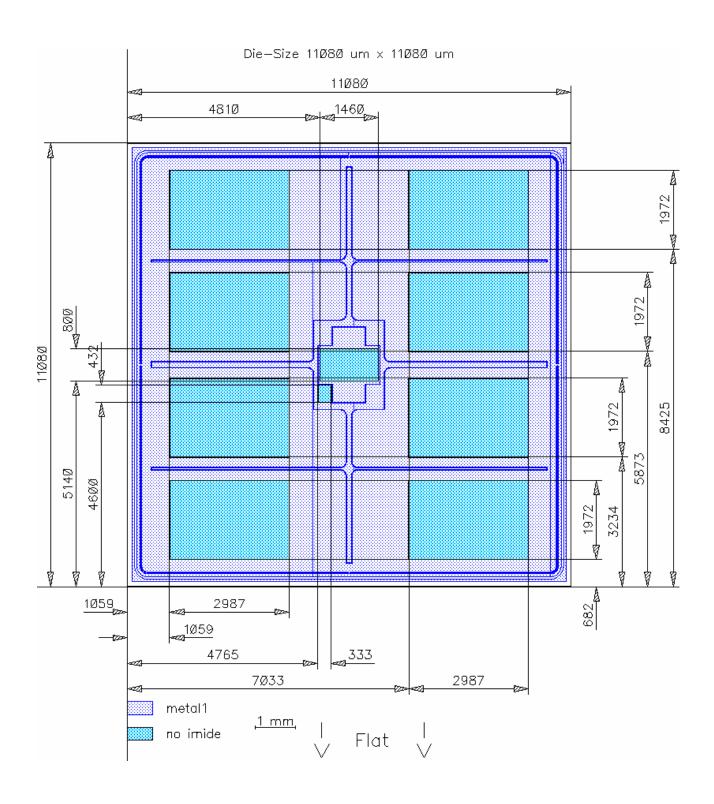
Parameter	Symbol Conditions	Conditions	Value			Unit
raiametei	Symbol	Conditions	min.	typ.	max.	Onne
Input capacitance	Ciss	V <sub>CE</sub> =25V,	-	5.1	-	nF
Output capacitance	Coss	$V_{GE}=0V$ ,	-	0.72	-	
Reverse transfer capacitance	Crss	f=1MHz	-	0.38	-	

#### SWITCHING CHARACTERISTICS (tested at component), Inductive Load

Parameter	Symbol	Conditions 1)	Value			Unit
			min.	typ.	max.	Joint
Turn-on delay time	$t_{d(on)}$	<i>T</i> <sub>j</sub> =125°C	-	30	60	ns
Rise time	$t_{\rm r}$	V <sub>CC</sub> =600V,	-	70	140	
Turn-off delay time	$t_{d(off)}$	I <sub>C</sub> =75A, V <sub>GE</sub> =+15/-15V,	-	450	600	
Fall time	$t_{f}$	$R_{\rm G}$ =15 $\Omega$	-	70	100	

<sup>&</sup>quot;values also influenced by parasitic L- and C- in measurement and package.

#### **CHIP DRAWING:**





#### **FURTHER ELECTRICAL CHARACTERISTICS:**

This chip data sheet refers to the	BSM 75GD120DN2	ECONOPACK3
device data sheet	DOW 730D 120DIV2	LOCITOT ACITS

#### **DESCRIPTION:**

AQL 0,65 for visual inspection according to failure catalog

Electrostatic Discharge Sensitive Device according to MIL-STD 883

Test-Normen Villach/Prüffeld

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