

7805 • THREE-TERMINAL POSITIVE VOLTAGE REGULATOR IC

FEATURES:

- OUTPUT CURRENT IN EXCESS OF 1A;
- NO EXTERNAL COMPONENTS REQUIRED;
- INTERNAL SHORT CIRCUIT CURRENT LIMITING;
- INTERNAL THERMAL OVERLOAD PROTECTION;
- OUTPUT TRANSISTOR SAFE-AREA COMPENSATION;
- OUTPUT VOLTAGE OFFERED IN 4% TOLERANCE.

ABSOLUTE MAXIMUM RATINGS ($T_a = 25^\circ C$)

Characteristic	Symbol	Norm	Unit
Input Voltage	V_{in}	V	35
Maximum Dissipated Power(with heat sink)	$P_{tot(max)}$	W	15
Maximum Dissipated Power(without heat sink)	$P_{tot(max)}$	W	1.5
Thermal Resistance Junction to Case	Θ_{jC}	$^\circ C/W$	5.0
Thermal Resistance, Junction to Air	Θ_{jA}	$^\circ C/W$	65
Junction Temperature	T_j	150	$^\circ C$

$T_c = -45 \text{ to } +70^\circ C$

ELECTRICAL CHARACTERISTICS

($V_{in} = 10V$, $I_o = 0.5A$, $C_i = 0.33\mu F$, $C_o = 0.1\mu F$, $T_j = 0 \text{ to } +125^\circ C$, unless otherwise noted.)

Characteristic	Symbol	Norm			Unit
		Min	TYP	Max	
Output Voltage($T_j = 25^\circ C$)	V_o	4.8		5.2	V
Output Voltage (5.0mA $\leq I_o \leq 1.0A$, $P_o \leq 15W$) $7.0V \leq V_{in} \leq 20V$	V_o	4.75		5.25	V
Line Regulation($T_j = +25^\circ C$) $7.0V \leq V_{in} \leq 25V$ $8.0V \leq V_{in} \leq 12V$	ΔV_v			100 50	mV
Load Regulation($T_j = +25^\circ C$) $5.0mA \leq I_o \leq 1.5A$ $0.25A \leq I_o \leq 0.75A$	ΔV_i			100 50	mV
Quiescent Current($T_j = +25^\circ C$)	I_b			8.0	mA
Quiescent Current Change $7.0V \leq V_{in} \leq 25V$ $5.0mA \leq I_o \leq 1.0A$	ΔI_b			1.3 0.5	mA
Dropout Voltage($I_o = 1.0A$, $T_j = +25^\circ C$)	V_{i-V_o}		2.0		V
Short Circuit Current Limit($T_a = +25^\circ C$), $V_{in} = 35V$	I_{sc}		0.4		A
Peak Output Current($T_j = +25^\circ C$)	I_{max}		2.2		A
Average Temperature Coefficient of Output Voltage	T_{CV_o}		0.3		$mV/^\circ C$

