

## SANYO Semiconductors DATA SHEET

# CPH3322 — Ultrahigh-Speed Switching Applications

#### **Features**

- · Low ON-resistance.
- · Ultrahigh-speed switching.
- · 4V drive.

### **Specifications**

#### Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	VDSS		-60	V
Gate-to-Source Voltage	VGSS		±20	٧
Drain Current (DC)	ΙD		-0.6	Α
Drain Current (Pulse)	IDP	PW≤10μs, duty cycle≤1%	-2.4	Α
Allowable Power Dissipation	PD	Mounted on a ceramic board (900mm <sup>2</sup> X0.8mm)	0.9	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C

#### Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Unit
Drain-to-Source Breakdown Voltage	V(BR)DSS	ID= -1mA, VGS=0	-60			V
Zero-Gate Voltage Drain Current	IDSS	V <sub>DS</sub> = -60V, V <sub>GS</sub> =0			-1	μΑ
Gate-to-Source Leakage Current	IGSS	V <sub>GS</sub> = ±16V, V <sub>DS</sub> =0			±10	μΑ
Cutoff Voltage	VGS(off)	V <sub>DS</sub> = -10V, I <sub>D</sub> = -1mA	-1.2		-2.6	V
Forward Transfer Admittance	yfs	V <sub>DS</sub> = -10V, I <sub>D</sub> = -300mA	0.46	0.67		S
Static Drain-to-Source On-State Resistance	R <sub>DS</sub> (on)1	I <sub>D</sub> = -0.3A, V <sub>G</sub> S= -10V		1.3	1.7	Ω
	RDS(on)2	ID= -0.3A, VGS= -4V		1.6	2.3	Ω
Input Capacitance	Ciss	V <sub>DS</sub> = -20V, f=1MHz		73		pF
Output Capacitance	Coss	V <sub>DS</sub> = -20V, f=1MHz		7		pF
Reverse Transfer Capacitance	Crss	V <sub>DS</sub> = -20V, f=1MHz		4		pF
Turn-ON Delay Time	t <sub>d</sub> (on)	See specified Test Circuit.		6		ns
Rise Time	t <sub>r</sub>	See specified Test Circuit.		3.5		ns
Turn-OFF Delay Time	t <sub>d</sub> (off)	See specified Test Circuit.		12.5		ns
Fall Time	tf	See specified Test Circuit.		3		ns

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#### **CPH3322**

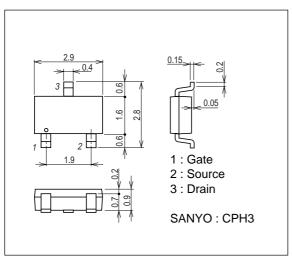
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Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Offic
Total Gate Charge	Qg	V <sub>DS</sub> = -30V, V <sub>GS</sub> = -10V, I <sub>D</sub> = -0.6A		2.6		nC
Gate-to-Source Charge	Qgs	V <sub>DS</sub> = -30V, V <sub>GS</sub> = -10V, I <sub>D</sub> = -0.6A		0.4		nC
Gate-to-Drain "Miller" Charge	Qgd	V <sub>DS</sub> = -30V, V <sub>GS</sub> = -10V, I <sub>D</sub> = -0.6A		0.4		nC
Diode Forward Voltage	VSD	IS= -0.6A, VGS=0		-0.88	-1.2	V

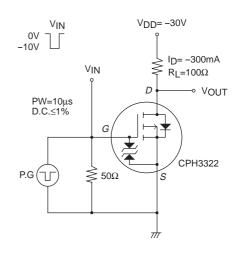
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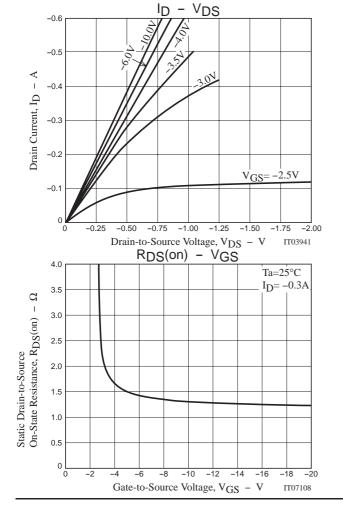
#### **Package Dimensions**

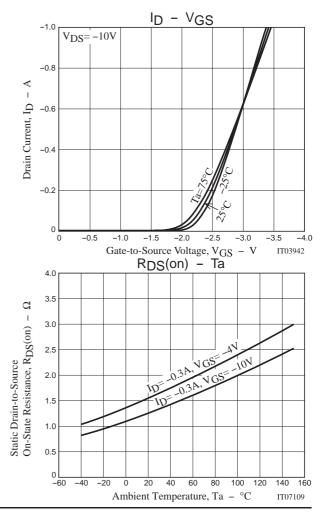
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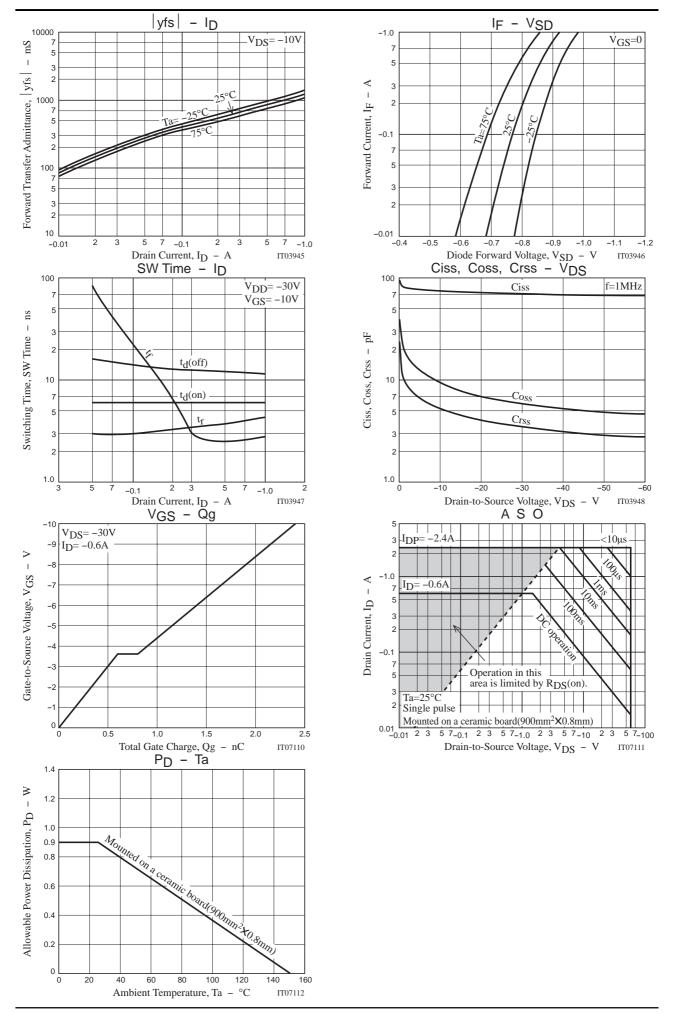


#### **Switching Time Test Circuit**









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