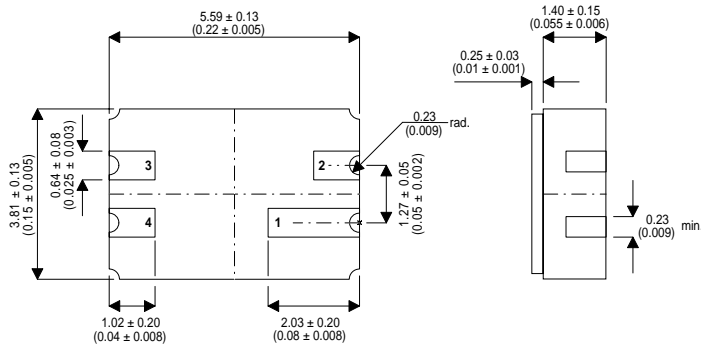


MECHANICAL DATA

Dimensions in mm



**LCC3 HERMETICALLY SEALED
 CERAMIC SURFACE MOUNT PACKAGE**

PIN 1 n/c **PIN 3 adj**
PIN 2 V+ **PIN 4 V-**

**PRECISION 5V SHUNT
 REGULATOR DIODE IN A
 CERAMIC SURFACE MOUNT
 PACKAGE**

FEATURES

- Adjustable 4V to 6V
- Low Temperature Coefficient
- Wide Operating Current 600µA to 10mA
- 0.6Ω Dynamic Impedance
- ± 1% Inital Tolerance Available

ABSOLUTE MAXIMUM RATINGS ($T_{case} = 25^{\circ}C$ unless otherwise stated)

I_R	Reverse Current	15mA
I_F	Forward Current	10mA
T_{case}	Operating Temperature Range	- 55°C to +150°C
T_{STG}	Storage Temperature	- 60°C to +150°C

ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$ unless otherwise stated)

Parameter	Test Conditions	LM136CSM4-5V LM136ACSM4-5V			Units
		Min.	Typ.	Max.	
Reverse Breakdown Voltage	$T_A = 25^{\circ}C$ $I_R = 1mA$ LM136CSM4-5V	4.90	5.00	5.10	V
		4.95	5.00	5.05	
Reverse Breakdown Change with Current	$T_A = 25^{\circ}C$ $600\mu A \leq I_R \leq 10mA$		6	12	mV
Reverse Dynamic Impedance	$T_A = 25^{\circ}C$ $I_R = 1mA$ $f = 100Hz$		0.6	1.2	Ω
Temperature Stability ^{Note 2}	V_R Adjusted 5.00V $I_R = 1mA$ $- 55^{\circ}C \leq T_A \leq +125^{\circ}C$		20	36	mV
Adjustment Range			±1		V
Reverse Dynamic Impedance	$I_R = 1mA$		0.8	1.6	Ω
Long Term Stability	$T_A = 25^{\circ}C \pm 0.1^{\circ}C$ $I_R = 1mA$ $t = 1000hrs$		20		ppm

Note 2.

Temperature stability is guaranteed by design. Design limits are guaranteed (but not 100% function tested) over the indicated temperature and supply voltage ranges. These limits are not used to calculate outgoing quality levels. Stability is defined as the maximum change in V_{xxx} FROM 25°C to T_A (MIN) or T_A (MAX).