TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TC7SAU04F,TC7SAU04FU

Inverter (unbuffer) with 3.6 V Tolerant Input

### **Features**

Low voltage operation: V<sub>CC</sub> = 1.8~3.6 V
 Low power disipation: I<sub>CC</sub> < 20 μA (max)</li>

•  $(V_{CC} = 3.6 \text{ V}, \text{Ta} = -40 \sim 85^{\circ}\text{C})$ 

• High speed operation :  $t_{pd} = 3.5 \text{ ns (max) (VCC} = 3.0 \sim 3.6 \text{ V)}$ 

 $t_{pd} = 4.2 \text{ ns (max) (VCC} = 2.3 \sim 2.7 \text{ V)}$ 

 $: t_{pd} = 8.4 \text{ ns (max) (V}_{CC} = 1.8 \text{ V})$ 

• High Output current  $: I_{OH}/I_{OL} = \pm 24 \text{ mA (min)} (V_{CC} = 3.0 \text{ V})$ 

:  $IOH/IOL = \pm 18 \text{ mA (min) (VCC} = 2.3 \text{ V)}$ 

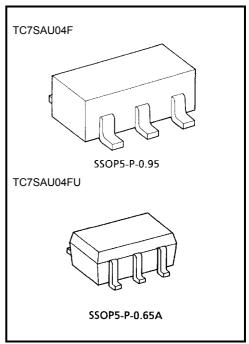
:  $I_{OH}/I_{OL} = \pm 6 \text{ mA (min) (V}_{CC} = 1.8 \text{ V)}$ 

• Latch-up performance: ±300 mA or more

• ESD performance : Human body model  $> \pm 200 \text{ V}$ 

: Machine model  $> \pm 2000 \text{ V}$ 

• Power down protection is provided on all inputs and outputs.



Weight

SSOP5-P-0.95 : 0.016 g (typ.) SSOP5-P-0.65A : 0.006 g (typ.)

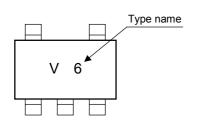
### Maximum Ratings (Ta = 25°C)

| Characteristics                    | Symbol           | Rating                              | Unit |
|------------------------------------|------------------|-------------------------------------|------|
| Power supply voltage               | $V_{CC}$         | -0.5~4.6                            | V    |
| DC input voltage                   | V <sub>IN</sub>  | -0.5~4.6                            | V    |
| DC output voltage                  | V <sub>OUT</sub> | -0.5~V <sub>CC</sub> + 0.5 (Note 1) | V    |
| Input diode current                | I <sub>IK</sub>  | -50                                 | mA   |
| Output diode current               | lok              | ±50 (Note 2)                        | mA   |
| DC output current                  | lout             | ±50                                 | mA   |
| Power dissipation                  | PD               | 200                                 | mW   |
| DC V <sub>CC</sub> /ground current | Icc              | ±100                                | mA   |
| Storage temperature range          | T <sub>stg</sub> | -65~150                             | °C   |

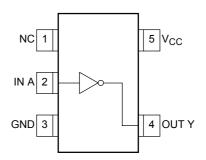
Note 1: High or low state. I<sub>OUT</sub> absolute maximum rating be observed.

Note 2: Vout < GND, Vout > Vcc

# Marking



# Pin Assignment (top view)



# **Logic Diagram**



### **Truth Table**

| А | Y |
|---|---|
| L | Н |
| Н | L |

# **Recommended Operating Range**

| Characteristics             | Symbol                           | Rating                     | Unit     |
|-----------------------------|----------------------------------|----------------------------|----------|
| Power supply voltage        | V <sub>CC</sub>                  | 1.8~3.6                    | - V      |
|                             |                                  | 1.2~3.6 (Note 3)           |          |
| Input voltage               | V <sub>IN</sub>                  | -0.3~3.6                   | V        |
| Output voltage              | V <sub>OUT</sub>                 | 0~V <sub>CC</sub> (Note 4) | <b>V</b> |
| Output current              | I <sub>OH</sub> /I <sub>OL</sub> | ±24 (Note 5)               |          |
|                             |                                  | ±18 (Note 6)               | mA       |
|                             |                                  | ±6 (Note 7)                |          |
| Operating temperature range | T <sub>opr</sub>                 | <b>−40~85</b>              | °C       |
| Input rise and fall time    | dt/dv                            | 0~10 (Note 8)              | ns/V     |

Note 3: Data retention only

Note 4: High or low state

Note 5:  $V_{CC} = 3.0 \sim 3.6 \text{ V}$ 

Note 6:  $V_{CC} = 2.3 \sim 2.7 \text{ V}$ 

Note 7:  $V_{CC} = 1.8 \text{ V}$ 

Note 8:  $V_{IN} = 0.8 \sim 2.0 \text{ V}, V_{CC} = 3.0 \text{ V}$ 

# **Electrical Characteristics**

# DC Characteristics (Ta = -40~85°C)

| Characteristics Symbol                   |                 | Symbol          | Test Condition                                   |                           |                          | Min                       | Max                      | Unit  |
|--|-----------------|-----------------|--|---------------------------|--------------------------|---------------------------|--------------------------|-------|
| Cilarac                                  | teristics       | Symbol          | rest Condition                                   |                           | V <sub>CC</sub> (V)      | IVIII                     | IVIAX                    | Offic |
| Llight level                             |                 | V               | _  |                           | 1.8                      | 0.85 ×<br>V <sub>CC</sub> |                          |       |
| Input voltage                            | V <sub>IH</sub> | 2.3~3.6         |  |                           | 0.8 ×<br>V <sub>CC</sub> | _                         | V                        |       |
|  | V <sub>IL</sub> |                 |  |                           | _                        | 0.15 ×<br>V <sub>CC</sub> | V                        |       |
|  | Low level       | ۷IL             |  | _                         |                          | _                         | 0.2 ×<br>V <sub>CC</sub> |       |
|  |                 |                 |  | $I_{OH} = -100 \mu A$     | 1.8~3.6                  | V <sub>CC</sub><br>- 0.2  |                          | V     |
|  |                 |                 |  | $I_{OH} = -6 \text{ mA}$  | 1.8                      | 1.4                       |                          |       |
|  |                 |                 |  | $I_{OH} = -12 \text{ mA}$ | 2.3                      | 1.8                       | _                        |       |
|  | High level      | V <sub>ОН</sub> | $V_{IN} = V_{IL}$                                | $I_{OH} = -18 \text{ mA}$ | 2.3                      | 1.7                       | _                        |       |
|  |                 |                 |  | $I_{OH} = -12 \text{ mA}$ | 2.7                      | 2.2                       | _                        |       |
|  |                 |                 |  | $I_{OH} = -18 \text{ mA}$ | 3.0                      | 2.4                       | _                        |       |
| Output voltage                           |                 |                 |  | I <sub>OH</sub> = -24 mA  | 3.0                      | 2.2                       | _                        |       |
|  |                 | V <sub>OL</sub> | V <sub>IN</sub> = V <sub>IH</sub>                | I <sub>OL</sub> = 100 μA  | 1.8~3.6                  | _                         | 0.2                      |       |
|  |                 |                 |  | I <sub>OL</sub> = 6 mA    | 1.8                      | _                         | 0.3                      |       |
|  |                 |                 |  | I <sub>OL</sub> = 12 mA   | 2.3                      | _                         | 0.4                      |       |
|  | Low level       |                 |  | $I_{OL} = 18 \text{ mA}$  | 2.3                      | _                         | 0.6                      |       |
|  |                 |                 | I <sub>OL</sub> = 12 mA                          | 2.7                       | _                        | 0.4                       |                          |       |
|  |                 |                 | $I_{OL} = 18 \text{ mA}$                         | 3.0                       | _                        | 0.4                       |                          |       |
|  |                 |                 | I <sub>OL</sub> = 24 mA                          | 3.0                       | _                        | 0.55                      |                          |       |
| Input leakage curre                      | ent             | I <sub>IN</sub> | V <sub>IN</sub> = 0~3.6 V                        |                           | 2.7~3.6                  | _                         | ±5.0                     | μΑ    |
| Quioscont supply o                       | urront          | laa             | V <sub>IN</sub> = V <sub>CC</sub> or GND         |                           | 2.7~3.6                  | _                         | 20.0                     | ^     |
| Quiescent supply current I <sub>CC</sub> |                 | ICC             | $V_{CC} \le (V_{IN}, V_{OUT}) \le 3.6 \text{ V}$ |                           | 2.7~3.6                  | _                         | ±20.0                    | μА    |

# AC Characteristics (Ta = $-40\sim85$ °C, input: $t_r = t_f = 2.0$ ns, $C_L = 30$ pF, $R_L = 500$ $\Omega$ )

| Characteristics        | Symbol           | Test Condition     | V <sub>CC</sub> (V) | Min | Max | Unit |
|------------------------|------------------|--------------------|---------------------|-----|-----|------|
| Propagation delay time | t <sub>pLH</sub> | Figure 1, Figure 2 | 1.8                 | 1.0 | 8.4 |      |
|                        |                  |                    | $2.5 \pm 0.2$       | 8.0 | 4.2 | ns   |
|                        | <sup>t</sup> pHL |                    | $3.3 \pm 0.3$       | 0.6 | 3.5 |      |

For  $C_L = 50$  pF, add approximately 300 ps to the AC maximum specification.

### Dynamic Switching Characteristics (Ta = 25°C, input: $t_r = t_f = 2.0$ ns, $C_L = 30$ pF)

| Characteristics                              | Symbol     | Test Condition                                      |                     | Тур.  | Unit |
|--|------------|---|---------------------|-------|------|
|  | - <b>,</b> |   | V <sub>CC</sub> (V) |       |      |
|  |            | $V_{IN} = 1.8 \text{ V}, V_{IL} = 0 \text{ V}$ (Not | 9) 1.8              | 0.25  |      |
| Quiet output maximum dynamic V <sub>OL</sub> | $V_{OLP}$  | $V_{IN} = 2.5 \text{ V}, V_{IL} = 0 \text{ V}$ (Not | 9) 2.5              | 0.6   | ns   |
|  |            | $V_{IN} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$ (Not | 9) 3.3              | 8.0   |      |
| Quiet output minimum dynamic V <sub>OL</sub> | $V_{OLV}$  | $V_{IN} = 1.8 \text{ V}, V_{IL} = 0 \text{ V}$ (Not | 9) 1.8              | -0.25 |      |
|  |            | $V_{IN} = 2.5 \text{ V}, V_{IL} = 0 \text{ V}$ (Not | 9) 2.5              | -0.6  | ns   |
|  |            | $V_{IN} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$ (Not | 9) 3.3              | -0.8  |      |
| Quiet output minimum dynamic V <sub>OH</sub> |            | $V_{IN} = 1.8 \text{ V}, V_{IL} = 0 \text{ V}$ (Not | 9) 1.8              | 1.5   |      |
|  |            | $V_{IN} = 2.5 \text{ V}, V_{IL} = 0 \text{ V}$ (Not | 9) 2.5              | 1.9   | ns   |
|  |            | $V_{IN} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$ (Not | 9) 3.3              | 2.2   |      |

Note 9: Parameter guaranteed by design.

# **Capacitive Characteristics (Ta = 25°C)**

| Characteristics               | Symbol          | Test Condition           |           | V <sub>CC</sub> (V) | Тур. | Unit |
|-------------------------------|-----------------|--------------------------|-----------|---------------------|------|------|
| Input capacitance             | C <sub>IN</sub> | _                        |           | 1.8, 2.5, 3.3       | 4    | pF   |
| Power dissipation capacitance | C <sub>PD</sub> | f <sub>IN</sub> = 10 MHz | (Note 10) | 1.8, 2.5, 3.3       | 7    | pF   |

Note 10: C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation.

 $I_{CC (opr.)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$ 

### **AC Test Circuit**

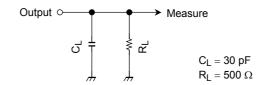


Figure 1

### **AC Wareform**

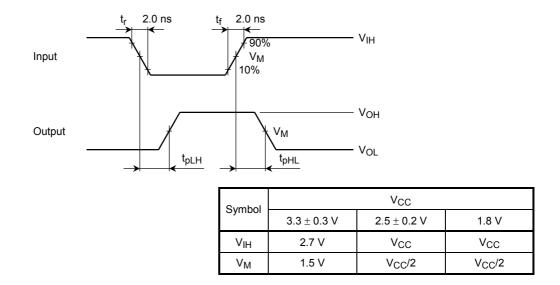


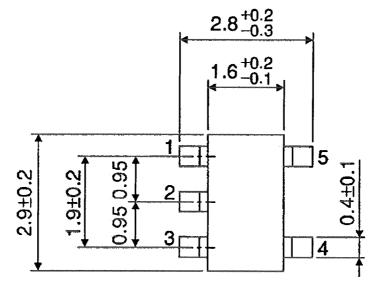
Figure 2 t<sub>pLH</sub>, t<sub>pHL</sub>

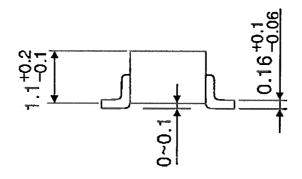
5

2002-12-13

# **Package Dimensions**

SSOP5-P-0.95 Unit: mm





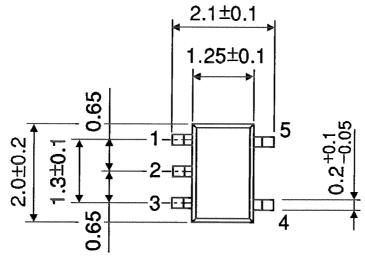
6

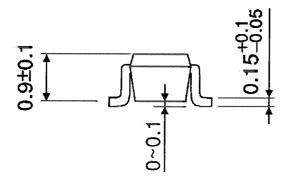
Weight: 0.016 g (typ.)

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# **Package Dimensions**

SSOP5-P-0.65A Unit: mm





7

Weight: 0.006 g (typ.)

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