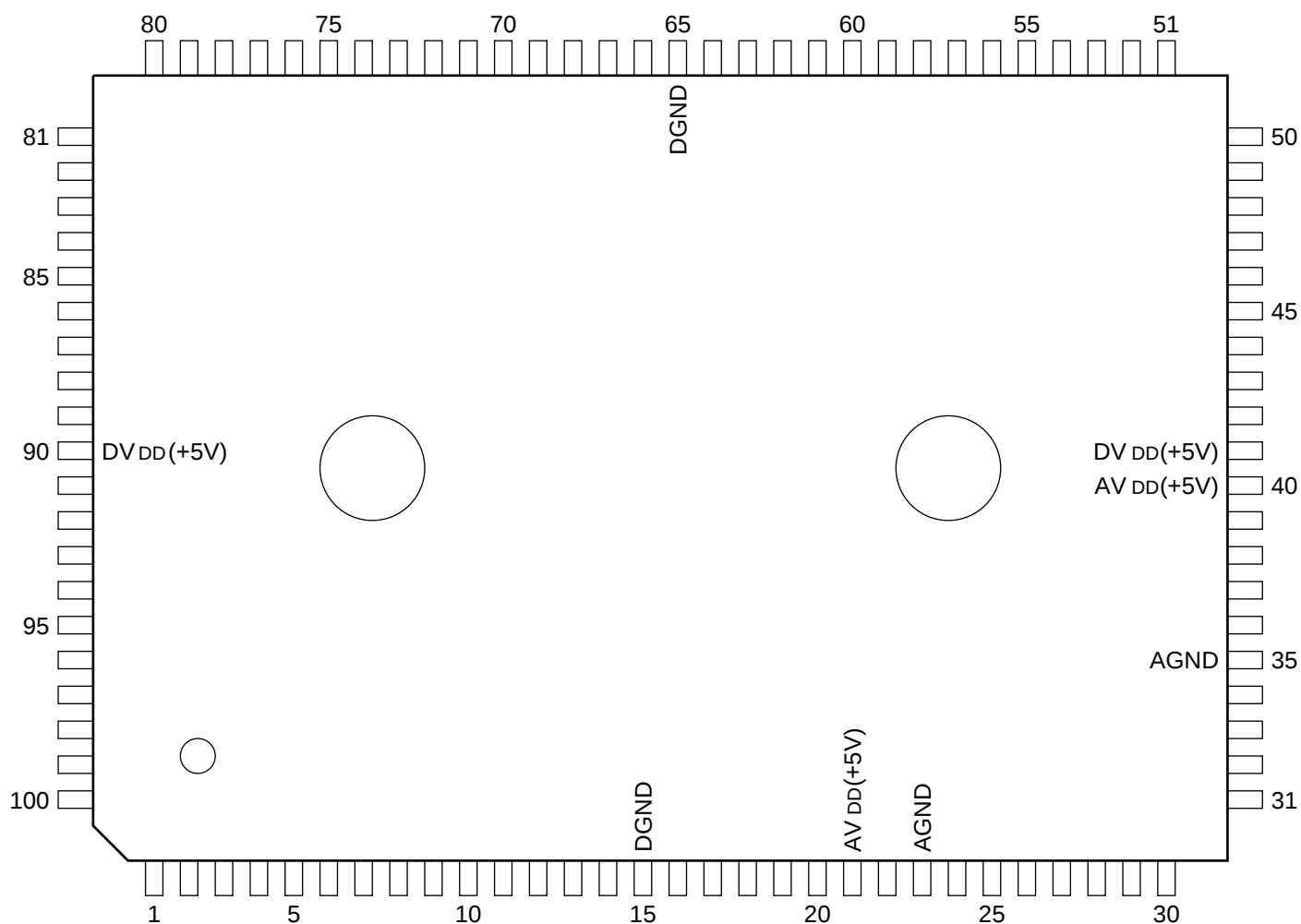

C-MOS CD DIGITAL PROCESSOR WITH SERVO

- TOP VIEW -



(AV_{DD},DV_{DD}=+5V normal)

PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL
1	O	SRON	26	I	RFDC	51	O	DA11	76	I	EXCK
2	O	SRDR	27	I	TE	52	O	DA10	77	O	SQSO
3	O	SFON	28	I	SE	53	O	DA09	78	I	SQCK
4	O	TFDR	29	I	FE	54	O	DA08	79	I	MUTE
5	O	TRON	30	I	VC	55	O	DA07	80	O	SENS
6	O	TRDR	31	O	FILO	56	O	DA06	81	I	XRST
7	O	TFON	32	I	FILI	57	O	DA05	82	I	DIRC
8	O	FFDR	33	O	PCO	58	O	DA04	83	I	SCLK
9	O	FRON	34	I	CLTV	59	O	DA03	84	I	DFSW
10	O	FRDR	35	–	AGND	60	O	DA02	85	I	ATSK
11	O	FFON	36	I	RFAC	61	O	DA01	86	I	DATA
12	O	VCOO	37	I	BIAS	62	I	XTAI	87	I	XLAT
13	I	VCOI	38	I	ASYI	63	O	XTAO	88	I	CLOCK
14	I	TEST	39	O	ASYO	64	I	XTSL	89	O	COUT
15	–	DGND	40	–	AV _{DD}	65	–	DGND	90	–	DV _{DD}
16	I	TES2	41	–	DV _{DD}	66	I	FSTI	91	O	MIRR
17	I	TES3	42	I	ASYE	67	O	FSTO	92	O	DFCT
18	O	PDO	43	I	PSSL	68	O	C4M	93	O	FOK
19	O	VPCO	44	O	WDCK	69	O	C16M	94	O	FSW
20	I	VCKI	45	O	LRCK	70	I	MD2	95	O	MON
21	–	AV _{DD}	46	O	DA16	71	O	DOUT	96	O	MDP
22	I	IGEN	47	O	DA15	72	O	EMPH	97	O	MDS
23	–	AGND	48	O	DA14	73	O	WFCK	98	O	LOCK
24	I	ADII	49	O	DA13	74	O	SCOR	99	I	SSTP
25	O	ADIO	50	O	DA12	75	O	SBSO	100	O	SFDR

