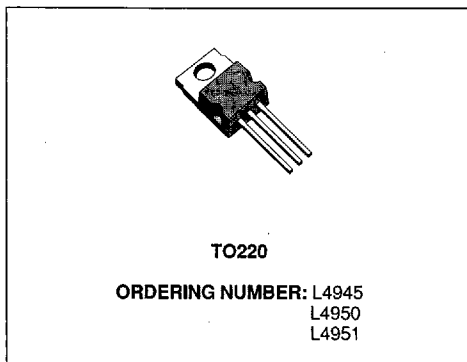


## 5V/8.5V/10V VERY LOW DROP VOLTAGE REGULATORS

- **PRECISE OUTPUT VOLTAGE:**  
5V  $\pm$  4% (L4945)  
8.5V  $\pm$  4% (L4950)  
10V  $\pm$  4% (L4951)  
OVER FULL TEMPERATURE RANGE  
(- 40 / 125 °C)
- **VERY LOW VOLTAGE DROP** (0.75Vmax)  
OVER FULL TEMPERATURE RANGE
- **OUTPUT CURRENT UP TO 500mA**
- **OVERVOLTAGE AND REVERSE VOLTAGE PROTECTIONS**
- **REVERSE VOLTAGE PROTECTION**
- **SHORT CIRCUIT PROTECTION AND THERMAL SHUT-DOWN** (with hysteresis)
- **LOW START UP CURRENT**

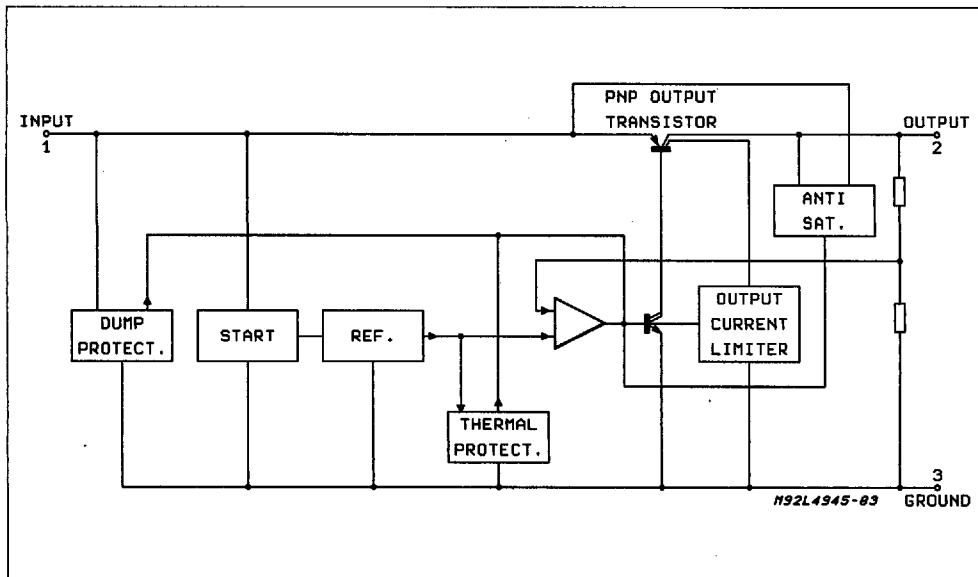


### DESCRIPTION

The devices are a monolithic integrated circuit in Versawatt package specially designed to provide a stabilized supply voltage for automotive and industrial electronic systems. Thanks to their very

low voltage drop, in automotive applications the devices can work correctly even during the cranking phase, when the battery voltage could fall as low as 6V. Furthermore, they incorporate a complete range of protection circuits against the dangerous overvoltages always present on the battery rail of the car.

### BLOCK DIAGRAM

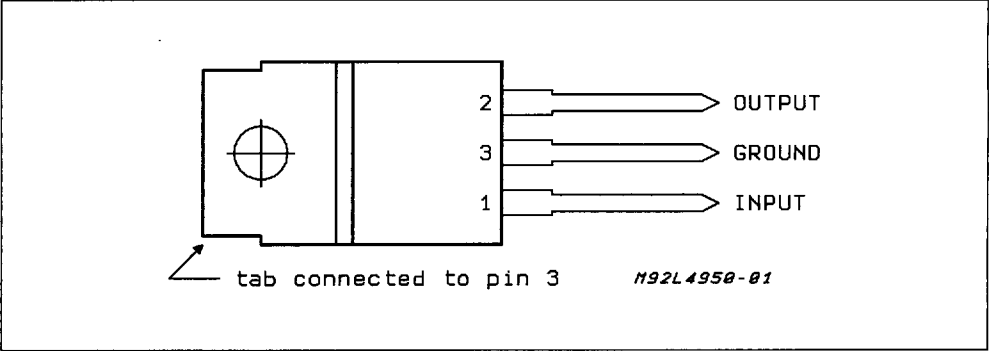


ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
$V_i$	DC Input Voltage	35	V
	DC Reverse Input Voltage	- 18	V
	Transient Input Overvoltages : Load Dump :	80	V
	5ms ≤ $t_{rise}$ ≤ 10ms $\tau_f$ Fall Time Constant = 100ms $R_{SOURCE} \geq 0.5\Omega$	- 80	V
	Field Decay : 5ms ≤ $t_{fall}$ ≤ 10ms, $R_{SOURCE} \geq 10\Omega$ $\tau_r$ Rise Time Constant = 33ms	± 100	V
	Low Energy Spike : $t_{rise} = 1\mu s$ , $t_{fall} = 500\mu s$ , $R_{SOURCE} \geq 10\Omega$ $f_r$ Repetition Frequency = 5Hz		
$T_J$	Junction Temperature Range	- 40 to 150	°C
$T_{OP}$	Operating Temperature Range	- 40 to 125	°C
$T_{stg}$	Storage Temperature Range	- 55 to 150	°C

Note: The circuit is ESD protected according to MIL-STD-883C.

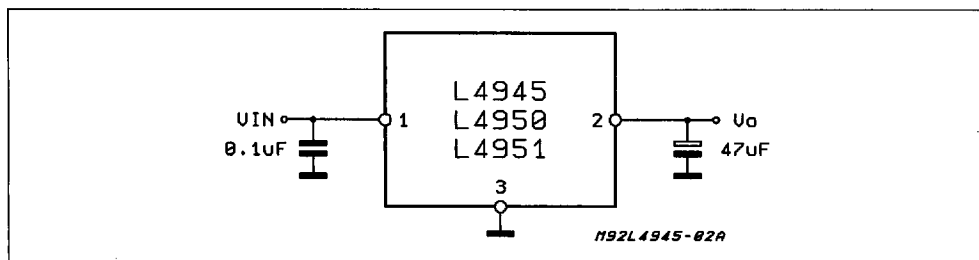
PIN CONNECTION (Top view)



THERMAL DATA

Symbol	Parameter	Value	Unit
$R_{th j-case}$	Thermal Resistance Junction-case      Max	3	°C/W

## TEST CIRCUIT



**ELECTRICAL CHARACTERISTICS** (refer to the test circuit,  $V_i = 14.4\text{V}$ ,  $C_o = 47\mu\text{F}$ ,  $\text{ESR} < 10\Omega$ ,  $R_p = 1\text{K}\Omega$ ,  $R_L = 1\text{K}\Omega$ ,  $-40^\circ\text{C} \leq T_J \leq 125^\circ\text{C}$ , unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
$V_o$	Output Voltage	$I_o = 0\text{mA}$ to $500\text{mA}$				
		Over Full T Range				
		for L4945	4.80	5.00	5.20	V
		for L4950	8.16	8.50	8.84	V
		for L4951	9.60	10	10.4	V
		$T_J = 25^\circ\text{C}$				
		for L4945	4.90	5.00	5.10	V
		for L4950	8.33	8.50	8.67	V
		for L4951	9.80	10	10.2	V
$V_i$	Operating Input Voltage	$I_o = 0\text{mA}$ to $(*) 500\text{mA}$	6		26	V
$\Delta V_o$	Line Regulation	$V_i = 6\text{V}$ to $26\text{V}$ ; $I_o = 5\text{mA}$		2	10	mV
$\Delta V_o$	Load Regulation	$I_o = 5\text{mA}$ to $500\text{mA}$		15	60	mV
$\frac{\Delta V_o}{\Delta V_i}$	Dropout Voltage	$I_o = 500\text{mA}$ , $T_J = 25^\circ\text{C}$ Over Full T Range		0.40	0.55	V
					0.75	V
$I_q$	Quiescent Current	$I_o = 0\text{mA}$ , $T_J = 25^\circ\text{C}$		5	10	mA
		$I_o = 0\text{mA}$ Over Full T		6.5	13	mA
		$I_o = 500\text{mA}$ Over Full T		110	180	mA
	Temperature Output Voltage Drift			-0.5		mV/ $^\circ\text{C}$
SVR	Supply Volt. Rej.	$I_o = 350\text{mA}$ ; $f = 120\text{Hz}$ $C_o = 100\mu\text{F}$ ; $V_i = 12\text{V} \pm 5\text{V}_{pp}$	50	60		dB
$I_{sc}$	Output Short Circuit Current		0.50	0.80	1.50	A

(\*) For a DC voltage  $26 < V_i < 37\text{V}$  the device is not operating

## FUNCTIONAL DESCRIPTION

The block diagram shows the basic structure of the devices : the reference, the error amplifier, the driver, the power PNP, the protection and reset functions.

The power stage is a Lateral PNP transistor which allows a very low dropout voltage (typ.  $400\text{mV}$  at  $T_J = 25^\circ\text{C}$ , max.  $750\text{mV}$  over the full temperature range @  $I_o = 500\text{mA}$ ). The typical curve of the dropout voltage as a function of the junction temperature is shown in Fig. 1 : that is the worst case, where  $I_o = 500\text{mA}$ .

The current consumption of the devices (quiescent current) are maximum  $10\text{mA}$  - over full T -

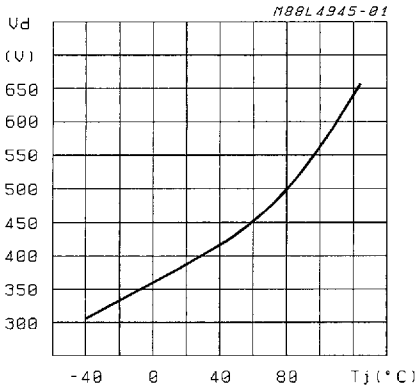
when no load current is required.

The internal antisaturation circuit allows a drastic reduction in the current peak which takes place during the start up.

The three gain stages (operational amplifier, driver and power PNP) require the external capacitor ( $C_{omin} = 20\mu\text{F}$ ) to guarantee the global stability of the system.

Load dump and field decay protections ( $\pm 80\text{V}$ ,  $t = 300\text{ms}$ ), reverse voltage ( $-18\text{V}$ ) and short circuit protection, thermal shutdown are the main features that make the devices specially suitable for applications in the automotive environment.

**Figure 1:** Typical Dropout Voltage vs.  $T_j$   
( $I_o = 500\text{mA}$ ).



#### EXTERNAL COMPENSATION

Since the purpose of a voltage regulator is to supply and load variations, the open loop gain of the regulators must be very high at low frequencies. This may cause instability as a result of the

various poles present in the loop. To avoid this instability dominant pole compensation is used to reduce phase shift due to other poles at the unity gain frequency. The lower the frequency of these other poles at the unity gain frequency, the lower the frequency of these other poles, the greater must be capacitor used to create the dominant pole for the same DC gain.

Where the output transistor is a lateral PNP type there is a pole in the regulation loop at a frequency too low to be compensated by a capacitor which can be integrated. An external compensation is therefore necessary so a very high value capacitor must be connected from the output to ground.

The parasitic equivalent series resistance of the capacitor used adds a zero to the regulation loop. This zero may compromise the stability of the system since its effect tends to cancel the effect of the pole added. In regulators this ESR must be less than  $3\Omega$  and the minimum capacitor value is  $47\mu\text{F}$ .