CM1800HC-34H

HIGH POWER SWITCHING USE
INSULATED TYPE

PRELIMINARY
Notice: This is not a final specification.
Some parametric limits are subject to change.

3rd-Version HVIGBT (High Voltage Insulated Gate Bipolar Transistor) Modules

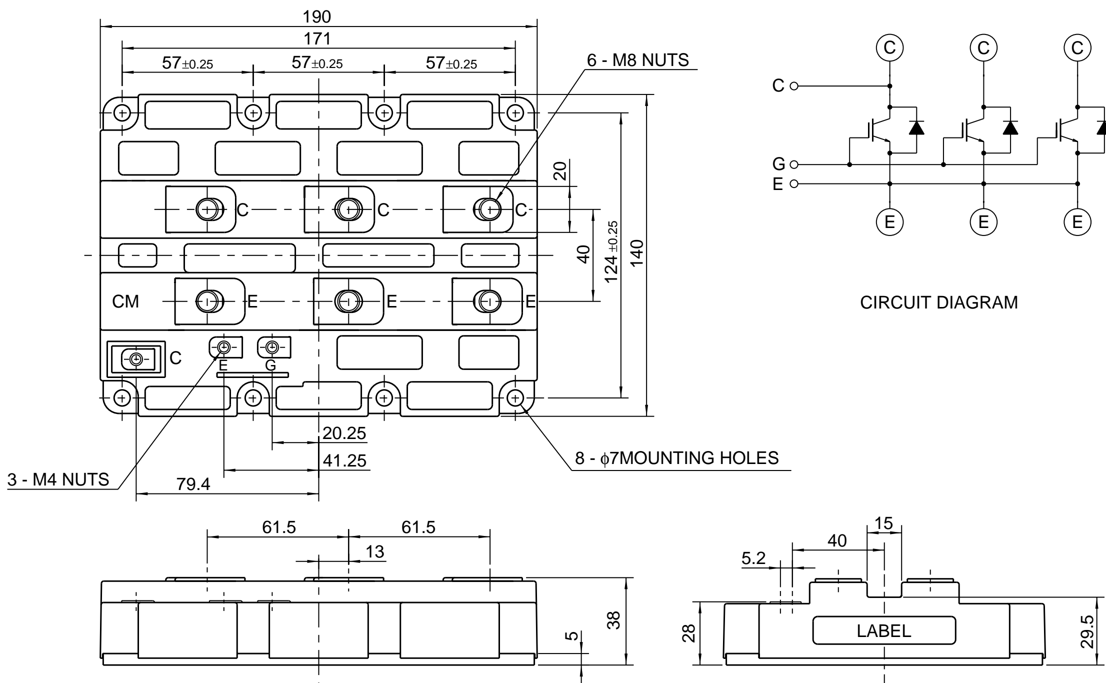
CM1800HC-34H

- I_C 1800A
- V_{CES} 1700V
- Insulated Type
- 1-element in a pack
- AlSiC base plate

APPLICATION

Inverters, Converters, DC choppers, Induction heating, DC to DC converters.

OUTLINE DRAWING & CIRCUIT DIAGRAM — Dimensions in mm



HVIGBT MODULES (High Voltage Insulated Gate Bipolar Transistor Modules)

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MAXIMUM RATINGS (T_j = 25°C)

Symbol	Item	Conditions	Ratings	Unit
V_{CES}	Collector-emitter voltage	V_{GE} = 0V	1700	V
V_{GES}	Gate-emitter voltage	V_{CE} = 0V	±20	V
I_C	Collector current	DC, T_C = 85°C	1800	A
I_{CM}		Pulse (Note 1)	3600	A
I_E (Note 2)	Emitter current		1800	A
I_{EM} (Note 2)		Pulse (Note 1)	3600	A
P_C (Note 3)	Maximum collector dissipation	T_C = 25°C, IGBT part	15600	W
T_j	Junction temperature	—	–40 ~ +150	°C
T_{stg}	Storage temperature	—	–40 ~ +125	°C
V_{iso}	Isolation voltage	Charged part to base plate, rms, sinusoidal, AC 60Hz 1min.	4000	V
—	Mounting torque	Main terminals screw M8	6.67 ~ 13.00	N·m
		Mounting screw M6	2.84 ~ 6.00	N·m
		Auxiliary terminals screw M4	0.88 ~ 2.00	N·m
—	Mass	Typical value	1.5	kg

ELECTRICAL CHARACTERISTICS (T_j = 25°C)

Symbol	Item	Conditions		Limits			Unit
				Min	Typ	Max	
I_{CES}	Collector cutoff current	V_{CE} = V_{CES}, V_{GE} = 0V		—	—	32	mA
$V_{GE(th)}$	Gate-emitter threshold voltage	I_C = 180mA, V_{CE} = 10V		4.5	5.5	6.5	V
I_{GES}	Gate-leakage current	V_{GE} = V_{GES}, V_{CE} = 0V		—	—	0.5	µA
$V_{CE(sat)}$	Collector-emitter saturation voltage	T_j = 25°C	I_C = 1800A, V_{GE} = 15V (Note 4)	—	2.40	—	V
		T_j = 125°C		—	2.95	—	
C_{ies}	Input capacitance	V_{CE} = 10V, V_{GE} = 0V		—	168	—	nF
C_{oes}	Output capacitance			—	21.0	—	nF
C_{res}	Reverse transfer capacitance			—	8.4	—	nF
Q_G	Total gate charge	V_{CC} = 850V, I_C = 1800A, V_{GE} = 15V		—	15.4	—	µC
$t_{d(on)}$	Turn-on delay time	V_{CC} = 850V, I_C = 1800A		—	—	1.60	µs
t_r	Turn-on rise time	V_{GE1} = V_{GE2} = 15V		—	—	2.00	µs
$t_{d(off)}$	Turn-off delay time	R_G = 0.3Ω		—	—	2.70	µs
t_f	Turn-off fall time	Resistive load switching operation		—	—	0.80	µs
V_{EC} (Note 2)	Emitter-collector voltage	I_E = 1800A, V_{GE} = 0V		—	2.50	—	V
t_{rr} (Note 2)	Reverse recovery time	I_E = 1800A,		—	—	2.70	µs
Q_{rr} (Note 2)	Reverse recovery charge	die / dt = –5100A / µs		—	700	—	µC
$R_{th(j-c)Q}$	Thermal resistance	Junction to case, IGBT part		—	—	0.008	K/W
$R_{th(j-c)R}$		Junction to case, FWDi part		—	—	0.013	K/W
$R_{th(c-f)}$	Contact thermal resistance	Case to fin, conductive grease applied		—	0.006	—	K/W

Note 1. Pulse width and repetition rate should be such that the device junction temp. (T_j) does not exceed T_{jmax} rating.
2. I_E, V_{EC}, t_{rr}, Q_{rr} & die/dt represent characteristics of the anti-parallel, emitter to collector free-wheel diode.
3. Junction temperature (T_j) should not increase beyond 150°C.
4. Pulse width and repetition rate should be such as to cause negligible temperature rise.