# **MA2S304**

## Silicon epitaxial planar type

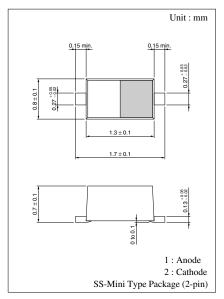
### For VCO

#### ■ Features

- $\bullet$  Good linearity and large capacitance-ratio in  $C_D \!-\!\!\!-\!\!\!\!- V_R$  relation
- Small series resistance r<sub>D</sub>
- SS-mini type package, allowing downsizing of equipment and automatic insertion through the taping package

## ■ Absolute Maximum Ratings $T_a = 25$ °C

Parameter	Symbol	Rating	Unit	
Reverse voltage (DC)	$V_R$	30	V	
Junction temperature	T <sub>j</sub>	150	°C	
Storage temperature	$T_{stg}$	-55 to +150	°C	



Marking Symbol: K

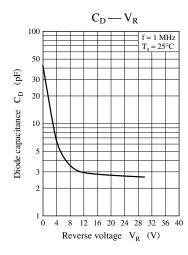
## ■ Electrical Characteristics $T_a = 25$ °C

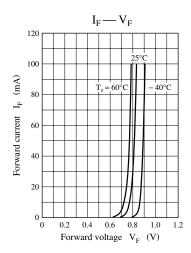
Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Reverse current (DC)	$I_R$	$V_R = 28 \text{ V}$			10	nA
Diode capacitance	C <sub>D(1V)</sub>	$V_R = 1 \text{ V, f} = 1 \text{ MHz}$	24.8		29.8	pF
	C <sub>D(4V)</sub>	$V_R = 4 V, f = 1 MHz$	6.0		8.3	pF
Capacitance ratio	C <sub>D(1V)</sub> /C <sub>D(4V)</sub>		3.0			_
Series resistance*	$r_{\mathrm{D}}$	$V_R = 4 \text{ V, f} = 100 \text{ MHz}$			1.0	Ω

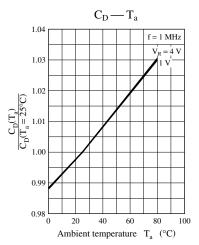
Note) 1. Rated input/output frequency: 100 MHz

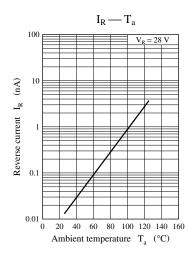
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<sup>2. \*:</sup> r<sub>f</sub> measuring instrument: YHP MODEL 4191A RF IMPEDANCE ANALYZER









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