

ADSL LINE TRANSFORMERS

- Designed to meet UL 1950/IEC-950 Requirements
- Working voltage up to 250Vrms

Electrical Specifications: @20

ISOLATION VOLTAGE: 1875Vrms 1 second between 5-6(PIN3-4,PIN8-7 tied)

URNS RATIO: (5-2):(6-9)=2.125:1 ±1% @10KHz 100mV (PIN8-7,PIN3-4 tied)

PIN5-2 OCL: 5.0mH ±10% @10KHz 100mV (PIN3-4 tied)

PIN5-2 Leakage Inductance: 12uH Max @100KHz 100mV (PIN3-4, PIN8-7, PIN6-9 tied)

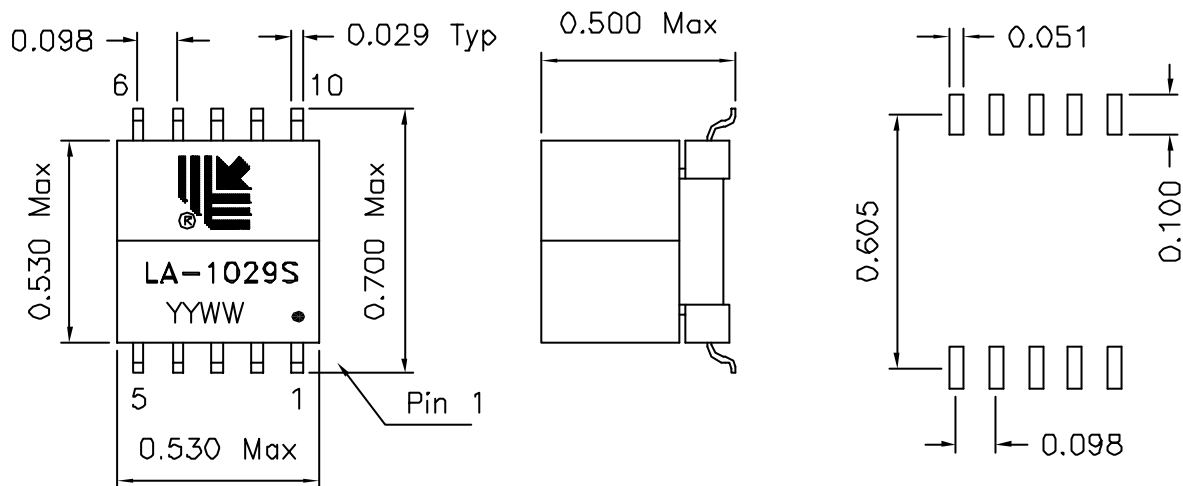
PIN6-8 DCR:0.350 ohms Max

PIN5-3 DCR:1.750 ohms Max

PIN4-2 DCR:1.750 ohms Max

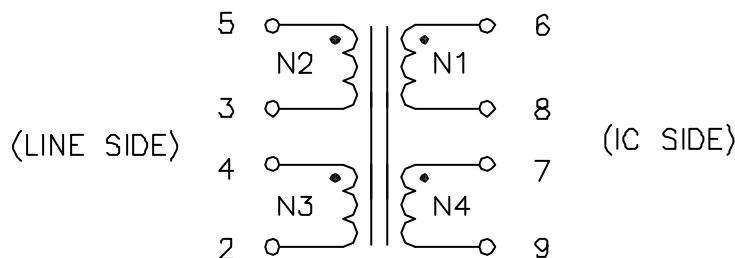
PIN7-9 DCR:0.400 ohms Max

Mechanicals:



SUGGESTED PCB PAD LAYOUT

Schematics:



Specification subject to change without notice

P/N: LA-1029S	DIMENSIONS IN INCH	Unless otherwise specified Dim. Tolerances are: .xxx ±0.010	REV: A1	DRWN:	DATE:03-11-14
	SCALE: NONE		PAGE:1/1	APPD:	DATE: