

TOSHIBA CMOS DIGITAL INTEGRATED CIRCUIT SILICON MONOLITHIC

**TC4W53F, TC4W53FU****2-CHANNEL MULTIPLEXER / DEMULTIPLEXER**

The TC4W53 is multiplexer with capabilities of selection and mixture of analog signal and digital signal.

TC4W53F has 2 channel configuration.

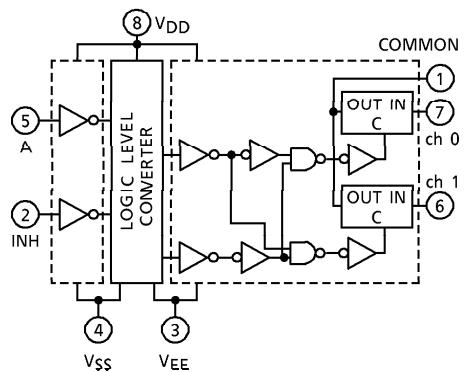
The digital signal to the control terminal turns "ON" the corresponding switch of each channel, with large amplitude ( $V_{DD}-V_{EE}$ ) can be switched by the control signal with small logical amplitude ( $V_{DD}-V_{SS}$ ).

For example, in the case of  $V_{DD}=5V$ ,  $V_{SS}=0V$  and  $V_{EE}=-5V$ , signals between  $-5V$  and  $+5V$  can be switched from the logical circuit with signal power supply of 5 volts.

As the ON-resistance of each switch is low, these can be connected to the circuits with low input impedance.

**MAXIMUM RATINGS**

CHARACTERISTIC	SYMBOL	RATING	UNIT
DC Supply Voltage	$V_{DD}-V_{SS}$	-0.5~20	V
DC Supply Voltage	$V_{DD}-V_{EE}$	-0.5~20	V
Control Input Voltage	$V_{CIN}$	$V_{SS}-0.5 \sim V_{DD}+0.5$	V
Switch I/O Voltage	$V_I/V_O$	$V_{EE}-0.5 \sim V_{DD}+0.5$	V
Control Input Current	$I_{CIN}$	$\pm 10$	mA
Potential difference across I/O during ON	$V_{I-O}$	-0.5~0.5	V
Power Dissipation	$P_D$	300	mW
Operating Temperature	$T_{opr}$	-40~85	°C
Storage Temperature	$T_{stg}$	-65~150	°C
Lead Temperature (10s)	$T_L$	260	°C

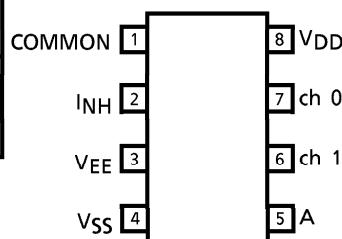
**LOGIC DIAGRAM****TRUTH TABLE**

CONTROL INPUT		ON CHANNEL
INH	A	
L	L	ch 0
L	H	ch 1
H	x	NONE

x : Don't Care

**TRUTH TABLE**

CONTROL C	IMPE-DANCE BETWEEN IN-OUT
L	0.5~ $5 \times 10^2 \Omega$
H	$> 10^9 \Omega$
L	

**PIN ASSIGNMENT (TOP VIEW)**

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## RECOMMENDED OPERATING CONDITIONS

CHARACTERISTIC	SYMBOL				MIN.	TYP.	MAX.	UNIT
DC Supply Voltage	V <sub>DD</sub> -V <sub>SS</sub>	—			3	—	18	V
	V <sub>DD</sub> -V <sub>EE</sub>	—			3	—	18	V
Control Input Voltage	V <sub>IN</sub>	—			V <sub>SS</sub>	—	V <sub>DD</sub>	V
Input/Output Voltage	V <sub>IN</sub> -V <sub>OUT</sub>	—			V <sub>EE</sub>	—	V <sub>DD</sub>	V

## DC ELECTRICAL CHARACTERISTICS

CHARACTERISTIC	SYM-BOL	TEST CONDITION			-40°C		25°C			85°C		UNIT	
		V <sub>SS</sub> (V)	V <sub>EE</sub> (V)	V <sub>DD</sub> (V)	MIN.	MAX.	MIN.	TYP.	MAX.	MIN.	MAX.		
Control Input High Voltage	V <sub>IH</sub>	V <sub>IS</sub> =V <sub>DD</sub> thru 1kΩ	V <sub>EE</sub> =V <sub>SS</sub> R <sub>L</sub> =1kΩ to V <sub>SS</sub> I <sub>LS</sub> <2μA on all OFF Channels	5	3.5	—	3.5	2.75	—	3.5	—	V	
				10	7.0	—	7.0	5.50	—	7.0	—		
				15	11.0	—	11.0	8.25	—	11.0	—		
Control Input Low Voltage	V <sub>IL</sub>			5	—	1.5	—	2.25	1.5	—	1.5	V	
				10	—	3.0	—	4.5	3.0	—	3.0		
				15	—	4.0	—	6.75	4.0	—	4.0		
On-State Resistance	R <sub>ON</sub>	0≤V <sub>IS</sub> ≤V <sub>DD</sub> R <sub>L</sub> =10kΩ	0	0	5	—	850	—	240	950	—	1200	Ω
			0	0	10	—	210	—	110	250	—	300	
			0	0	15	—	140	—	80	160	—	200	
Δ On-State Resistance Between 2 Switches	ΔR <sub>ON</sub>	—	0	0	5	—	—	—	10	—	—	—	Ω
			0	0	10	—	—	—	6	—	—	—	
			0	0	15	—	—	—	4	—	—	—	
Input/Output Leakage Current	I <sub>OFF</sub>	V <sub>IN</sub> =18V, V <sub>OUT</sub> =0V V <sub>IN</sub> =0V, V <sub>OUT</sub> =18V			18	—	±100	—	±0.01	±100	—	±1000	nA
					18	—	±100	—	±0.01	±100	—	±1000	
Quiescent Device Current	I <sub>DD</sub>	V <sub>IN</sub> =V <sub>SS</sub> , V <sub>DD</sub> *			5	—	5.0	—	0.005	5.0	—	150	μA
					10	—	10	—	0.010	10	—	300	
					15	—	20	—	0.015	20	—	600	
Input Current	I <sub>IN</sub>	V <sub>IH</sub> =18V, V <sub>IL</sub> =0V			18	—	0.1	—	10 <sup>-5</sup>	0.1	—	1.0	μA
					18	—	-0.1	—	-10 <sup>-5</sup>	-0.1	—	-1.0	
Input Capacitance	C <sub>IN</sub>	—			—	—	—	—	5	7.5	—	—	pF
Switch Input Capacitance	C <sub>IN</sub>	—			—	—	—	—	10	—	—	—	pF
Switch Output Capacitance	C <sub>OUT</sub>	—			10	—	—	—	17	—	—	—	
Feedthrough Capacitance	C <sub>IN</sub> -OUT	—			10	—	—	—	0.2	—	—	—	

\* All valid input combinations.

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AC ELECTRICAL CHARACTERISTICS ( $T_a = 25^\circ C$ ,  $C_L = 50\text{pF}$ )

CHARACTERISTIC	SYMBOL		TEST CONDITION			MIN.	TYP.	MAX.	UNIT
			$V_{SS}$ (V)	$V_{EE}$ (V)	$V_{DD}$ (V)				
Phase difference between input to output	$\phi_{I-O}$	—	0	0	5	—	15	45	ns
			0	0	10	—	8	20	
			0	0	15	—	6	15	
Propagation Delay Time (A-OUT)	$t_{pZL}$ $t_{pZH}$ $t_{pLZ}$ $t_{pHZ}$	$R_L = 1\text{k}\Omega$	0	0	5	—	170	550	ns
			0	0	10	—	90	240	
			0	0	15	—	70	160	
			0	-5	5	—	100	240	
			0	-7.5	7.5	—	80	160	
Propagation Delay Time (INH-OUT)	$t_{pZL}$ $t_{pZH}$	$R_L = 1\text{k}\Omega$	0	0	5	—	120	380	ns
			0	0	10	—	60	200	
			0	0	15	—	50	160	
			0	-5	5	—	80	200	
			0	-7.5	7.5	—	60	160	
Propagation Delay Time (INH-OUT)	$t_{pLZ}$ $t_{pHZ}$	$R_L = 1\text{k}\Omega$	0	0	5	—	170	450	ns
			0	0	10	—	90	210	
			0	0	15	—	70	160	
			0	-5	5	—	100	210	
			0	-7.5	7.5	—	80	160	
-3dB Cutoff Frequency	$f_{MAX}$ (I-O)	$R_L = 1\text{k}\Omega$ (*1)	-5	-5	5	—	40	—	MHz
Total Harmonic Distortion	—	$R_L = 10\text{k}\Omega$ $f = 1\text{kHz}$ (*2)	-2.5	-2.5	2.5	—	0.15	—	%
-50dB Feedthrough (Switch OFF)	—	$R_L = 1\text{k}\Omega$ (*3)	-5	-5	5	—	500	—	
Crosstalk (CONTROL-OUT)	—	$R_{IN} = 1\text{k}\Omega$ $R_{OUT} = 10\text{k}\Omega$ $C_L = 15\text{pF}$	0	0	5	—	200	—	
			0	0	10	—	400	—	mV
			0	0	15	—	600	—	

\*1 Sine wave of  $\pm 2.5\text{V}_{\text{p-p}}$  shall be used for  $V_{IS}$  and the frequency of  $20\log_{10} \frac{V_{OS}}{V_{IS}}$   
 $= -3\text{dB}$  shall be  $f_{MAX}$ .

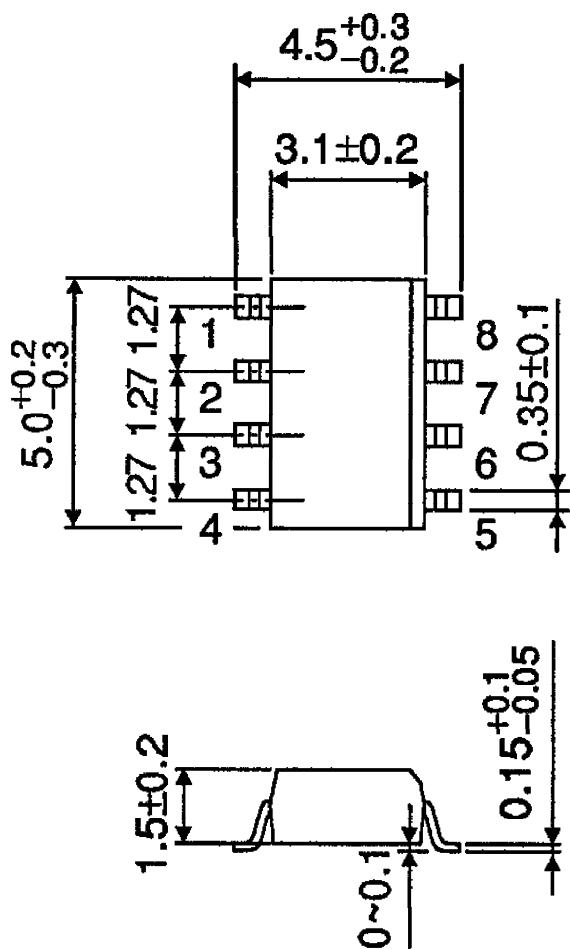
\*2  $V_{IS}$  shall be sine wave of  $\pm \left( \frac{V_{DD} - V_{EE}}{4} \right) \text{p-p}$ .

\*3 Sine wave of  $\pm 2.5\text{V}_{\text{p-p}}$  shall be used for  $V_{IS}$  and the frequency of  $20\log_{10} \frac{V_{OS}}{V_{IS}}$   
 $= -50\text{dB}$  shall be feed-through.

## OUTLINE DRAWING

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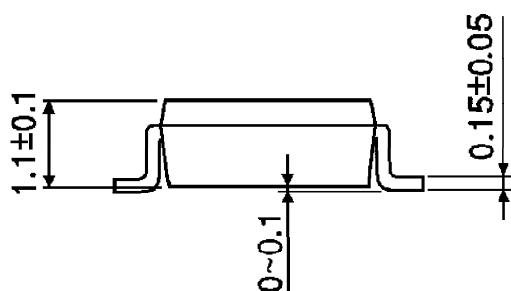
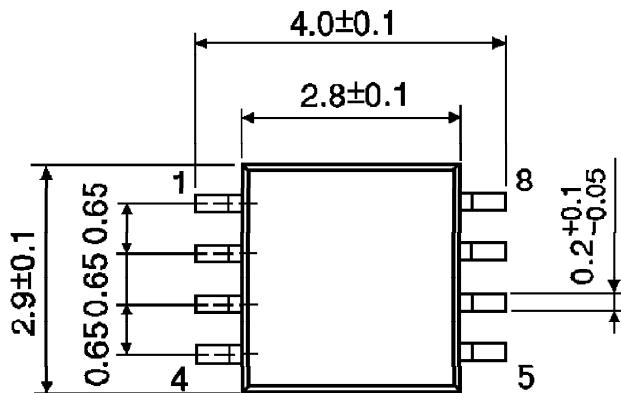
Unit : mm



Weight : 0.05g (Typ.)

**OUTLINE DRAWING**  
SSOP8-P-0.65

Unit : mm



Weight : 0.02g (Typ.)

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