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PIC16F84

(Direction seen from controller)

Diagram showing the PIC16F84 microcontroller pins connected to a host. The PIC TXD pin is connected to a 5V TXD terminal. The PIC RXD pin is connected to a 5V RXD terminal. The PIC DSR pin is connected to a 5V DSR terminal. The PIC DTR pin is connected to a 5V DTR terminal. Two jumpers, JP1_7 and JP2_7, are shown connected to the DSR and DTR pins respectively, with one end of each jumper connected to VSS.

RS232

(Direction seen from host)

Diagram showing the RS232 connector (CON1_7 DB9) pins connected to a host. The DTR pin is connected to an RS232 DTR terminal. The TXD pin is connected to an RS232 TXD terminal. The RXD pin is connected to an RS232 RXD terminal. The DSR pin is connected to an RS232 DSR terminal. The DTR pin is also connected to a VSS terminal.

D

C

B

A

Diagram showing the VDD and VSS pins of the PIC16F84 connected to a VDD_RS terminal. A 100nF capacitor (C1_7) is connected between VDD and VSS.

Diagram showing the MAX232CPE(16) IC pin connections. The R1 IN pin is connected to an RS232 TXD terminal. The R2 IN pin is connected to a 5V TXD terminal. The T1 IN pin is connected to a 5V DSR terminal. The T2 IN pin is connected to a 5V DTR terminal. The R1 OUT pin is connected to a 5V RXD terminal. The R2 OUT pin is connected to a 5V DTR terminal. The T1 OUT pin is connected to an RS232 RXD terminal. The T2 OUT pin is connected to an RS232 DSR terminal. The C1+ pin is connected to a 5V DSR terminal. The C1- pin is connected to a 5V DTR terminal. The C2+ pin is connected to a 5V DTR terminal. The C2- pin is connected to a 5V DSR terminal. The VDD pin is connected to a VDD terminal. The VSS pin is connected to a VSS terminal. Two 10uF capacitors (C2_7 and C4_7) are connected between VDD and VSS. A 10uF capacitor (C3_7) is connected between the C1+ and C1- pins. A 10uF capacitor (C5_7) is connected between the C2+ and C2- pins. A jumper (JP3_7) is connected between the VDD and VSS pins.

Title

RS232 Interface using MAX232

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