325MHz, 140V/µs Rail-to-Rail Input and Output Low Distortion, Low Noise Precision Op Amp

June 2000

FEATURES

- **Gain Bandwidth Product: 325MHz**
- **Slew Rate: 140V/µs**
- **Wide Supply Range: 2.5V to 12V**
- **Large Output Current: 85mA**
- **Low Distortion, 5MHz: –80dBc**
- **Low Voltage Noise: 3.5nV/√Hz**
- Input Common Mode Range Includes Both Rails
- Output Swings Rail-to-Rail
- Input Offset Voltage (Rail-to-Rail): 550µV Max
- Common Mode Rejection: 106dB Typ
- Power Supply Rejection: 105dB Typ
- Open-Loop Gain: 300V/mV Typ
- Power Down Pin
- SO-8 Package
- Operating Temperature Range –40°C to 85°C

APPLICATIONS

- Active Filters
- Rail-to-Rail Buffer Amplifiers
- Driving A/D Converters
- Low Voltage Signal Processing
- Video Line Driver

DESCRIPTION

The LT®1806 is a rail-to-rail input and output op amp that features a 325MHz gain bandwidth product, a 140V/µs slew rate and a 85mA output current to fit the need for low voltage, high performance signal conditioning systems.

The LT1806 has a very low distortion of –80dBc at 5MHz, a low input referred noise voltage of 3.5nV/√Hz and a maximum offset voltage of 550µV that allows it to be used in high performance data acquisition systems.

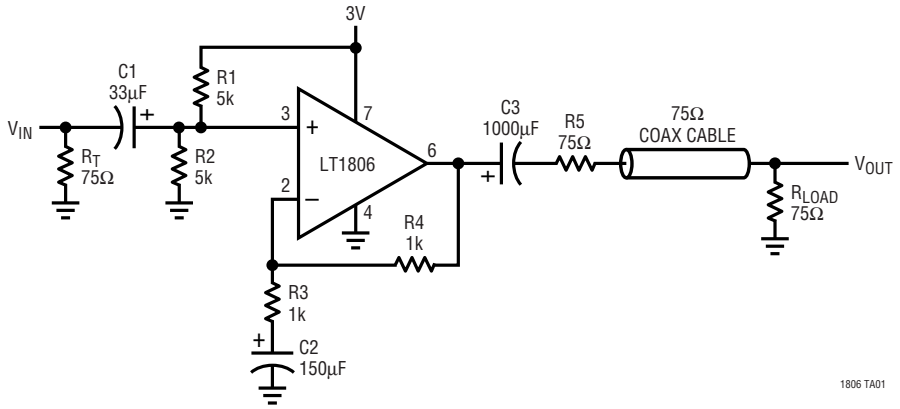
The LT1806 has an input range that includes both supply rails and an output that swings within 20mV of either supply rail to maximize the signal dynamic range in low supply applications.

The LT1806 maintains its performance for supplies from 2.5V to 12V and is specified at 3V, 5V and ±5V supplies. The inputs can be driven beyond the supplies without damage or phase reversal of the output.

The LT1806 is available in an 8-pin SO package with the standard op amp pinout. This device can be used as a plug-in replacement for many op amps to improve input/output range and performance.

TYPICAL APPLICATION

3V Single Supply Video Line Driver

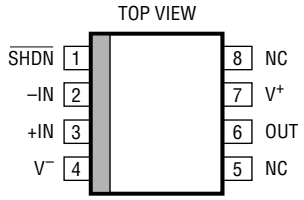


ABSOLUTE MAXIMUM RATINGS

(Note 1)

Total Supply Voltage (V^+ to V^-) 12.6V
Input Voltage (Note 2) ..... $\pm V_S$
Input Current (Note 2) ..... ±10mA
Output Short-Circuit Duration (Note 3) ..... Indefinite
Operating Temperature Range (Note 4) .. –40°C to 85°C
Specified Temperature Range (Note 5) ... –40°C to 85°C
Junction Temperature ..... 150°C
Storage Temperature Range ..... –65°C to 150°C
Lead Temperature (Soldering, 10 sec) ..... 300°C

PACKAGE/ORDER INFORMATION



S8 PACKAGE
8-LEAD PLASTIC SO

T_{JMAX} = 150°C, θ_{JA} = 90°C/W (Note 9)

ORDER PART NUMBER
LT1806CS8 LT1806IS8
S8 PART MARKING
1806 1806I

Consult factory for Military grade parts.

ELECTRICAL CHARACTERISTICS

T_A = 25°C. V_S = 5V, 0V; V_S = 3V, 0V; $V_{\overline{SHDN}}$ = open; V_{CM} = V_{OUT} = half supply unless otherwise noted.

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
V_{OS}	Input Offset Voltage	$V_{CM} = V^+$ $V_{CM} = V^-$		100 100	550 550	µV µV
ΔV_{OS}	Input Offset Voltage Shift	$V_{CM} = V^-$ to V^+		50	500	µV
I_B	Input Bias Current	$V_{CM} = V^+$ $V_{CM} = V^-$	 –13	1 –5	4	µA µA
ΔI_B	Input Bias Current Shift	$V_{CM} = V^-$ to V^+		6	17	µA
I_{OS}	Input Offset Current	$V_{CM} = V^+$ $V_{CM} = V^-$		0.03 0.05	0.6 1.5	µA µA
ΔI_{OS}	Input Offset Current Shift	$V_{CM} = V^-$ to V^+		0.08	2.1	µA
	Input Noise Voltage	0.1Hz to 10Hz		40		nV_{P-P}
e_n	Input Noise Voltage Density	f = 10kHz		3.5		$nV/\sqrt{Hz}$
i_n	Input Noise Current Density	f = 10kHz		4		$pA/\sqrt{Hz}$
C_{IN}	Input Capacitance			2		pF
A_{VOL}	Large Signal Voltage Gain	V_S = 5V, V_O = 0.5V to 4.5V, R_L = 1k V_S = 5V, V_O = 1V to 4V, R_L = 100 V_S = 3V, V_O = 0.5V to 2.5V, R_L = 1k	90 10 75	220 22 150		V/mV V/mV V/mV
CMRR	Common Mode Rejection Ratio	V_S = 5V, V_{CM} = V^- to V^+ V_S = 3V, V_{CM} = V^- to V^+	80 75	100 95		dB dB
	Input Common Mode Range		V^-		V^+	V
PSRR	Power Supply Rejection Ratio	V_S = 2.5V to 10V, V_{CM} = 0V	91	105		dB
	Minimum Supply Voltage (Note 6)			2.3	2.5	V
V_{OL}	Output Voltage Swing LOW (Note 7)	No Load I_{SINK} = 5mA I_{SINK} = 25mA		6 45 170	35 130 400	mV mV mV
V_{OH}	Output Voltage Swing HIGH (Note 7)	No Load I_{SOURCE} = 5mA I_{SOURCE} = 25mA		12 85 350	50 180 700	mV mV mV
I_{SC}	Short-Circuit Current	V_S = 5V V_S = 3V	±35 ±30	±70 ±60		mA mA
I_S	Supply Current			9	13	mA

ELECTRICAL CHARACTERISTICS

T_A = 25°C. V_S = 5V, 0V; V_S = 3V, 0V; $V_{\overline{SHDN}}$ = open; V_{CM} = V_{OUT} = half supply unless otherwise noted.

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
	Disable Supply Current	V_S = 5V, $V_{\overline{SHDN}}$ = 0V V_S = 3V, $V_{\overline{SHDN}}$ = 0V		0.30 0.16	0.9 0.7	mA mA
GBW	Gain Bandwidth Product	Frequency = 2MHz		325		MHz
SR	Slew Rate	V_S = 5V, A_V = –1, R_L = 1k, V_O = $4V_{P-P}$		130		V/µs
THD	Total Harmonic Distortion	V_S = 5V, A_V = 1, R_L = 1k, V_O = $2V_{P-P}$, f_C = 5MHz		–78		dB
t_S	Settling Time	0.01%, V_S = 5V, V_{STEP} = 2V, A_V = 1, R_L = 1k		60		ns
ΔG	Differential Gain (NTSC)	V_S = 5V, A_V = 2, R_L = 150		0.015		%
Δθ	Differential Phase (NTSC)	V_S = 5V, A_V = 2, R_L = 150		0.05		Deg

The ● denotes specifications which apply over the 0°C < T_A < 70°C temperature range. V_S = 5V, 0V; V_S = 3V, 0V; $V_{\overline{SHDN}}$ = open; V_{CM} = V_{OUT} = half supply unless otherwise noted.

SYMBOL	PARAMETER	CONDITIONS		MIN	TYP	MAX	UNITS
V_{OS}	Input Offset Voltage	V_{CM} = V^+ V_{CM} = V^-	● ●		200 200	700 700	µV µV
V_{OS} TC	Input Offset Voltage Drift (Note 8)	V_{CM} = V^+ V_{CM} = V^-	● ●		1.5 1.5	5 5	µV/°C µV/°C
ΔV_{OS}	Input Offset Voltage Shift	V_{CM} = V^- to V^+	●		100	700	µV
I_B	Input Bias Current	V_{CM} = V^+ – 0.1V V_{CM} = V^- + 0.2V	● ●	 –14	1 –5	5	µA µA
ΔI_B	Input Bias Current Shift	V_{CM} = V^- + 0.2V to V^+ – 0.1V	●		6	19	µA
I_{OS}	Input Offset Current	V_{CM} = V^+ – 0.1V V_{CM} = V^- + 0.2V	● ●		0.03 0.05	0.75 1.8	µA µA
ΔI_{OS}	Input Offset Current Shift	V_{CM} = V^- + 0.2V to V^+ – 0.1V	●		0.08	2.55	µA
A_{VOL}	Large Signal Voltage Gain	V_S = 5V, V_O = 0.5V to 4.5V, R_L = 1kΩ V_S = 5V, V_O = 1V to 4V, R_L = 100Ω V_S = 3V, V_O = 0.5V to 2.5V, R_L = 1kΩ	● ● ●	75 9 65	175 20 140		V/mV V/mV V/mV
CMRR	Common Mode Rejection Ratio	V_S = 5V, V_{CM} = V^- to V^+ V_S = 3V, V_{CM} = V^- to V^+	● ●	77 72	94 89		dB dB
	Input Common Mode Range		●	V^-		V^+	V
PSRR	Power Supply Rejection Ratio	V_S = 2.5V to 10V, V_{CM} = 0V	●	89	105		dB
	Minimum Supply Voltage (Note 6)		●		2.3	2.5	V
V_{OL}	Output Voltage Swing LOW (Note 7)	No Load I_{SINK} = 5mA I_{SINK} = 25mA	● ● ●		8 50 180	60 150 450	mV mV mV
V_{OH}	Output Voltage Swing HIGH (Note 7)	No Load I_{SOURCE} = 5mA I_{SOURCE} = 25mA	● ● ●		30 110 370	80 220 750	mV mV mV
I_{SC}	Short-Circuit Current	V_S = 5V V_S = 3V	● ●	±30 ±25	±65 ±55		mA mA
I_S	Supply Current		●		10	14	mA
	Disable Supply Current	V_S = 5V, $V_{\overline{SHDN}}$ = 0V V_S = 3V, $V_{\overline{SHDN}}$ = 0V	● ●		0.3 0.18	1.1 0.9	mA mA
GBW	Gain Bandwidth Product	Frequency = 2MHz	●		300		MHz
SR	Slew Rate	V_S = 5V, A_V = –1, R_L= 1k, V_O = $4V_{P-P}$	●		100		V/µs

ELECTRICAL CHARACTERISTICS

The ● denotes the specifications which apply over the – 40°C < T_A < 85°C temperature range. V_S = 5V, 0V; V_S = 3V, 0V; $V_{\overline{SHDN}}$ = open; V_{CM} = V_{OUT} = half supply unless otherwise noted. (Note 5)

SYMBOL	PARAMETER	CONDITIONS		MIN	TYP	MAX	UNITS
V_{OS}	Input Offset Voltage	$V_{CM} = V^+$ $V_{CM} = V^-$	● ●		200 200	800 800	µV µV
V_{OS} TC	Input Offset Voltage Drift (Note 8)	$V_{CM} = V^+$ $V_{CM} = V^-$	● ●		1.5 1.5	5 5	µV/°C µV/°C
ΔV_{OS}	Input Offset Voltage Shift	$V_{CM} = V^-$	●		100	800	µV
I_B	Input Bias Current	$V_{CM} = V^+ – 0.1V$ $V_{CM} = V^- + 0.2V$	● ●	 –16	1 –5	6 	µA µA
ΔI_B	Input Bias Current Shift	$V_{CM} = V^- + 0.2V$ to $V^+ – 0.1V$	●		6	22	µA
I_{OS}	Input Offset Current	$V_{CM} = V^+ – 0.1V$ $V_{CM} = V^- + 0.2V$	● ●		0.02 0.05	0.9 2.1	µA µA
ΔI_{OS}	Input Offset Current Shift	$V_{CM} = V^- + 0.2V$ to $V^+ – 0.1V$	●		0.07	3	µA
A_{VOL}	Large Signal Voltage Gain	V_S = 5V, V_O = 0.5V to 4.5V, R_L = 1kΩ V_S = 5V, V_O = 1V to 4V, R_L = 100Ω V_S = 3V, V_O = 0.5V to 2.5V, R_L = 1kΩ	● ● ●	60 7 50	140 16 100	 	V/mV V/mV V/mV
CMRR	Common Mode Rejection Ratio	V_S = 5V, $V_{CM} = V^-$ to V^+ V_S = 3V, $V_{CM} = V^-$ to V^+	● ●	75 71	94 89	 	dB dB
	Input Common Mode Range		●	V^-		V^+	V
PSRR	Power Supply Rejection Ratio	V_S = 2.5V to 10V, V_{CM} = 0V	●	87	105		dB
	Minimum Supply Voltage (Note 6)		●		2.3	2.5	V
V_{OL}	Output Voltage Swing LOW (Note 7)	No Load I_{SINK} = 5mA I_{SINK} = 20mA,	● ● ●		10 50 170	70 160 400	mV mV mV
V_{OH}	Output Voltage Swing HIGH (Note 7)	No Load I_{SOURCE} = 5mA I_{SOURCE} = 20mA	● ● ●		300 110 310	100 240 650	mV mV mV
I_{SC}	Short-Circuit Current	V_S = 5V V_S = 3V	● ●	±22 ±20	±45 ±40	 	mA mA
I_S	Supply Current		●		11	16	mA
	Disable Supply Current	V_S = 5V, $V_{\overline{SHDN}}$ = 0V V_S = 3V, $V_{\overline{SHDN}}$ = 0V	● ●		0.4 0.2	1.2 1	mA mA
GBW	Gain Bandwidth Product	Frequency = 2MHz	●		250		MHz
SR	Slew Rate	V_S= 5V, A_V = –1, R_L= 1k, V_O = $4V_{P-P}$	●		80		V/µV

ELECTRICAL CHARACTERISTICS

T_A = 25°C. V_S = ±5V, $V_{\overline{SHDN}}$ = open; V_{CM} = 0V, V_{OUT} = 0V unless otherwise noted.

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
V_{OS}	Input Offset Voltage	$V_{CM} = V^+$ $V_{CM} = V^-$		100 100	650 650	µV µV
ΔV_{OS}	Input Offset Voltage Shift	$V_{CM} = V^-$ to V^+		50	600	µV
I_B	Input Bias Current	$V_{CM} = V^+$ $V_{CM} = V^-$	 –14	1 –5	5	µA µA
ΔI_B	Input Bias Current Shift	$V_{CM} = V^-$ to V^+		6	19	µA
I_{OS}	Input Offset Current	$V_{CM} = V^+$ $V_{CM} = V^-$		0.03 0.04	0.7 1.6	µA µA
ΔI_{OS}	Input Offset Current Shift	$V_{CM} = V^-$ to V^+		0.07	2.3	µA
	Input Noise Voltage	0.1Hz to 10Hz		40		nV_{P-P}
e_n	Input Noise Voltage Density	f = 10kHz		3.5		$nV/\sqrt{Hz}$
i_n	Input Noise Current Density	f = 10kHz		5		$pA/\sqrt{Hz}$
C_{IN}	Input Capacitance	f = 100kHz		2		pF
A_{VOL}	Large Signal Voltage Gain	V_O = –4V to 4V, R_L = 1kΩ V_O = –2.5V to 2.5V, R_L = 100Ω	120 12	300 27		V/mV V/mV
CMRR	Common Mode Rejection Ratio	$V_{CM} = V^-$ to V^+	84	106		dB
	Input Common Mode Range		V^-		V^+	V
PSRR	Power Supply Rejection Ratio	V^+ = 2.5V to 10V, V_{CM} = 0V	91	105		dB
V_{OL}	Output Voltage Swing LOW (Note 7)	No Load I_{SINK} = 5mA I_{SINK} = 25mA		10 45 180	60 140 450	mV mV mV
V_{OH}	Output Voltage Swing HIGH (Note 7)	No Load I_{SOURCE} = 5mA I_{SOURCE} = 25mA		20 90 360	70 200 700	mV mV mV
I_{SC}	Short-Circuit Current		±40	±85		mA
I_S	Supply Current			11	16	mA
	Disable Supply Current	$V_{\overline{SHDN}}$ = 0V		0.4	1.2	mA
GBW	Gain Bandwidth Product	Frequency = 2MHz	180	325		MHz
SR	Slew Rate	A_V = –1, R_L = 1k, V_O = ±4V, Measure at V_O = ±2V	70	140		V/µs
THD	Total Harmonic Distortion	A_V = 1, R_L = 1k, V_O = $2V_{P-P}$, f_C = 5MHz		–80		dB
t_S	Settling Time	0.01%, V_{STEP} = 5V, A_V = 1, R_L = 1k		85		ns
ΔG	Differential Gain (NTSC)	A_V = 2, R_L = 150		0.01		%
Δθ	Differential Phase (NTSC)	A_V = 2, R_L = 150		0.01		Deg

ELECTRICAL CHARACTERISTICS

The ● denotes specifications which apply over the 0°C < T_A < 70°C temperature range. V_S = ±5V, $V_{\overline{SHDN}}$ = open; V_{CM} = 0V, V_{OUT} = 0V unless otherwise noted.

SYMBOL	PARAMETER	CONDITIONS		MIN	TYP	MAX	UNITS
V_{OS}	Input Offset Voltage	$V_{CM} = V^+$ $V_{CM} = V^-$	● ●		200 200	800 800	µV µV
V_{OS} TC	Input Offset Voltage Drift (Note 8)	$V_{CM} = V^+$ $V_{CM} = V^-$	● ●		1.5 1.5	5 5	µV/°C µV/°C
ΔV_{OS}	Input Offset Voltage Shift	$V_{CM} = V^-$ to V^+	●		100	800	µV
I_B	Input Bias Current	$V_{CM} = V^+ – 0.1V$ $V_{CM} = V^- + 0.2V$	● ●	 –15	1 –6	6 	µA µA
ΔI_B	Input Bias Current Shift	$V_{CM} = V^- + 0.2V$ to $V^+ – 0.1V$	●		7	21	µA
I_{OS}	Input Offset Current	$V_{CM} = V^+ – 0.1V$ $V_{CM} = V^- + 0.2V$	● ●		0.03 0.04	0.9 1.9	µA µA
ΔI_{OS}	Input Offset Current Shift	$V_{CM} = V^- + 0.2V$ to $V^+ – 0.1V$	●		0.07	2.8	µA
A_{VOL}	Large Signal Voltage Gain	V_O = –4V to 4V, R_L = 1kΩ V_O = –2.5V to 2.5V, RL = 100Ω	● ●	100 10	250 25	 	V/mV V/mV
CMRR	Common Mode Rejection Ratio	$V_{CM} = V^-$ to V^+	●	81	100		dB
	Input Common Mode Range		●	V^-		V^+	V
PSRR	Power Supply Rejection Ratio	V^+ = 2.5V to 10V, V_{CM} = 0V	●	89	105		dB
V_{OL}	Output Voltage Swing LOW (Note 7)	No Load I_{SINK} = 5mA I_{SINK} = 25mA	● ● ●		10 45 200	100 160 550	mV mV mV
V_{OH}	Output Voltage Swing HIGH (Note 7)	No Load I_{SOURCE} = 5mA I_{SOURCE} = 25mA	● ● ●		40 110 320	120 240 750	mV mV mV
I_{SC}	Short-Circuit Current		●	±35	±75		mA
I_S	Supply Current		●		14	20	mA
	Disable Supply Current	$V_{\overline{SHDN}}$ = 0V	●		0.4	1.4	mA
GBW	Gain Bandwidth Product	Frequency = 2MHz	●	150	300		MHz
SR	Slew Rate	A_V = –1, R_L = 1k, V_O = ±4V, Measure at V_O = ±2V	●	60	120		V/µs

ELECTRICAL CHARACTERISTICS

The ● denotes the specifications which apply over the –40°C < T_A < 85°C temperature range. V_S = ±5V, $V_{\overline{SHDN}}$ = open; V_{CM} = 0V, V_{OUT} = 0V unless otherwise noted. (Note 5)

SYMBOL	PARAMETER	CONDITIONS		MIN	TYP	MAX	UNITS
V_{OS}	Input Offset Voltage	$V_{CM} = V^+$ $V_{CM} = V^-$	● ●		200 200	900 900	µV µV
V_{OS} TC	Input Offset Voltage Drift (Note 8)	$V_{CM} = V^+$ $V_{CM} = V^-$	● ●		1.5 1.5	5 5	µV/°C µV/°C
ΔV_{OS}	Input Offset Voltage Shift	$V_{CM} = V^-$ to V^+	●		100	900	µV
I_B	Input Bias Current	$V_{CM} = V^+ – 0.1V$ $V_{CM} = V^- + 0.2V$	● ●	 –16	1.2 –5	7	µA µA
ΔI_B	Input Bias Current Shift	$V_{CM} = V^- + 0.2V$ to $V^+ – 0.1V$	●		6.2	23	µA
I_{OS}	Input Offset Current	$V_{CM} = V^+ – 0.1V$ $V_{CM} = V^- + 0.2V$	● ●		0.03 0.04	1 2.2	µA µA
ΔI_{OS}	Input Offset Current Shift	$V_{CM} = V^- + 0.2V$ to $V^+ – 0.1V$	●		0.07	3.2	µA
A_{VOL}	Large Signal Voltage Gain	V_O = –4V to 4V, R_L = 1kΩ V_O = –2V to 2V, RL =100Ω	● ●	80 8	175 17		V/mV V/mV
CMRR	Common Mode Rejection Ratio	$V_{CM} = V^-$ to V^+	●	80	100		dB
	Input Common Mode Range		●	V^-		V^+	V
PSRR	Power Supply Rejection Ratio	V^+ = 2.5V to 10V, V_{CM} = 0V	●	87	105		dB
V_{OL}	Output Voltage Swing LOW (Note 7)	No Load I_{SINK} = 5mA I_{SINK} = 20mA	● ● ●		20 60 200	120 170 500	mV mV mV
V_{OH}	Output Voltage Swing HIGH (Note 7)	No Load I_{SOURCE} = 5mA I_{SOURCE} = 20mA	● ● ●		50 115 360	140 260 700	mV mV mV
I_{SC}	Short-Circuit Current		●	±25	±55		mA
I_S	Supply Current		●		15	22	mA
	Disable Supply Current	$V_{\overline{SHDN}}$ = 0V	●		0.45	1.5	mA
GBW	Gain Bandwidth Product	Frequency = 2MHz	●	125	250		MHz
SR	Slew Rate	A_V = –1, R_L = 1k, V_O = ±4V, Measure at V_O = ±2V	●	50	100		V/µs

Note 1: Absolute maximum ratings are those values beyond which the life of the device may be impaired.

Note 2: The inputs are protected by back-to-back diodes. If the differential input voltage exceeds 1.4V, the input current should be limited to less than 10mA.

Note 3: A heat sink may be required to keep the junction temperature below the absolute maximum rating when the output is shorted indefinitely.

Note 4: The LT1806C/LT1806I are guaranteed functional over the temperature range of –40°C and 85°C.

Note 5: The LT1806C is guaranteed to meet specified performance from 0°C to 70°C. The LT1806C is designed, characterized and expected to meet specified performance from –40°C to 85°C but is not tested or QA sampled at these temperatures. The LT1806I is guaranteed to meet specified performance from –40°C to 85°C.

Note 6: Minimum supply voltage is guaranteed by power supply rejection ratio test.

Note 7: Output Voltage swings are measured between the output and power supply rails.

Note 8: This parameter is not 100% tested.

Note 9: Thermal resistance varies depending upon the amount of PC board metal attached to Pin 4 of the device. θ_{JA} is specified for a 2500mm^2 test board covered with 2 oz copper on both sides.

APPLICATIONS INFORMATION

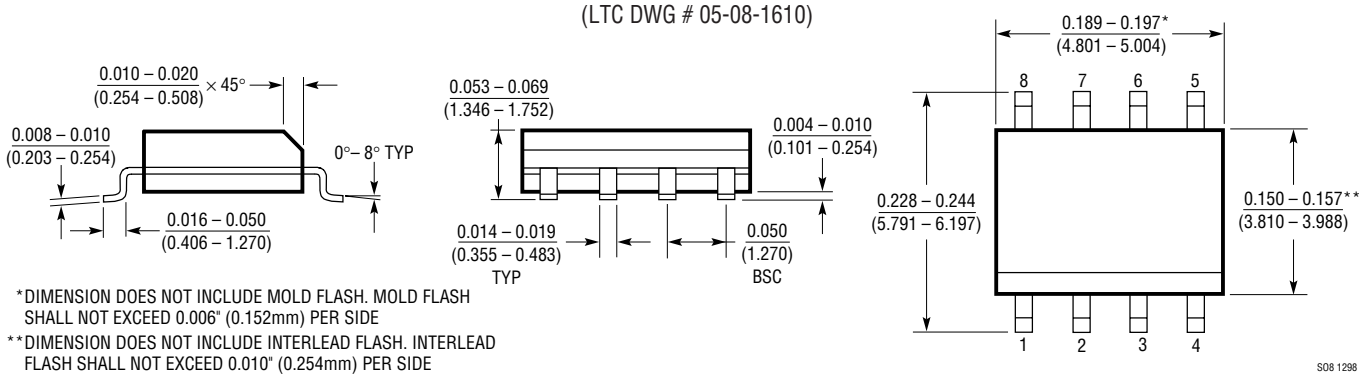
Single Supply Video Line Driver

The LT1806 is a wideband rail-to-rail op amp with high output current that allows it to drive video signals in low supply applications. The figure on the front page depicts a single supply video line driver with AC coupling to minimize the quiescent power dissipation. Resistors R1 and R2 are used to level-shift the input and output to provide the largest signal swing. The gain of two is set up with R3 and R4 to restore the signal at V_{OUT} which is attenuated by 6dB due to the matching of the 75Ω line with the back-terminated resistor, R5. The back termination will eliminate any reflection of the signal that comes from the load. The input termination resistor, R_T, is optional—it is used only if matching of the incoming line is necessary. The values of C1, C2 and C3 are selected to minimize the droop of the luminance signal. In some less stringent requirements, the value of capacitors could be reduced.

PACKAGE DESCRIPTION

Dimensions in inches (millimeters) unless otherwise noted.

S8 Package
8-Lead Plastic Small Outline (Narrow 0.150)
(LTC DWG # 05-08-1610)



*DIMENSION DOES NOT INCLUDE MOLD FLASH. MOLD FLASH SHALL NOT EXCEED 0.006" (0.152mm) PER SIDE

**DIMENSION DOES NOT INCLUDE INTERLEAD FLASH. INTERLEAD FLASH SHALL NOT EXCEED 0.010" (0.254mm) PER SIDE

RELATED PARTS

PART NUMBER	DESCRIPTION	COMMENTS
LT1395	400MHz Current Feedback Amplifier	800V/µs Slew Rate, Shutdown
LT1399	Triple 300MHz Current Feedback Amplifier	0.1dB Gain Flatness to 150MHz, Shutdown
LT1809	180MHz Rail-to-Rail Amplifier	350V/µs Slew Rate, Shutdown

Linear Technology Corporation
1630 McCarthy Blvd., Milpitas, CA 95035-7417
(408)432-1900 • FAX: (408) 434-0507 • www.linear-tech.com

1806i LT/LCG 0600 4K • PRINTED IN USA